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PCI-6602

CALIBRATION PROCEDURE

NI 6601/6602

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This document contains information and instructions for calibrating the National Instruments 6601/6602 data acquisition devices.

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Introduction

What Is Calibration?

Calibration consists of verifying the measurement accuracy of a device and adjusting for any measurement error. For the NI 6601/6602, calibration is simply verifying the measurement accuracy of the crystal oscillator on the device because the crystal oscillator is not user-adjustable. *Verification* is measuring the performance of a device and comparing these measurements to the factory specifications. If the device fails after calibration, return it to NI for repair or replacement. For more information about how to contact NI for repair or replacement, refer to the *Technical Support Information* document.

Why Should You Verify?

The accuracy of timing components drifts with time and temperature, which can affect measurement accuracy. Verification determines whether the device still meets NI standards.

How Often Should You Verify?

Calibrate the NI 6601/6602 at a regular interval as defined by the measurement accuracy requirements of your application. NI recommends that you perform a complete calibration at least once every year. You can shorten this interval based on the accuracy requirements of your application.

Software and Documentation

You will need the following software and documentation to verify the NI 6601/6602. You can find these resources on the NI Web site at ni.com:

- Traditional NI-DAQ (Legacy) 7.4.4
- *Traditional NI-DAQ (Legacy) C Function Reference Help* file
- *Traditional NI-DAQ User Manual* document

Software

This calibration procedure requires using the Traditional NI-DAQ (Legacy) 7.4.4. Traditional NI-DAQ supports a number of application development environments (ADEs) and programming languages, including LabVIEW, LabWindows™/CVI™, Microsoft Visual C++, Microsoft Visual Basic, and Borland C++. When you install the driver, you only need to install support for the ADE or programming language you will use.

Documentation

The *Traditional NI-DAQ (Legacy) C Function Reference Help* file and the *Traditional NI-DAQ User Manual* document contain detailed information about using the Traditional NI-DAQ driver. The help file includes detailed information about the driver functions. You can access the help file by selecting **Start»All Programs»National Instruments»NI-DAQ**. The *Traditional NI-DAQ User Manual* document provides instructions on installing Traditional NI-DAQ. This manual also includes detailed information about creating applications that use the Traditional NI-DAQ driver. These are your primary references for writing your calibration utility. You can also refer to the documentation for the programming language you are using. For further information about the products you are calibrating, refer to the *NI 660x User Manual*.

Test Equipment

Verifying the NI 6601/6602 devices requires the following equipment:

- A highly stable 10 MHz, 5 MHz, or 1 MHz clock source that is accurate to within 1 ppm. NI recommends using the PXI-6608 to provide the stable clock. The PXI-6608 provides a 10 MHz clock that is accurate to 0.075 ppm over 1 year.
- An SH68-68-D1 cable or a standard 68-pin cable connector
- A 68-pin breakout box (if you are using a clock source other than the PXI-6608)

Test Conditions

Follow these guidelines to optimize the equipment and the environment during calibration:

- Keep connections to the device as short as possible. Long cables and wires act as antennae by picking up extra noise that can affect measurements.
- Maintain an ambient temperature of approximately 25 °C.
- Keep relative humidity below 80%.
- Use shielded copper wire for all cable connections to the device. Use twisted-pair wire to eliminate noise and thermal offsets.
- Allow a warm-up time of at least 15 minutes to ensure the measurement circuitry is at a stable operating temperature.

Calibration Process

This section contains instructions for verifying the operation of the crystal oscillator on the NI 6601/6602 devices using a stable clock source. Verifying the performance of the oscillator on an NI 6601/6602 device requires two counters on the NI 6601/6602 device—counters 0 and 1. Counter 1 uses a stable clock as the source to generate a 50 ms single pulse, which is applied to the GATE of counter 0. The 20 MHz timebase on the NI 6601/6602 device, which is derived from the crystal oscillator, is the SOURCE of counter 0. You will count the number of clock edges of the 20 MHz timebase that occur during the 50 ms single pulse and calculate the frequency of the oscillator to determine if the device is within its specifications. Figure 1 shows a simple diagram of the process.

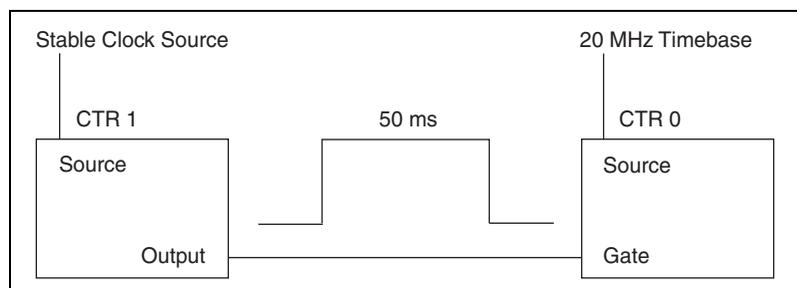


Figure 1. NI 6601/6602 Verification Process

The frequency measurement will have an uncertainty that is equal to the sum of the stable clock error and the quantization error caused by the counter. The clock error should be 1 ppm or less, as specified in the [Test Equipment](#) section of this document. The quantization error, which is the inherent uncertainty in digitizing an analog value, will always be up to 50 ns. Using a measurement duration of 50 ms gives a quantization error as follows:

$$\frac{50 \text{ ns}}{50 \text{ ms}} = 1 \text{ ppm}$$

The sum of the stable clock error, 1 ppm, and the quantization error, 1 ppm, gives a total measurement uncertainty of 2 ppm. You can reduce measurement uncertainty by using a more stable clock, by increasing the measurement duration, or both. The calculations presented at the end of this document allow for a 2 ppm measurement uncertainty.

Both Traditional NI-DAQ and LabVIEW programming instructions are presented in the following sections. Choose the appropriate instructions for your programming environment.

Setting Up the Stable Clock

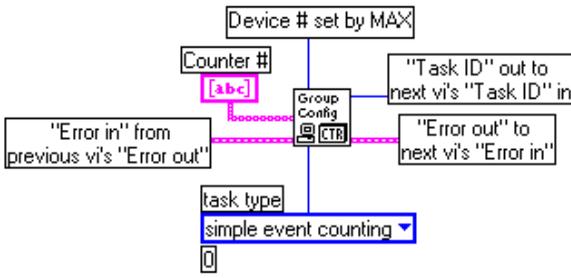
This section describes how to set up the clock. NI recommends using the PXI-6608 to provide the stable clock. You can use an alternative stable clock if it meets the specifications described in the [Test Equipment](#) section of this document. Instructions for both clock alternatives are included in the following two sections, [Setting Up a PXI-6608 Clock Source](#) and [Setting Up an Alternative Clock Source](#).

Setting Up a PXI-6608 Clock Source

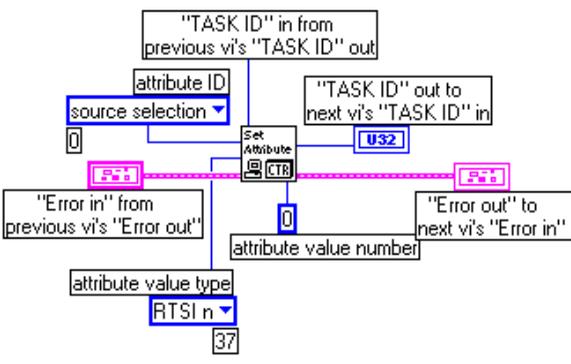
To set up a PXI-6608 as the stable clock source, complete the following steps:

1. Power on the 6608 device.
2. Wait 15 minutes to allow the measurement circuitry in the device to warm up.

3. Route the 10 MHz stable clock of the PXI-6608 onto PFI_35.
 - a. Configure counter 1 on the PXI-6608 to perform simple event counting.

Traditional NI-DAQ Function Call	LabVIEW Block Diagram
<p>Call GPCTR_Set_Application with the following parameters:</p> <p>deviceNumber: The value set by Measurement & Automation Explorer (MAX)</p> <p>gpctrNum: ND_COUNTER_1</p> <p>application: ND_SIMPLE_EVENT_CNT</p>	 <p>The LabVIEW block diagram shows the GPCTR_Set_Application block. The 'Device #' input is set to 'Device # set by MAX'. The 'Counter #' input is set to '1'. The 'task type' dropdown is set to 'simple event counting'. The 'Error in' input is connected to a terminal labeled '"Error in" from previous vi's "Error out"'. The 'Error out' output is connected to a terminal labeled '"Error out" to next vi's "Error in"'. The 'Task ID' output is connected to a terminal labeled '"Task ID" out to next vi's "Task ID" in'.</p>

- b. Configure the **source** of counter 1 as RTSI line 0.

Traditional NI-DAQ Function Call	LabVIEW Block Diagram
<p>Call GPCTR_Change_Parameter with the following parameters:</p> <p>deviceNumber: The value set by MAX</p> <p>gpctrNum: ND_COUNTER_1</p> <p>paramID: ND_SOURCE</p> <p>paramValue: ND_RTSI_0</p>	 <p>The LabVIEW block diagram shows the GPCTR_Change_Parameter block. The 'attribute ID' dropdown is set to 'source selection'. The 'attribute value number' input is set to '0'. The 'attribute value type' dropdown is set to 'RTSI n'. The 'Error in' input is connected to a terminal labeled '"Error in" from previous vi's "Error out"'. The 'Error out' output is connected to a terminal labeled '"Error out" to next vi's "Error in"'. The 'Task ID' output is connected to a terminal labeled '"TASK ID" out to next vi's "TASK ID" in'.</p>

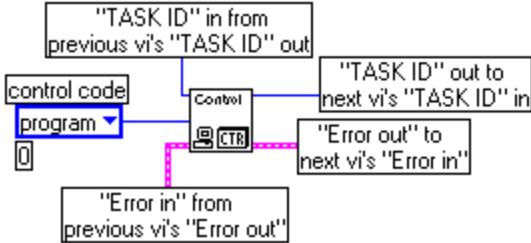
c. Route the stable 10 MHz clock on the PXI-6608 onto RTSI line 0.

Traditional NI-DAQ Function Call	LabVIEW Block Diagram
<p>Call <code>Select_Signal</code> with the following parameters:</p> <p>deviceNumber: The value set by MAX</p> <p>signal: ND_RTSI_0</p> <p>source: ND_STABLE_10_MHZ</p> <p>sourceSpec: ND_DONT_CARE</p>	

d. Configure the **source** of counter 1 to be available on PFI line 35.

Traditional NI-DAQ Function Call	LabVIEW Block Diagram
<p>Call <code>Select_Signal</code> with the following parameters:</p> <p>deviceNumber: The value set by MAX</p> <p>signal: ND_PFI_35</p> <p>source: ND_GPCTR1_SOURCE</p> <p>sourceSpec: ND_DONT_CARE</p>	

- e. Program counter 1 to arm it.

Traditional NI-DAQ Function Call	LabVIEW Block Diagram
<p>Call GPCTR_Control with the following parameters:</p> <p>deviceNumber: The value set by MAX</p> <p>gpctrNum: ND_COUNTER_1</p> <p>action: ND_PROGRAM</p>	 <p>“TASK ID” out connects to “TASK ID” in of step 14 in the Measuring the Frequency of the NI 6601/6602 Device section.</p>

Setting Up an Alternative Clock Source

To set up a clock source other than a PXI-6608, complete the following steps:

1. Ensure that the clock source meets the requirements listed in the [Test Equipment](#) section.
2. Power the clock device.



Note After you complete the clock setup procedure, the clock is running and generating pulses.

Connecting the Clock Source to the NI 6601/6602 Device

This section describes how to connect the clock source to the NI 6601/6602 device, whether you are using a PXI-6608 or another clock source.

Connecting a PXI-6608 to the NI 6601/6602 Device

Connect the PXI-6608 clock source to the NI 6601/6602 device using an SH68-68-D1 cable, which connects to the I/O connector of each device.

Connecting an Alternative Clock Source to the NI 6601/6602 Device

To connect an alternative clock source to the NI 6601/6602 device, complete the following steps:

1. Connect the NI 6601/6602 device to a 68-pin breakout box using the SH68-68-D1 cable.
2. Connect the clock source to pins 7 and 41 of the 68-pin breakout box using twisted-pair wires.
 - Connect the clock to pin 7. Pin 7 corresponds to PFI_35, which is the SOURCE of counter 1 on all NI 6601/6602 devices.
 - Connect the ground of the clock to pin 41, which is digital ground.

Measuring the Frequency of the NI 6601/6602 Device



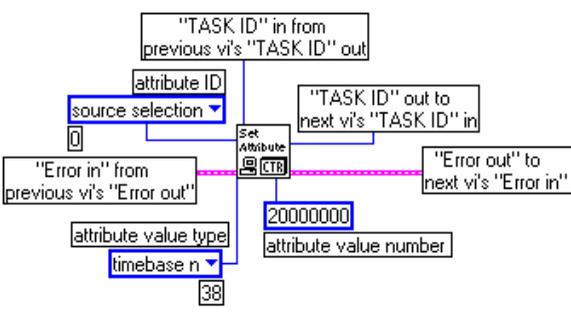
Note If you are verifying the PXI-6602 on a PXI chassis, the measured frequency is that of the PXI backplane clock instead of the onboard oscillator. To verify the onboard oscillator, you must calibrate the PXI-6602 on a PCI chassis using a CompactPCI-to-PCI adapter.

To measure the frequency of the NI 6601/6602 device for verification, complete the following steps:

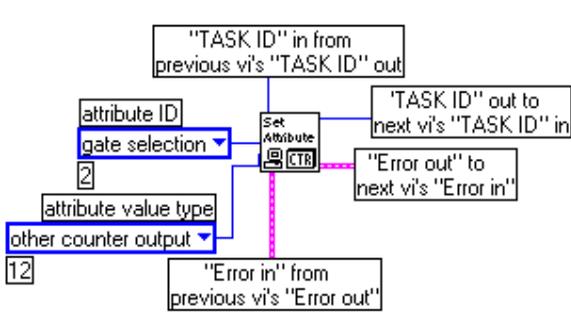
1. Configure counter 0 to perform simple event counting.

Traditional NI-DAQ Function Call	LabVIEW Block Diagram
<p>Call GPCTR_Set_Application with the following parameters:</p> <p>deviceNumber: The value set by MAX</p> <p>gpctrNum: ND_COUNTER_0</p> <p>application: ND_SIMPLE_EVENT_CNT</p>	

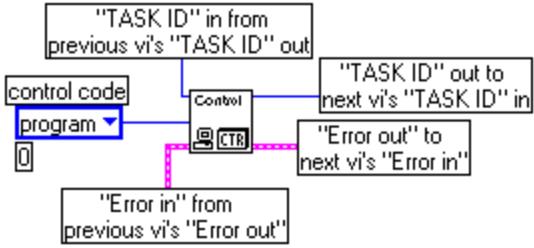
- Set the **source** of counter 0 to be the 20 MHz timebase.

Traditional NI-DAQ Function Call	LabVIEW Block Diagram
<p>Call GPCTR_Change_Parameter with the following parameters:</p> <p>deviceNumber: The value set by MAX</p> <p>gpctrNum: ND_COUNTER_0</p> <p>paramID: ND_SOURCE</p> <p>paramValue: ND_INTERNAL_20_MHZ</p>	 <p>The diagram shows a 'Set Attribute' block with 'CTR' selected. The 'attribute ID' is 0, and the 'attribute value type' is 'timebase n' with a value of 38. The 'attribute value number' is 20000000. The 'source selection' dropdown is set to 0. Error handling is shown with 'Error in' and 'Error out' connections.</p>

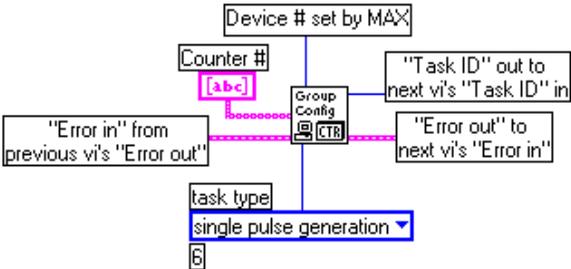
- Configure the **gate** of counter 0 to be the **output** of counter 1.

Traditional NI-DAQ Function Call	LabVIEW Block Diagram
<p>Call GPCTR_Change_Parameter with the following parameters:</p> <p>deviceNumber: The value set by MAX</p> <p>gpctrNum: ND_COUNTER_0</p> <p>paramID: ND_GATE</p> <p>paramValue: ND_OTHER_GPCTR_OUTPUT</p>	 <p>The diagram shows a 'Set Attribute' block with 'CTR' selected. The 'attribute ID' is 2, and the 'attribute value type' is 'other counter output' with a value of 12. The 'gate selection' dropdown is set to 2. Error handling is shown with 'Error in' and 'Error out' connections.</p>

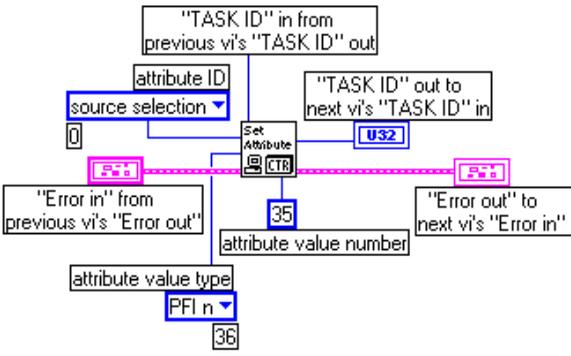
4. Arm counter 0 to perform simple event counting.

Traditional NI-DAQ Function Call	LabVIEW Block Diagram
<p>Call GPCTR_Control with the following parameters:</p> <p>deviceNumber: The value set by MAX</p> <p>gpctrNum: ND_COUNTER_0</p> <p>action: ND_PROGRAM</p>	 <p>The diagram shows a 'Control' block with a 'CTB' icon. It has four inputs: 'control code' (a dropdown menu set to 'program'), 'Error in' from the previous vi's 'Error out', 'TASK ID' in from the previous vi's 'TASK ID' out, and 'Error out' to the next vi's 'Error in'. The 'TASK ID' out connects to the 'TASK ID' in of step 11.</p>

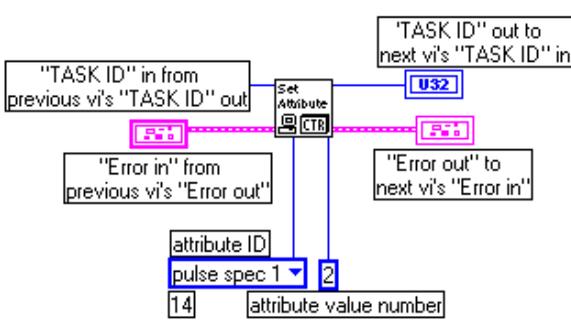
5. Configure counter 1 to perform single-pulse generation.

Traditional NI-DAQ Function Call	LabVIEW Block Diagram
<p>Call GPCTR_Set_Application with the following parameters:</p> <p>deviceNumber: The value set by MAX</p> <p>gpctrNum: ND_COUNTER_1</p> <p>application: ND_SINGLE_PULSE_GNR</p>	 <p>The diagram shows a 'Group Config' block with a 'CTB' icon. It has four inputs: 'Device # set by MAX', 'Counter #' (a text box containing '[abc]'), 'Error in' from the previous vi's 'Error out', and 'task type' (a dropdown menu set to 'single pulse generation'). The 'Task ID' out connects to the 'Task ID' in of the next vi, and the 'Error out' connects to the 'Error in' of the next vi.</p>

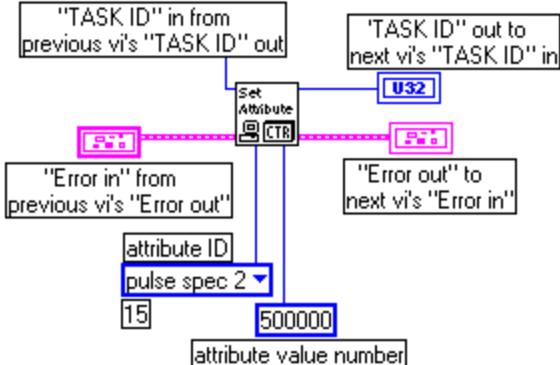
6. Configure **source** of counter 1 to be PFI line 35.

Traditional NI-DAQ Function Call	LabVIEW Block Diagram
<p>Call GPCTR_Change_Parameter with the following parameters:</p> <p>deviceNumber: The value set by MAX</p> <p>gpctrNum: ND_COUNTER_1</p> <p>paramID: ND_SOURCE</p> <p>paramValue: ND_PFI_35</p>	 <p>The LabVIEW block diagram shows a 'Set Attribute' block with 'CTR' selected. The 'attribute ID' is set to 'source selection' with a dropdown menu showing '0'. The 'attribute value number' is set to '35'. The 'attribute value type' is set to 'PFI n' with a dropdown menu showing '36'. The block is connected to 'TASK ID' and 'Error' lines between two VI icons. A 'U32' block is connected to the 'TASK ID' output line.</p>

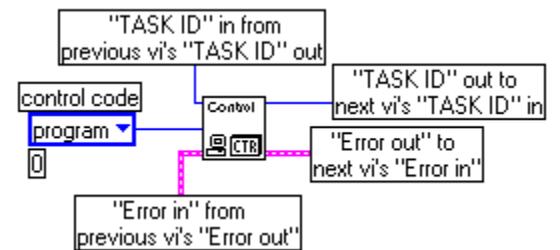
7. Set the pulse delay for the single-pulse generation.

Traditional NI-DAQ Function Call	LabVIEW Block Diagram
<p>Call GPCTR_Change_Parameter with the following parameters:</p> <p>deviceNumber: The value set by MAX</p> <p>gpctrNum: ND_COUNTER_1</p> <p>paramID: ND_COUNT_1</p> <p>paramValue: 2</p>	 <p>The LabVIEW block diagram shows a 'Set Attribute' block with 'CTR' selected. The 'attribute ID' is set to 'pulse spec 1' with a dropdown menu showing '14'. The 'attribute value number' is set to '2'. The block is connected to 'TASK ID' and 'Error' lines between two VI icons. A 'U32' block is connected to the 'TASK ID' output line.</p>

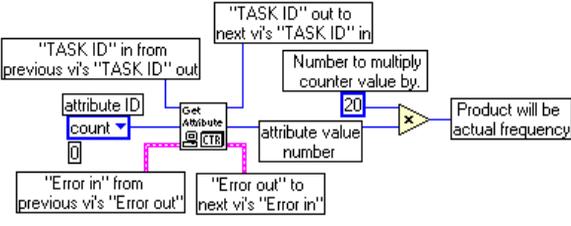
8. Set the pulse width for the single-pulse generation. The following example assumes a 10 MHz source (period = 100 ns) for counter 1. Therefore, the pulse width is $100 \text{ ns} \times 500000 = 50 \text{ ms}$. If you use a frequency other than 10 MHz, adjust the number accordingly. For a frequency F , $1/F \times \text{ND_COUNT_2}$ should equal 50 ms.

Traditional NI-DAQ Function Call	LabVIEW Block Diagram
<p>Call GPCTR_Change_Parameter with the following parameters:</p> <p>deviceNumber: The value set by MAX</p> <p>gpctrNum: ND_COUNTER_1</p> <p>paramID: ND_COUNT_2</p> <p>paramValue: 500000</p>	 <p>The LabVIEW block diagram shows the 'Set Attribute' block with the following connections: <ul style="list-style-type: none"> 'TASK ID' in from previous vi's 'TASK ID' out' (input) 'TASK ID' out to next vi's 'TASK ID' in' (output) 'Error in' from previous vi's 'Error out' (input) 'Error out' to next vi's 'Error in' (output) 'attribute ID' dropdown set to 'pulse spec 2' 'attribute value number' numeric control set to 500000 'U32' numeric control set to 15 'U32' numeric control set to 500000 </p>

9. Program counter 1 to perform single-pulse generation.

Traditional NI-DAQ Function Call	LabVIEW Block Diagram
<p>Call GPCTR_Control with the following parameters:</p> <p>deviceNumber: The value set by MAX</p> <p>gpctrNum: ND_COUNTER_1</p> <p>action: ND_PROGRAM</p>	 <p>The LabVIEW block diagram shows the 'Control' block with the following connections: <ul style="list-style-type: none"> 'TASK ID' in from previous vi's 'TASK ID' out' (input) 'TASK ID' out to next vi's 'TASK ID' in' (output) 'Error in' from previous vi's 'Error out' (input) 'Error out' to next vi's 'Error in' (output) 'control code' dropdown set to 'program' 'control code' numeric control set to 0 </p>

11. Calculate the frequency of the device.
 - a. Retrieve the count from counter 0. The count represents the number of cycles of the 20 MHz timebase in 50 ms.
 - b. Multiply the count returned above by 20 to calculate the number of pulses that should occur in one second, ($50 \text{ ms} \times 20 = 1 \text{ s}$), which is the *measured frequency* of the 20 MHz timebase.

Traditional NI-DAQ Function Call	LabVIEW Block Diagram
<p>Call GPCTR_Watch with the following parameters:</p> <p>deviceNumber: The value set by MAX</p> <p>gpctrNum: ND_COUNTER_0</p> <p>entityID: ND_COUNT</p> <p>entityValue: A user-declared variable</p> <p>See example code in the next paragraph.</p>	 <p>“TASK ID” in connects to “TASK ID” out of step 4.</p>

Example Code for GPCTR Watch

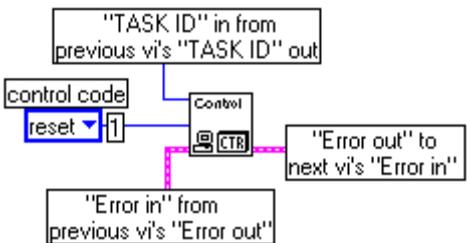
```

u32 ulCount = 0;

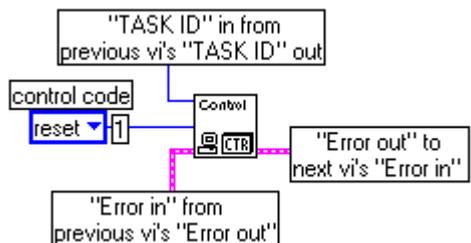
iStatus = GPCTR_WATCH (deviceNumber, ND_COUNTER_0, ND_COUNT, &ulCount);
ulCount = ulCount * 20;

```

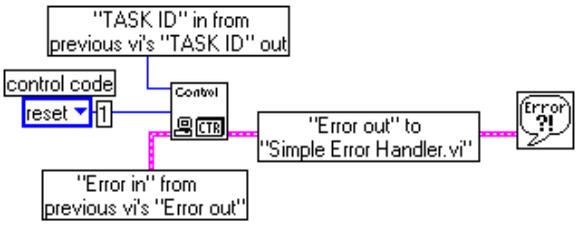
12. Reset counter 0 to its original state.

Traditional NI-DAQ Function Call	LabVIEW Block Diagram
<p>Call GPCTR_Control with the following parameters:</p> <p>deviceNumber: The value set by MAX</p> <p>gpctrNum: ND_COUNTER_0</p> <p>action: ND_RESET</p>	

13. Reset counter 1 to its original state.

Traditional NI-DAQ Function Call	LabVIEW Block Diagram
<p>Call GPCTR_Control with the following parameters:</p> <p>deviceNumber: The value set by MAX</p> <p>gpctrNum: ND_COUNTER_1</p> <p>action: ND_RESET</p>	 <p>“TASK ID” in connects to “TASK ID” out of step 10.</p>

14. Reset counter 1 on the PXI-6608 to its original state, if necessary.

Traditional NI-DAQ Function Call	LabVIEW Block Diagram
<p>Call <code>GPCTR_Control</code> with the following parameters:</p> <p>deviceNumber: The value set by MAX</p> <p>gpctrNum: <code>ND_COUNTER_1</code></p> <p>action: <code>ND_RESET</code></p>	 <p>“TASK ID” in connects to “TASK ID” out of step 3e in the <i>Setting Up the Stable Clock</i> section.</p>

You have now completed measuring the frequency of the crystal oscillator on the NI 6601/6602 device.

Comparing the Measured Frequency to the NI 6601/6602 Device Specifications

Compare the calculated frequency from step 11 in the previous section with the following values:

- If the frequency you measure falls between the upper limit of 20,001,500 Hz and the lower limit of 19,998,500 Hz, the device is operating within its specifications.
- If the frequency you measure is above 20,001,500 Hz, the device is nonfunctional. Return the device to NI for repair or replacement.
- If the frequency you measure is below 19,998,500 Hz, the device is nonfunctional. Return the device to NI for repair or replacement.

You have completed verifying the operation of the NI 6601/6602 device.

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