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**PXIe-6612**

**Board Assembly Part Number(s)**

Part Number	Description
153852A-01L	NI PCIe-6612
153849B-01L	NI PXLe-6612

**Manufacturer:** National Instruments

**Volatile Memory**

Type	Size	User Accessible/ System Accessible	Battery Backup?	Purpose	Method of Clearing
Counter Input FIFO <sup>1</sup>	256 x 32-bit	No/No	No	Data Processing	Cycle power
Counter Output FIFO <sup>2</sup>	256 x 32-bit	Yes/Yes	No	Data Processing	Cycle power
Digital Input FIFO	256 x 32-bit	No/No	No	Data Processing	Cycle power
Digital Output FIFO	2k x 32-bit	Yes/Yes	No	Data Processing	Cycle power

**Non-Volatile Memory**

Type	Size	User Accessible/ System Accessible	Battery Backup?	Purpose	Method of Clearing
EEPROM	2 KB	No <sup>3</sup> /No	No	Device Configuration	None available to user

**Media Storage**

Type	Size	User Accessible/ System Accessible	Battery Backup?	Purpose	Method of Clearing
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NONE

<sup>1</sup> NI PCIe/PXLe-6612 contains 8 Counter Input FIFO.

<sup>2</sup> NI PCIe/PXLe-6612 contains 8 Counter Output FIFO.

<sup>3</sup> The device configuration area is not accessible from any API.

**Terms and Definitions**

**User Accessible** Allows the user to directly write or modify the contents of the memory during normal instrument operation.

**System Accessible** Does not allow the user to access or modify the memory during normal instrument operation. However, system accessible memory may be accessed or modified by background processes. This can be something that is not deliberate by the user and can be a background driver implementation, such as storing application information in RAM to increase speed of use.

**Cycle Power** The process of completely removing power from the device and its components. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

**Volatile Memory** Requires power to maintain the stored information. When power is removed from this memory, its contents are lost.

**Non-Volatile** Retains its contents when power is removed. This type of memory typically contains calibration or chip configuration information, such as power up states.