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***GPIB-1014D***

# NOTE TO LABVIEW™ 5.0.1f1 POWERMAX USERS

This kit contains LabVIEW 5.0.1f1 for Concurrent PowerMAX. This document expands on and updates some information given in the LabVIEW 5.0 Upgrade Notes and Release Notes.

## Operating System and Patches

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LabVIEW 5.0.1f1 requires version 4.2 or later of the PowerMAX operating system. In addition, you must install the following patches, available from Concurrent, so that basic LabVIEW networking functions correctly:

- inet-005
- One of the following, dependent on your system:
  - base-007 for Power Hawk 610, Power Hawk 620, and all single-processor PowerStack systems
  - base-008 for Power Hawk 640 and multi-processor PowerStack II systems
  - base-009 for Night Hawk systems

## Permissions

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LabVIEW 5.0.1f1 for Concurrent always binds threads to lightweight processes (LWPs). Unless you have permission to adjust priorities to LWPs, LabVIEW will stop, indicating that it does not have enough permission to run. Please notice that this updates the information in the Version 5.0 Release Notes concerning how LabVIEW responds if it cannot increase its LWP priorities from the default.

The permissions you need depends on the scheduler you are using. If you are using the time-shared scheduler—the default—you need the `P_TSHAR` privilege. If you are able to use the real-time scheduler, you do not need any additional privileges.

# Known Issues

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Two known issues can affect the correct behavior of LabVIEW 5.0.1f1. Concurrent may release patches to resolve the following issues:

- The LabVIEW function Date/Time To Seconds may give an incorrect result due to a problem in the C library function `mktime()`. The Concurrent Software Action Request (SAR) is HM12465.
- Some bytes may be lost if the PowerMAX kernel runs out of memory when performing a TCP write. The Concurrent SAR is HM12466.

# Restrictions

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Please notice the following model-specific restrictions:

- Night Hawk systems support only 8-bit serial I/O.
- Early revisions of the processor boards for Night Hawk systems prevent LabVIEW from being run in global memory. This means that LabVIEW cannot take advantage of all the processors in the system.
- Some Concurrent models do not support serial I/O hardware flow control (RTS/CTS). If you are not sure whether your model supports RTS/CTS, contact Concurrent.



**Note**

***The NI-488 driver for GPIB-1014 boards no longer ships with LabVIEW. This driver last shipped with LabVIEW 4.1.***



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