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#### SCXI-1350 Multichassis Adapter Installation Guide

Part Number 371179A-01

This guide describes how to connect a multichassis SCXI system using the SCXI-1350 multichassis adapter. In addition to the SCXI-1350 kits, you will need SCXI chassis, SCXI modules, a computer, a data acquisition board, cables, and a small flathead screwdriver.

# Introduction

With the SCXI-1350 multichassis adapter and ribbon cables, you can connect a multichassis SCXI system to your data acquisition board. The following table shows which SCXI modules and data acquisition boards can be connected with the SCXI-1350 multichassis adapter.

	Data Aquisition Board												
Module	AT-MIO-16	AT-MIO-16D	AT-MIO-16F-5	AT-MIO-16X	AT-MIO-64F-5	MC-DIO-24	MC-MIO-16	NB-DIO-24	NB-DIO-96	NB-MIO-16	NB-MIO-16X	PC-DIO-24	PC-DIO-96
SCXI-1100		$\checkmark$	$\checkmark$		$\checkmark$		$\checkmark$			$\checkmark$	$\checkmark$		
SCXI-1120		$\checkmark$	$\checkmark$								$\checkmark$		
SCXI-1121	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$					$\checkmark$	$\checkmark$		
SCXI-1140	$\checkmark$	$\checkmark$	$\checkmark$		$\checkmark$					$\checkmark$	$\checkmark$		
SCXI-1160		$\checkmark$	$\checkmark$		$\checkmark$								
SCXI-1161	$\checkmark$	$\checkmark$		$\checkmark$	$\checkmark$	$\checkmark$		$\checkmark$	$\checkmark$		$\checkmark$	$\checkmark$	$\checkmark$
SCXI-1162	$\checkmark$	$\checkmark$	$\checkmark$					$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$
SCXI-1163		$\checkmark$	$\checkmark$					$\checkmark$	V	$\checkmark$	$\checkmark$	V	$\checkmark$

You can connect the SCXI-1181 breadboard module and the SCXI-1180 feedthrough panel to any data acquisition board with a 50-pin or 100-pin ribbon cable.

The SCXI-1350 multichassis adapter consists of a male rear connector, a female module connector, and a male chassis extender connector. You use a ribbon cable to connect the rear connector to the data acquisition board or a preceding chassis. The module connector plugs into the rear signal connector of an SCXI module. Another ribbon cable connects the chassis extender connector to the multichassis adapter in the next chassis.

# What Your Kit Should Contain

The contents of the SCXI-1350 multichassis adapter kit are as follows:

Kit Name and Part Number	Component	Part Number		
SCXI-1350 (776575-50)	Multichassis adapter Two small screws SCXI-1350 Cable Assembly Installation Guide	181750-01 742413-01 320615-01		

If your kit is missing any of the components, contact National Instruments.

# **Installation Procedure**

To cable a multichassis SCXI system to a data acquisition board, you need a 0.5 m NB1 ribbon cable between each SCXI chassis, and a 0.5 m or 1 m ribbon cable to connect the data acquisition board to the first chassis. The following table shows which cable you can use with which data acquisiton board:

Data Acquisition Board	Cable
AT-MIO-16	NB1
AT-MIO-16D	NB5
AT-MIO-16F-5	NB1
AT MIO-16X	NB1
AT-MIO-10A	NB1
AT-MIO-64F-5	NB5
MC-DIO-24	NB1
MC-MIO-16	NB1
NB-DIO-24	NB1
NB-DIO-96	NB6
NB-MIO-16	NB1
NB-MIO-16X	NB1
PC-DIO-24	NB1
PC-DIO-96	NB5

For example, to connect a three-chassis system with an AT-MIO-16 data acquisiton board, you need three NB1 cables and three SCXI-1350 multichassis adapters. The finished installation of this system would look like the following figure.



Completed Cable Installation

Perform the following steps to install the SCXI-1350 cable:

- 1. Turn off the power to your computer and the SCXI chassis.
- 2. Install your SCXI modules in the SCXI chassis according to the instructions in your module user manual.

Note: You cannot use SCXI-1000 chassis in a multichassis system.

- 3. Install your data acquisition board in a slot in your computer according to the instructions in your data acquisition board user manual.
- 4. If you are connecting an NB5 or NB6 cable, connect the 100-pin connector of the cable to the I/O connector of your data acquisition board. If you are connecting an NB1 cable, connect one end of the cable to the I/O connector of your data acquisition board.
- 5. Plug the unattached end of the cable or the 50-pin connector that contains positions 1 through 50 of the NB5 or NB6 cable into the connector with the latches at the rear of the SCXI-1350 multichassis adapter, as shown in the previous illustration.
- 6. Connect another NB1 ribbon cable to the chassis extender connector that is in the middle of the SCXI-1350.

3

- 7. Plug the SCXI-1350 into the back of one of the SCXI modules in the first chassis so that the connector at the rear of the module mates with the front connector on the SCXI-1350 multichassis adapter.
- 8. Screw the rear panel to the threaded strips in the rear of the chassis.
- 9. Connect the other end of the NB1 cable to the rear connector of the next SCXI-1350 multichassis adapter.
- 10. Repeat steps 6 through 9 until you have connected all the chassis. You do not need to connect anything to the middle connector of the SCXI-1350 multichassis adapter in the last chassis.

### **SCXI-1350** Multichassis Pin Translations

The following table lists the pin connections that the SCXI-1350 multichassis adapter makes.

**Notes:** When you are cabling a system that has an MIO data acquisition board, the multichassis adapters connect analog channel 0 of the MIO board to the analog output of the first chassis. Analog channel 1 of the MIO board is connected to the analog output of the second chassis, analog channel 2 is connected to the analog output of the third chassis, and so on.

When you are cabling a system that has a DIO data acquisition board, the DIO board can control all of the chassis, but some of the extra DIO board lines will be scrambled according to the pin translation table.

AIGNDPAIGNDCACH0PACH8CACH1PACH9CACH2PACH10CACH3PACH11CACH4P	PC7 GND PC6 GND PC5 GND	APC7 BPC7 APC6 BPC6 APC5		12	•	1	1	
AIGND     C       AIGND     C       ACH0     P       ACH0     P       ACH1     P       ACH2     P       ACH10     C       ACH2     P       ACH10     C       ACH10     C       ACH11     C       ACH11     C       ACH11     C       ACH4     P	GND PC6 GND PC5 GND GND	BPC7 APC6 BPC6 APC5		2				1
ACH0     P       ACH8     C       ACH1     P       ACH2     P       ACH10     C       ACH10     C       ACH10     C       ACH10     C       ACH11     C       ACH10     C       ACH11     C       ACH11     C       ACH4     P	PC6 GND PC5 GND	APC6 BPC6 APC5		-		2	2	
ACH8     C       ACH1     P       ACH2     P       ACH2     P       ACH10     C       ACH3     P       ACH11     C       ACH4     P	GND PC5 GND	BPC6 APC5		3		3	3	
ACH1     P       ACH9     C       ACH2     P       ACH10     C       ACH3     P       ACH11     C       ACH4     P	PC5 JND	APC5		4		4	4	
ACH9CACH2PACH10CACH3PACH11CACH4PACH4P	<u>SND</u>			5	-•	5 /	/5	
ACH2PACH10CACH3PACH11CACH4P		BPC5		6		$\overline{6}$	6	
ACH10CACH3PACH3PACH11CACH4P	<b>1</b> 4	APC4	<b>_</b> _/	7		7 /	7	
ACH3 P ACH11 C ACH4 P	AND	BPC4		8		8	8	
ACH11 C ACH4 P	PC3	APC3		9		$\frac{0}{9}$	9	
ACH4 P	GND	BPC3		10		$\frac{10}{10}$	10	
1.0771.0	PC2	APC2		11		11/		
ACH12 C	GND	BPC2		12		$\frac{11}{12}$	12	
ACH5 P	PC1	APC1		13		$\frac{1}{13}/$	13	
ACH13 C	GND	BPC1		14		14//	14	
ACH6 P	PC0	APC0		15		15//	15	
ACH14 C	GND	BPC0		16		16//	16	
ACH7 P	PB7	APB7		17		<u>17</u> //	17	
<u>ACH15</u>	GND	BPB7		18		18/	18	
AISENSE P	<u>PB6</u>	APB6		19		19	19	
DACOOUT C	JND	BPB6		20		20	20	
DACIOUT P	<u>282</u>	APB5		21		21	21	
EXTREF (	<u>JND</u>	BPB5		22		22	22	
AUGND P	<u>284</u>	APB4		23	•	23	$\frac{23}{24}$	
ADIOO D		ADD 2		<u>24</u> 25		24	$\frac{24}{25}$ •	
ADIO0 P		AFD3 DDD2		<u>25</u> 26		$\frac{25}{26}$	$\frac{23}{26}$	
ADIO1 D				20 27		20	27	
RDIO1 C	<u>DZ</u>	RPR2	- <b>•</b>	21		28	$\frac{27}{28}$	
$\frac{DDIO1}{ADIO2}$ P	DR1	ΔPR1		<u>20</u> 20		20	$\frac{20}{29}$	
BDIO2	<u>SND</u>	RPR1		<u>2)</u> 30		30	30	
ADIO3 P	PB0	APB0		31	•	31	31	
BDIO3 C	<u>and</u>	BPB0		32	-	32	32	
DIGGND P	PA7	APA7		33		33	33	
+5 V (	GND	BPA7		34		34	34	
+5 V P	PA6	APA6		35		35	35	
SCANCLK (	GND	BPA6		36		36	36	
EXTSTROBE* P	PA5	APA5		37		37	37	
STARTTRIG* C	GND	BPA5		38		38	38	
STOPTRIG P	PA4	APA4		39		39	39	
EXTCONV* C	GND	BPA4		40		40	40	
SOURCE1 P	PA3	APA3		41		41	41	
GATE1 C	GND	BPA3		42		42	42	
OUT1 P	PA2	APA2		43		43	43	
SOURCE2	JND	BPA2	• ·	44		44	44	
GATE2 P		APAI	(	45		45	45	
		BPAI		46		40	40	
SUUKLES P	<u>2AU</u>	APAU	• _ !	4/	┝●┤	4/	4/	
UALES (		BPAU 5 V		4ð 40	┝●┤	40	48	
$\frac{0013}{FOUT}$		+3 V	<b></b>	<u>49</u> 50		<u>49</u> 50	50	
<u>FUUI</u> (	UND	UND		30	┝●┤	30	<u> </u>	

#### SCXI-1350 Multichassis Pin Translations

Note: Several of the pins on other MIO boards may have slightly different names.

5