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***NI-6581***

**Manufacturer:** National Instruments

**Board Assembly Part Numbers** (Refer to Procedure 1 for identification procedure):

Part Number and Revision	Description
198812A-01L or later	NI 1483, CAMERA LINK ADAPTER MODULE
151148A-0xL or later	NI 5731/5732/5733/5734, DIGITIZER ADAPTER MODULE
153113A-0xL or later	NI 5741/5742, SIGNAL GENERATOR ADAPTER MODULE
199543A-01L or later 158676A-01L or later	NI 5751/5751B, DC COUPLED DIGITIZER ADAPTER MODULE
199468A-01L or later 158679A-01L or later	NI 5752/5752B, AC COUPLED DIGITIZER ADAPTER MODULE
143234A-0x or later	NI 5753, DIGITIZER ADAPTER MODULE
150937A-0x or later	NI 5761, DIGITIZER ADAPTER MODULE
151727A-0xL or later	NI 5762, DIGITIZER ADAPTER MODULE
195815A-0xL or later	NI 5771, DIGITIZER ADAPTER MODULE
152125A-0xL or later	NI 5772, DIGITIZER ADAPTER MODULE
196496A-01L or later	NI 5781, BASEBAND TRANSCEIVER ADAPTER MODULE
153241A-0x or later	NI 5782, IF TRANSCEIVER ADAPTER MODULE
158183A-0xL or later	NI 5783, BASEBAND TRANSCEIVER ADAPTER MODULE
152888A-01L or later	NI 5791, RF TRANSCEIVER ADAPTER MODULE
152889A-01L or later	NI 5792, RF RECEIVER ADAPTER MODULE
152890A-01L or later	NI 5793, RF TRANSMITTER ADAPTER MODULE
198713A-01L or later 156996A-01L or later	NI 6581/6581B, DIGITAL ADAPTER MODULE
199490A-0xL or later	NI 6583, MIXED LOGIC DIGITAL ADAPTER MODULE
199496A-0xL or later	NI 6584, RS-485/RS-422 DIGITAL ADAPTER MODULE
190687A-01L or later 156999A-0xL or later	NI 6585/6585B, LVDS DIGITAL ADAPTER MODULE
192169A-01L or later	NI 6587, LVDS DIGITAL ADAPTER MODULE
157002A-01L or later	NI 6589, LVDS DIGITAL ADAPTER MODULE

**Volatile Memory**

<i>Target Data</i>	<i>Type</i>	<i>Size</i>	<i>Battery Backup</i>	<i>User<sup>1</sup> Accessible</i>	<i>System Accessible</i>	<i>Sanitization Procedure</i>
None						

**Non-Volatile Memory (incl. Media Storage)**

<i>Target Data</i>	<i>Type</i>	<i>Size</i>	<i>Battery Backup</i>	<i>User Accessible</i>	<i>System Accessible</i>	<i>Sanitization Procedure</i>
Device configuration, Product Identification and Configuration Data	EEPROM	2 KB	No	Yes	Yes	Procedure 2

<sup>1</sup> Refer to *Terms and Definitions* section for clarification of *User* and *System Accessible*

## Procedures

### **Procedure 1 –Board Assembly Part Number Identification:**

To determine the Board Assembly Part Number and Revision, refer to the label applied to the surface of your product. The Assembly Part Number should be formatted as “P/N: #####a-##L

### **Procedure 2 – Device Configuration EEPROM:**

The Device Configuration EEPROM and its entire address space are exposed through the FlexRIO Host Interface in LabVIEW. To clear the entire EEPROM, overwrite the entire address space with null values using either the FlexRIO\_Host\_EEPROMWriteByteArray VI or FlexRIO\_Host\_EEPROMWrite32 VI.

## Terms and Definitions

### **Cycle Power:**

The process of completely removing power from the device and its components and allowing for adequate discharge. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

### **Volatile Memory:**

Requires power to maintain the stored information. When power is removed from this memory, its contents are lost. This type of memory typically contains application specific data such as capture waveforms.

### **Non-Volatile Memory:**

Power is not required to maintain the stored information. Device retains its contents when power is removed. This type of memory typically contains information necessary to boot, configure, or calibrate the product or may include device power up states.

### **User Accessible:**

The component is read and/or write addressable such that a user can store arbitrary information to the component from the host using a publicly distributed NI tool, such as a Driver API, the System Configuration API, or MAX.

### **System Accessible:**

The component is read and/or write addressable from the host without the need to physically alter the product.

### **Clearing:**

Per *NIST Special Publication 800-88 Revision 1*, “clearing” is a logical technique to sanitize data in all User Accessible storage locations for protection against simple non-invasive data recovery techniques using the same interface available to the user; typically applied through the standard read and write commands to the storage device.

### **Sanitization:**

Per *NIST Special Publication 800-88 Revision 1*, “sanitization” is a process to render access to “Target Data” on the media infeasible for a given level of effort. In this document, clearing is the degree of sanitization described.