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PCI-6250

Manufacturer: National Instruments

Board Assembly Part Numbers (Refer to Procedure 1 for identification procedure):

Part Number and Revision	Description
190996B-04 or later	PCI-6250
191325B-04 or later	PXI-6250
190996B-03 or later	PCI-6251
192242A-03 or later	PCIe-6251
191325B-03, 191325D-13 or later	PXI-6251
196100A-03 or later	PXIe-6251
190996B-02 or later	PCI-6254
191325B-02 or later	PXI-6254
193588A-01 or later	PCI-6255
193618A-01 or later	PXI-6255
190996A-01 or later	PCI-6259
192242A-01 or later	PCIe-6259
191325A-01, 191325D-11 or later	PXI-6259
196100A-01 or later	PXIe-6259

Volatile Memory

<i>Target Data</i>	<i>Type</i>	<i>Size</i>	<i>Battery Backup</i>	<i>User¹ Accessible</i>	<i>System Accessible</i>	<i>Sanitization Procedure</i>
None						

Non-Volatile Memory (incl. Media Storage)

<i>Target Data</i>	<i>Type</i>	<i>Size</i>	<i>Battery Backup</i>	<i>User Accessible</i>	<i>System Accessible</i>	<i>Sanitization Procedure</i>
Device configuration	EEPROM	2 KB	No			
<ul style="list-style-type: none"> Device information 				No	Yes	None
<ul style="list-style-type: none"> Calibration metadata 				Yes	Yes	Procedure 2
<ul style="list-style-type: none"> Calibration data² 				No	Yes	None

¹ Refer to *Terms and Definitions* section for clarification of *User* and *System Accessible*

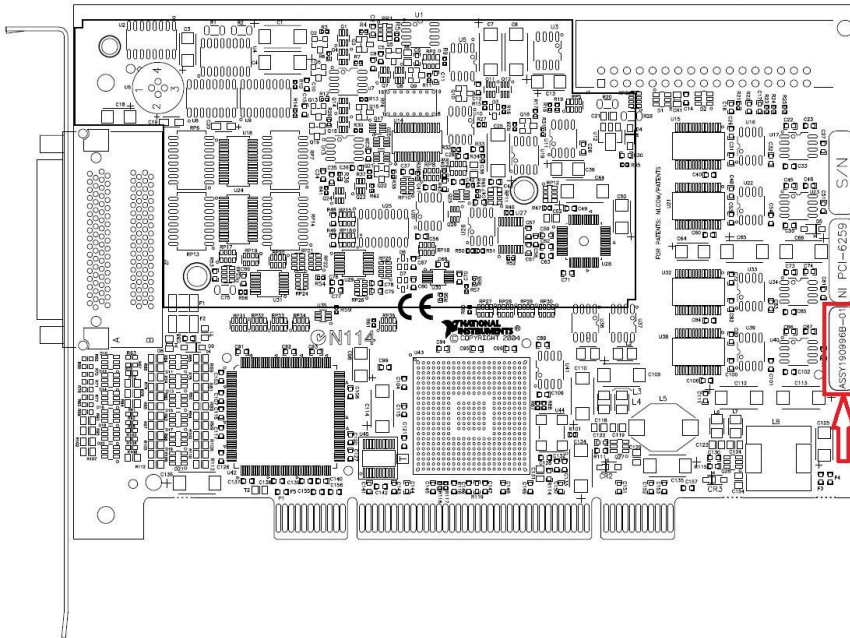
² Calibration constants that are stored on the device include information for the device's full operating range. Any implications resulting from partial self-calibration can be eliminated by running the full self-calibration procedure.

Procedures

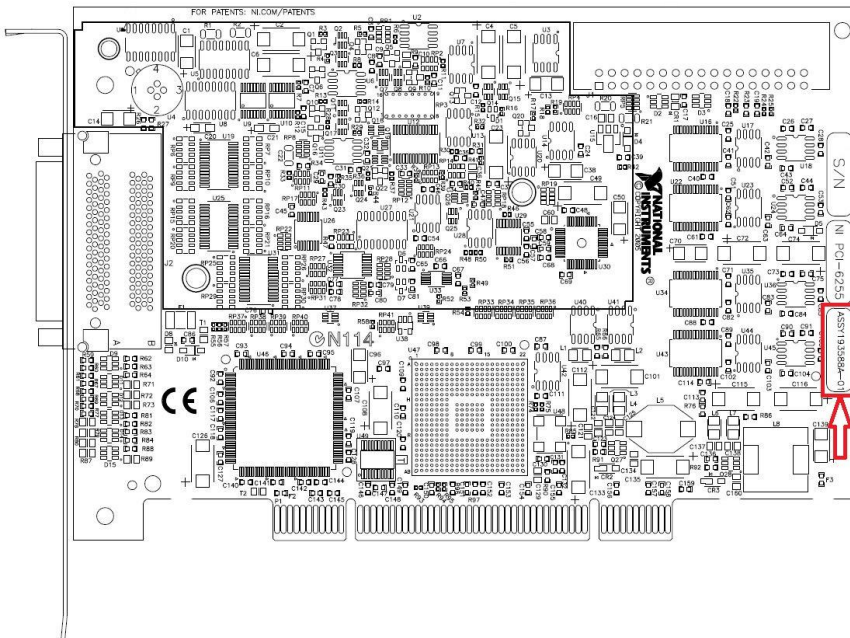
Procedure 1—Board Assembly Part Number Identification:

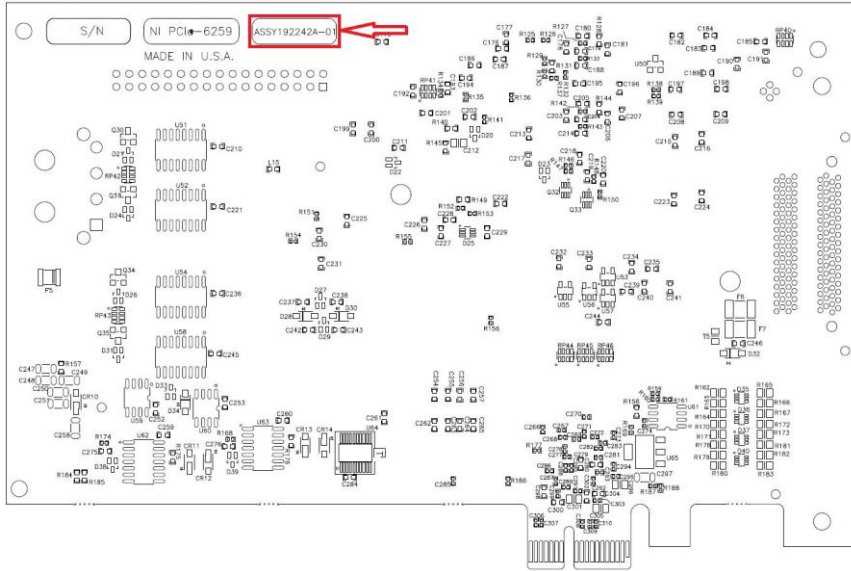
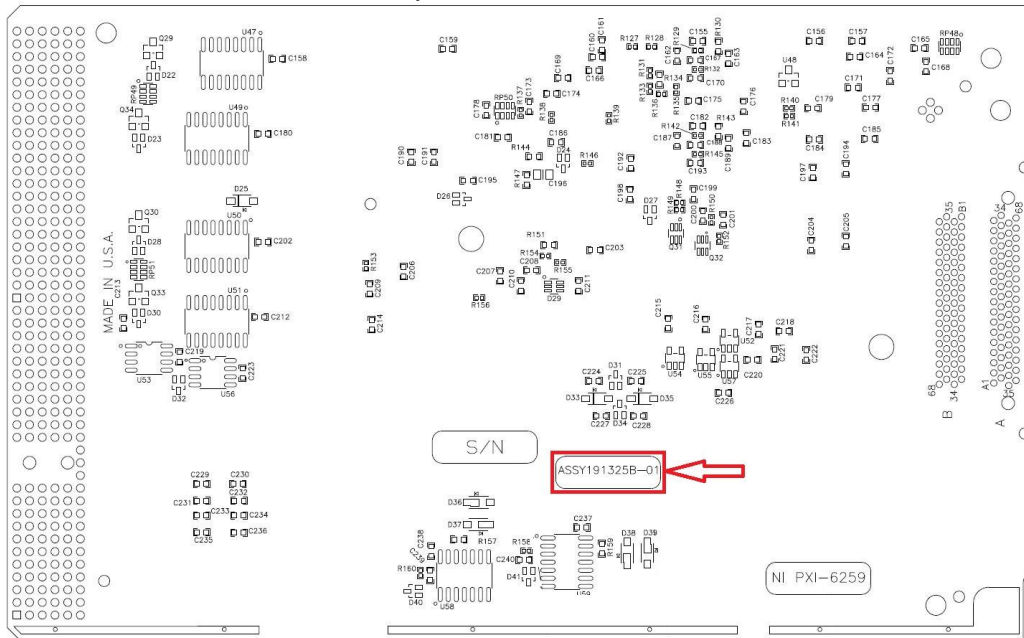
To determine the Board Assembly Part Number and Revision, refer to the “P/N” label applied to the surface of your product as shown below. The Assembly Part Number should be formatted as “P/N: #####a-vvL” where “a” is the letter revision of the Board Assembly (eg. A, B, C...) and the “vv” is the type identifier. If the product is RoHS compliant, “L” can be found at the end of the part number.

PCI-6250/6251/6254/6259 – Primary Side

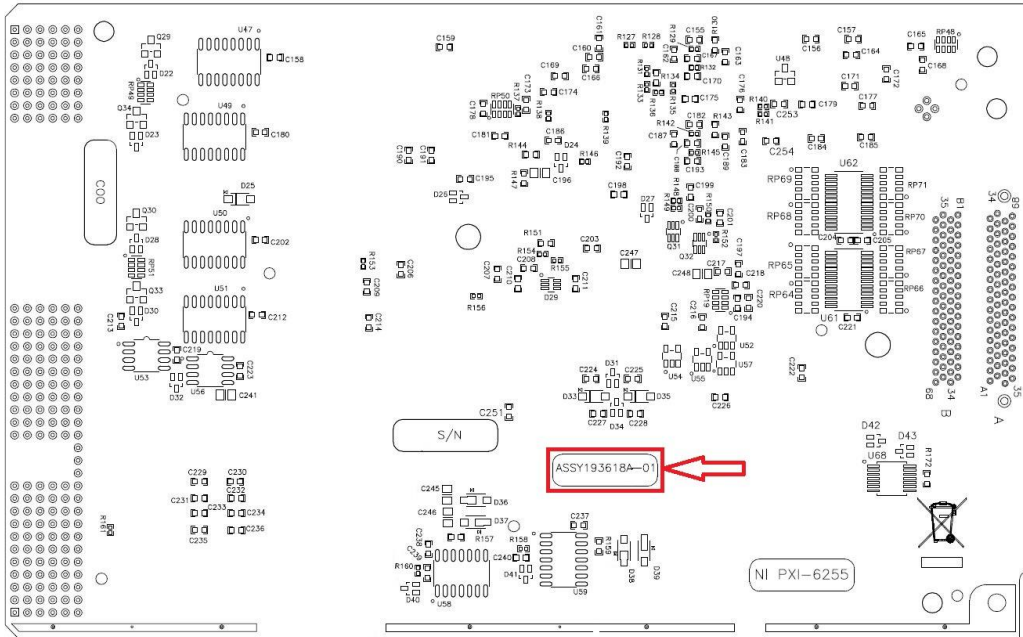


PCI-6255 – Primary Side

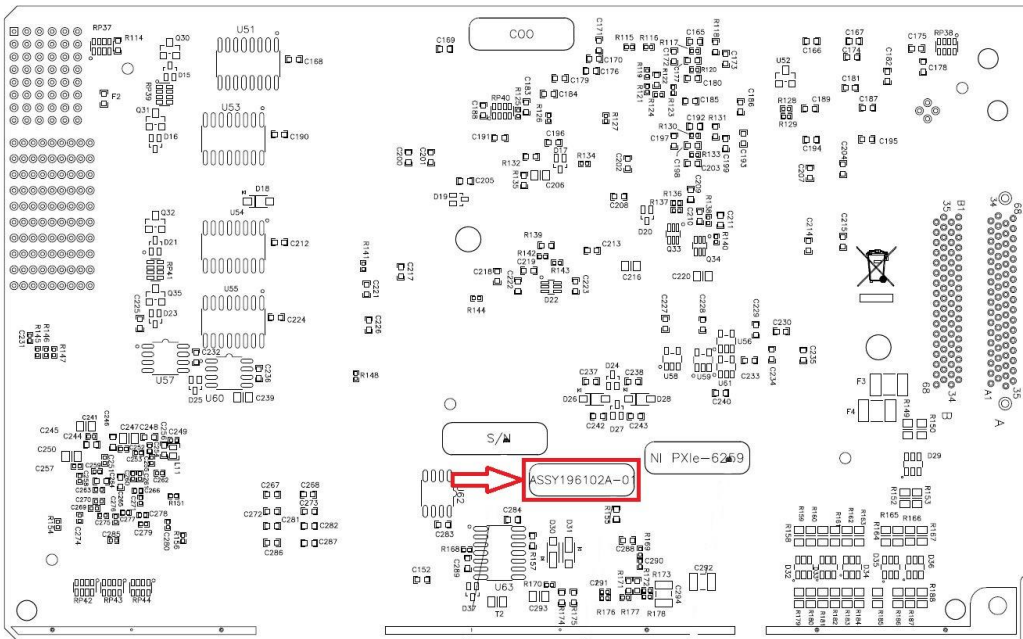


PCIe-6251/6259 – Secondary Side

PXI-6250/6251/6254/6259 – Secondary Side


PXI-6255 – Secondary Side



PXIe-6251/6259 – Secondary Side



Procedure 2 – Device Configuration EEPROM (Calibration Metadata):

The user-accessible areas of the Device Configuration EEPROM are exposed through a calibration Applications Programming Interface (API) in LabVIEW. To clear the Calibration Metadata area, complete the following steps:

1. To clear the calibration password, use the DAQmx Change External Calibration Password.vi to overwrite the current password of the device you wish to clear.
2. The user-accessible area of the Device Configuration EEPROM can be cleared using the NI DAQmx API. For instructions on how to clear these areas, go to www.ni.com/info and enter info code DAQmxLOV.

Terms and Definitions

Cycle Power:

The process of completely removing power from the device and its components and allowing for adequate discharge. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

Volatile Memory:

Requires power to maintain the stored information. When power is removed from this memory, its contents are lost. This type of memory typically contains application specific data such as capture waveforms.

Non-Volatile Memory:

Power is not required to maintain the stored information. Device retains its contents when power is removed. This type of memory typically contains information necessary to boot, configure, or calibrate the product or may include device power up states.

User Accessible:

The component is read and/or write addressable such that a user can store arbitrary information to the component from the host using a publicly distributed NI tool, such as a Driver API, the System Configuration API, or MAX.

System Accessible:

The component is read and/or write addressable from the host without the need to physically alter the product.

Clearing:

Per *NIST Special Publication 800-88 Revision 1*, “clearing” is a logical technique to sanitize data in all User Accessible storage locations for protection against simple non-invasive data recovery techniques using the same interface available to the user; typically applied through the standard read and write commands to the storage device.

Sanitization:

Per *NIST Special Publication 800-88 Revision 1*, “sanitization” is a process to render access to “Target Data” on the media infeasible for a given level of effort. In this document, clearing is the degree of sanitization described.