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PCIe-8253

Manufacturer: IOI Technology Corporation¹

Board Assembly Part Numbers (Refer to Procedure 1 for identification procedure):

Part Number and Revision	OEM P/N	Description
747423-01	FWB-PCIE1X21B	PCIe-8253

¹ Support for this product is provided by National Instruments

Procedures

Procedure 1 – Board Assembly Part Number identification:

To determine the Board Assembly Part Number and Revision, refer to the large print near the user ports. The Part Number should list both FWB-PCIE1x21A and FWB-PCIE1x21B and the Revision is printed above the Part Number.

Procedure 2 – Device Sanitization:

This device does not provide a mechanism for the user to sanitize any memory contents. To sanitize the device, completely destroy the device via burning, melting, disintegration or similar method. Specialized services are available to safely and securely complete this process.

Terms and Definitions

Cycle Power:

The process of completely removing power from the device and its components and allowing for adequate discharge. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

Volatile Memory:

Requires power to maintain the stored information. When power is removed from this memory, its contents are lost. This type of memory typically contains application specific data such as capture waveforms.

Non-Volatile Memory:

Power is not required to maintain the stored information. Device retains its contents when power is removed. This type of memory typically contains information necessary to boot, configure, or calibrate the product or may include device power up states.

User Accessible:

The component is read and/or write addressable such that a user can store arbitrary information to the component from the host using a publicly distributed NI tool, such as a Driver API, the System Configuration API, or MAX.

System Accessible:

The component is read and/or write addressable from the host without the need to physically alter the product.

Clearing:

Per *NIST Special Publication 800-88 Revision 1*, “clearing” is a logical technique to sanitize data in all User Accessible storage locations for protection against simple non-invasive data recovery techniques using the same interface available to the user; typically applied through the standard read and write commands to the storage device.

Sanitization:

Per *NIST Special Publication 800-88 Revision 1*, “sanitization” is a process to render access to “Target Data” on the media infeasible for a given level of effort. In this document, clearing is the degree of sanitization described.