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PXI-1020

# **PXI**

NI PXI-8104 User Manual



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# **Contents**

About	This Manual	
	How to Use the Documentation Set	vii
	Conventions	
	Related Documentation	
Chapt	er 1	
-	luction	
	Benefits of PXI	1-1
	NI PXI-8104	
	Description	
	Functional Overview	
	National Instruments Software	
Chapt	er 2	
-	lation and Configuration	
	Installing the NI PXI-8104	2-1
	How to Remove the Controller from the PXI Chassis	
	BIOS Setup	
	Entering BIOS Setup	
	Main Setup Menu	
	Advanced Setup Menu	
	PXI Setup Menu	
	LabVIEW RT Options Setup Menu	
	Security Menu	
	Boot Setup Menu	
	Exiting BIOS Setup	2-11
	System CMOS	2-11
	LabVIEW RT Installation	2-12
	LabVIEW RT Software Installation	2-12
	LabVIEW RT Configuration Switches	2-15
	Drivers and Software	2-16
	Files and Directories Installed on Your Hard Drive	2-16
	PXI Features	2-17
	PXI Trigger Connectivity	
	Chassis Configuration	
	Basic PXI System Configuration	
	Upgrading RAM	2-19

-20
-21 -21
-1
-2
-3
-5
-6
-7
-9 -10
-10
-11
-11
-1
-1
-2
-3
-4
-6

# About This Manual

This manual contains detailed instructions for installing and configuring your National Instruments NI PXI-8104 embedded controller kit.

## **How to Use the Documentation Set**

Begin by reading the *NI PXI-8104 Installation Guide*, a brief quick-start guide that describes how to install and get started with your controller.

This manual, the *NI PXI-8104 User Manual*, contains more details about changing the installation or configuration from the defaults and using the hardware.

## **Conventions**

The following conventions appear in this manual:

The » symbol leads you through nested menu items and dialog box options to a final action. The sequence **File**»**Page Setup**»**Options** directs you to pull down the **File** menu, select the **Page Setup** item, and select **Options** 

from the last dialog box.

This icon denotes a tip, which alerts you to advisory information.

This icon denotes a note, which alerts you to important information.

This icon denotes a caution, which advises you of precautions to take to

avoid injury, data loss, or a system crash.

bold Bold text denotes items that you must select or click in the software, such

as menu items and dialog box options. Bold text also denotes parameter

names.

italic Italic text denotes variables, emphasis, a cross-reference, or an introduction

to a key concept. Italic text also denotes text that is a placeholder for a word

or value that you must supply.

monospace Text in this font denotes text or characters that you should enter from the

keyboard, sections of code, programming examples, and syntax examples. This font is also used for the proper names of disk drives, paths, directories,

programs, subprograms, subroutines, device names, functions, operations, variables, filenames, and extensions.

#### monospace bold

Bold text in this font denotes the messages and responses that the computer automatically prints to the screen. This font also emphasizes lines of code that are different from the other examples.

## **Related Documentation**

The following documents contain information you may find helpful as you read this manual:

- PICMG 2.0 R3.0 CompactPCI Specification, PCI Industrial Computers Manufacturers Group
- IEEE Standard P1284.1-1997 (C/MM) Standard for Information Technology for Transport Independent Printer/System Interface
- PCI Local Bus Specification, Revision 2.3, PCI Special Interest Group
- PXI Hardware Specification, Revision 2.2, PXI Systems Alliance
- PXI Software Specification, Revision 2.1, PXI Systems Alliance
- Serialized IRQ Support for PCI Systems Specification, Revision 6.0, Compaq Computer et al.
- Universal Serial Bus (USB) Specification, Revision 2.0
- Digital Visual Interface (DVI) Specification, Revision 1.0

# Introduction

## **Benefits of PXI**

The PXI (PCI eXtensions for Instrumentation) industry standard, an open specification governed by the PXI Systems Alliance (PXISA), defines a compact modular PC platform for test, measurement, and control systems. PXI leverages the PCI bus, which is the *de facto* standard for today's desktop computer software and hardware designs. As a result, PXI users receive all the benefits of PCI within an architecture that supports mechanical, electrical, and software features tailored to industrial instrumentation, data acquisition, industrial automation, and control applications.

Well-suited for industrial applications, PXI leverages the CompactPCI specification, which defines a rugged form factor for PCI that offers superior mechanical integrity and easy installation and removal of hardware components. PXI products offer higher and more carefully defined levels of environmental performance required by the vibration, shock, temperature, and humidity extremes of industrial environments. PXI adds mandatory environmental testing and active cooling to the CompactPCI mechanical specification to ease system integration and ensure multivendor interoperability.

Additionally, PXI meets the more specific needs of instrumentation users by adding an integrated trigger bus and reference clock for multiple-board synchronization, a star trigger bus for very precise timing, and local buses for communication between adjacent peripherals.

### NI PXI-8104

#### **Description**

The NI PXI-8104 PXI/CompactPCI embedded computer is a high-performance PXI/CompactPCI system controller. The NI PXI-8104 controller integrates standard I/O features in a single unit by using state-of-the-art packaging. Combining an NI PXI-8104 embedded controller with a PXI-compatible chassis, such as the NI PXI-1042, results in a fully PC-compatible computer in a compact, rugged package.



**Note** The NI PXI-8104 controller will not function in the NI PXI-1020 or NI PXI-1025 chassis.

The standard I/O on each module includes DVI-I (Digital Video Interface Integrated Analog/Digital) video, one RS-232 serial port, a parallel port, four Hi-Speed USB ports, Gigabit Ethernet, a reset button, and a PXI trigger.

The NI PXI-8104 has an Intel<sup>®</sup> Celeron M 440 (1.86 GHz single core processor), all the standard I/O, and a 60 GB (or larger) hard drive.

#### **Functional Overview**

This section contains functional descriptions of each major logic block on the NI PXI-8104 embedded computer.

#### **NI PXI-8104 Functional Description**

The NI PXI-8104 is a modular PC in a PXI 3U-size form factor. Figure 1-1 is a functional block diagram of the NI PXI-8104. Following the diagram is a description of each logic block shown.

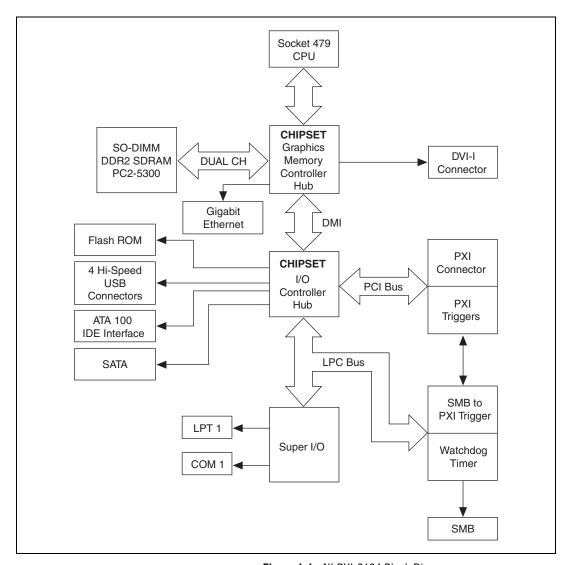


Figure 1-1. NI PXI-8104 Block Diagram

The NI PXI-8104 consists of the following logic blocks on the CPU module and the I/O module. The CPU module has the following logic blocks:

- Socket 479 CPU is the socket definition for the Intel® Celeron M 440 processor.
- The *SO-DIMM* block consists of two 64-bit DDR2 SDRAM sockets that can hold up to 1 GB each.

- The *Chipset 945GMCH* (Graphics and Memory Controller Hub) connects to the CPU, DDR2 SDRAM, and DVI-I video.
- The *SMB to PXI Trigger* provides a routable connection of the PXI triggers to/from the SMB on the front panel.
- The *Watchdog Timer* block consists of a watchdog timer that can reset the controller or generate a trigger.
- The Chipset ICH7M connects to the PCI, USB, IDE, SATA, and LPC buses.
- The *USB Connectors* are connected to the ICH7M chipset.
- The *PXI Connector* connects the NI PXI-8104 to the PXI/CompactPCI backplane.
- The Super I/O block represents the other peripherals supplied by the NI PXI-8104. The NI PXI-8104 has one serial port, and an ECP/EPP parallel port.
- The Gigabit Ethernet connects to either 10 Mbit, 100 Mbit, or 1,000 Mbit Ethernet interfaces.
- The SATA block connects a Serial ATA hard drive to the ICH7M.
- The *ATA-100 IDE* block is dedicated PCI-IDE circuitry providing fast ATA-100 transfers to the internal 2.5 in. hard drive. The IDE feature is built into the chipset. The hard drive is 30 GB or larger.

## **National Instruments Software**

National Instruments has developed several software tools you can use with the NI PXI-8104.

National Instruments' hardware and software work together to help you make the most of your PXI system. The LabVIEW, Measurement Studio, and LabWindows™/CVI™ application development environments combine with leading hardware drivers such as NI-DAQmx to provide exceptional control of NI hardware. Instrument drivers are available at ni.com/idnet to simplify communication with instruments over a variety of busses.

LabVIEW is a powerful and easy-to-use graphical programming environment you can use to acquire data from thousands of different instruments including USB, IEEE 488.2, VXI, serial, PLCs, and plug-in boards. LabVIEW helps you convert acquired data into meaningful results using powerful data analysis routines. Add-on tools provide additional specialized functionality. For more information visit ni.com/labview and ni.com/toolkits.

If you prefer to use Microsoft's Visual Basic, Visual C++, and Visual Studio .NET for the core of your application, Measurement Studio adds tools for Measurement and Automation to each language. For more information visit ni.com/mstudio.

LabWindows/CVI is an interactive ANSI C programming environment designed for building virtual instrument applications. LabWindows/CVI delivers a drag-and-drop editor for building user interfaces, a complete ANSI C environment for building your test program logic, and a collection of automated code generation tools, as well as utilities for building automated test systems, monitoring applications, or laboratory experiments. For more information visit ni.com/lwcvi.

NI-DAQmx provides an extensive library of functions that you can call from your application development environment or interactive environment such as NI SignalExpress. These functions provide an intuitive API for National Instruments' multifunction DAQ products. Features available include analog input (A/D conversion), buffered data acquisition (high-speed A/D conversion), analog output (D/A conversion), waveform generation, digital I/O, counter/timer operations, SCXI signal conditioning, RTSI or PXI synchronization, self-calibration, messaging, and acquiring data to extended memory. For more information visit ni.com/daq.

National Instruments' Modular Instruments use specialized drivers suited to each product's specialization. Express VIs provide customized, interactive programming of instruments in a single interface and soft front panels provide an interface for testing the functionality of each instrument with no programming required. NI Switches, DMMs, High-Speed DIO, High-Speed Digitizers, and Sources each have customized drivers for high-end modular instrumentation systems. RF applications leverage two drivers, NI-RFSG and NI-RFSA and Dynamic Signal Acquisition is available through NI-DAQmx. For more information visit ni.com/

You can expand the timing and triggering functionality of your PXI system with PXI Timing and Synchronization products. These products provide precision clock sources, custom routing of triggers for multi-chassis synchronization, clock sharing, and more and are programmed with NI-Sync. For more information visit ni.com/pxi.

NI-VISA is the National Instruments implementation of the VISA specification. VISA is a uniform API for communicating and controlling USB, Serial, GPIB, PXI, VXI, and various other types of instruments. This API aids in the creation of portable applications and instrument drivers. For information on writing your own PXI instrument driver with NI-VISA, refer to the *NI-VISA Getting Started Manual* and the readme.txt file in the NI-VISA directory. For more information visit ni.com/visa.

With LabVIEW for Linux and support for over two hundred devices on Linux with the NI-DAQmx driver, you can now create Virtual Instruments based on the Linux OS. Instrument control in Linux has been improved by the NI-VISA driver for Linux and NI Modular Instruments are partially supported. For more information visit ni.com/linux.

# **Installation and Configuration**

This chapter contains information about installing and configuring your NI PXI-8104 controller.

# Installing the NI PXI-8104

This section contains general installation instructions for the NI PXI-8104. Consult your PXI chassis user manual for specific instructions and warnings.

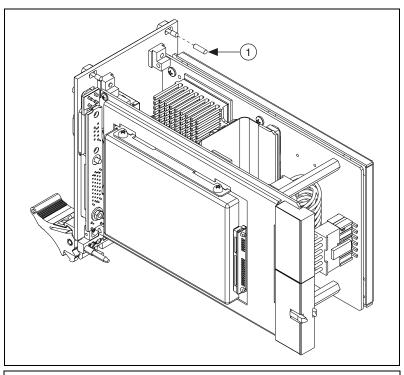
1. Plug in your chassis before installing the NI PXI-8104. The power cord grounds the chassis and protects it from electrical damage while you install the module. (Make sure the power switch is turned off.)



**Caution** To protect both yourself and the chassis from electrical hazards, leave the chassis powered off until you finish installing the NI PXI-8104 module.

- 2. Remove any filler panels blocking access to the system controller slot (Slot 1) in the chassis.
- 3. Touch the metal part of the case to discharge any static electricity that might be on your clothes or body.

4. Remove the protective plastic covers from the four bracket-retaining screws as shown in Figure 2-1.



1 Protective Screw Cap (4X)

Figure 2-1. Removing Protective Screw Caps

Make sure the injector/ejector handle is in its downward position.
 Align the NI PXI-8104 with the card guides on the top and bottom of the system controller slot.



**Caution** Do *not* raise the injector/ejector handle as you insert the NI PXI-8104. The module will not insert properly unless the handle is in its downward position so that it does not interfere with the injector rail on the chassis.

- 6. Hold the handle as you slowly slide the module into the chassis until the handle catches on the injector/ejector rail.
- 7. Raise the injector/ejector handle until the module firmly seats into the backplane receptacle connectors. The front panel of the NI PXI-8104 should be even with the front panel of the chassis.

- 8. Tighten the four bracket-retaining screws on the top and bottom of the front panel to secure the NI PXI-8104 to the chassis.
- 9. Check the installation.
- 10. Connect the keyboard and mouse to the appropriate connectors. If you are using a PS/2 keyboard and a PS/2 mouse, a Y-splitter adapter is available to connect both to a single USB connector. Refer to Figure 4-1, *Y-Splitter Cable*.
- 11. Connect the DVI monitor video cable to the DVI connector. If you are using a VGA monitor, use the DVI-to-VGA adapter included with your kit.
- 12. Connect devices to ports as required by your system configuration.
- 13. Power on the display device.
- 14. Power on the chassis.
- 15. Verify that the controller boots. If the controller does not boot, refer to the *What if the NI PXI-8104 does not boot?* section of Chapter 5, *Troubleshooting*.

Figure 2-2 shows an NI PXI-8104 installed in the system controller slot of a National Instruments PXI-1042 chassis. You can place PXI devices in any other slots.

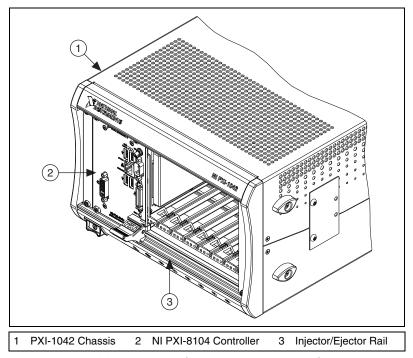


Figure 2-2. NI PXI-8104 Controller Installed in a PXI Chassis

#### How to Remove the Controller from the PXI Chassis

The NI PXI-8104 controller is designed for easy handling. To remove the unit from the PXI chassis, complete the following steps:

- 1. Power off the chassis.
- 2. Remove any cables that may be attached to the controller front panel.
- 3. Unscrew the bracket-retaining screws in the front panel. Refer to Figure 2-1 for the location of these screws.
- 4. Press the injector/ejector handle down.
- 5. Slide the unit out of the chassis.

## **BIOS Setup**

You can change the NI PXI-8104 configuration settings in the BIOS setup. The BIOS is the low-level interface between the hardware and PC software that configures and tests your hardware when you boot the system. The BIOS setup program includes menus for configuring settings and enabling NI PXI-8104 controller features.

Most users do not need to use the BIOS setup program, as the NI PXI-8104 controller ships with default settings that work well for most configurations.



**Caution** Changing BIOS settings may lead to incorrect controller behavior and possibly an unbootable controller. If this happens, follow the instructions for restoring default settings in the *System CMOS* section. In general, do *not* change a setting unless you are absolutely certain what it does.

#### **Entering BIOS Setup**

To start the BIOS setup utility, complete the following steps:

- 1. Power on or reboot your NI PXI-8104 controller.
- 2. When the message Press <DEL> to enter SETUP appears, press the Delete key on the keyboard. The message Entering Setup appears, and the setup program is loaded after a short delay.
- 3. When you first enter the BIOS setup program, it displays the **Main** menu.

Use the following keys to navigate through the BIOS setup:

- Left Arrow, Right Arrow—Use these keys to move between the different setup menus. If you are in a submenu, these keys have no effect, and you need to press <Esc> to leave the submenu first. (To use the arrows on the numeric keypad, you must turn off Num Lock.)
- Up Arrow, Down Arrow—Use these keys to move between the options within a setup menu. (To use the arrows on the numeric keypad, you must turn off Num Lock.)
- **<Enter>**—Use this key either to enter a submenu or display all available settings for a highlighted configuration option.
- **<Esc>**—Use this key to return the parent menu of a submenu. At the top-level menus, this key serves as a shortcut to the **Exit** menu.

- <+> and <->—Use these keys to cycle between all available settings for a selected configuration option.
- **Tab>**—Use this key to select time and date fields.

#### Main Setup Menu

The most commonly accessed and modified BIOS settings are in the **Main** setup menu. The **Main** setup menu includes the following settings:

- **System Time & Date**—This setting controls the time of day, which is stored in a battery-backed real-time clock. Most operating systems also include a way to change this setting. Use <+> and <-> in conjunction with <Enter> and <Tab> to change these values.
- Require Keyboard to Boot—When Enabled, a missing or
  malfunctioning keyboard causes the BIOS to halt with an error. When
  Disabled, the BIOS allows booting without a keyboard. If you are
  using a USB keyboard, you may attach it at any time during the
  powered up state. To use this controller in a "headless" mode, you must
  set this option to Disabled. The default value is Enabled.



**Note** Attaching a USB-to-PS/2 keyboard adapter may allow the system to boot even if no PS/2 keyboard is attached.

- **Num Lock**—This setting indicates whether you turn on Num Lock at boot time. The default value is **On**.
- SATA Port—This item displays the SATA devices detected in the system. Normally, you do not need to modify this item. However, if a SATA device is not autodetected properly, you can specify it manually by pressing <Enter> on the item.
- **IDE Channel 0 Master**—These items display the IDE/ATA devices detected in the system. Normally, you do not need to modify these items. However, if an IDE/ATA device is not autodetected properly, you can specify it manually by pressing <Enter> on an item.
- System Information—This setting displays a screen containing important system information about the NI PXI-8104 controller.

### **Advanced Setup Menu**

This menu contains BIOS settings that normally do not require modification. If you have specific problems such as unbootable disks or resource conflicts, you may need to examine these settings.



**Caution** Changing settings in this menu may result in an unstable or unbootable controller. If this happens, follow the procedures outlined in the *System CMOS* section to restore BIOS settings to their factory defaults.

The **Advanced** setup menu includes the following settings:

- Reset Configuration Data—A portion of the EEPROM on the controller is designated as the Extended System Configuration Data region (ESCD). The BIOS and Plug-and-Play operating systems use this table to store the *Last Known Good* configuration of system peripherals. If you experience resource conflicts or peripheral malfunction, set this setting to Yes to force the BIOS to recreate the ESCD on the next reboot. This is rarely necessary.
- Integrated Peripherals—Use this setting to bring up the Integrated Peripherals submenu. (Refer to the Integrated Peripherals Submenu section.)
- **Quick Boot Mode**—When you enable this option, certain lengthy BIOS tests that rarely fail are skipped to shorten controller boot time. The default is **Enabled**.
- Summary Screen—This setting controls the display of the summary screen shown after BIOS completes its initialization, but before booting takes place. You can disable this screen in the interest of shortening controller boot time. The default is **Disabled**.
- **PXE Network Boot**—This setting enables the option for booting from a network PXE server on the subnet. The default is **Disabled**.

#### **Integrated Peripherals Submenu**

Use this submenu to apply nondefault configurations to the front panel peripherals of an NI PXI-8104 controller. Normally, you do not need to modify these settings, as the factory default settings provide the most compatible and optimal configuration possible.

- **Serial Port A**—This setting enables or disables COM1. You also can change this setting to **Enabled** and modify the base address and Interrupt Request Level (IRQ) of a port. The default is **Auto**, which places COM1 at 0x3F8 IRQ 4.
- **Parallel Port**—Use this setting to enable or disable LPT1. You also can change this setting to **Enabled** and modify the base address, IRQ level, and ISA Direct Memory Access (DMA) channel of the port. The default is **Auto**, which places LPT1 at 0x378, IRQ 7, using ISA DMA Channel 3 if necessary.

- Parallel Port Mode—The PC industry has created several different
  modes of operation for this port over the years. Usually, the default
  setting works for all applications. However, if a parallel port device
  specifically requires a nondefault setting, you can change it here.
  The default is Bidirectional, for full IEEE 1284 capabilities.
- **Legacy USB Support**—Use this setting to use a USB keyboard and mouse as if they were standard PS/2-style peripherals. You *must* enable this setting to use these devices in operating systems with no USB support and to boot from a USB floppy or CD-ROM. The BIOS setup screen always works with USB keyboards regardless of this setting. Certain real-time applications may require you to disable this setting to reduce loop time jitter. The default is **Enabled**.
- AHCI Configuration—This setting determines whether AHCI mode
  is Enabled or Disabled for the SATA port. Some operating systems,
  such as Windows 2000, do not support AHCI mode. You can use this
  setting to disable AHCI mode so that non-compatible OSes function
  correctly. The default setting is Enabled.
- Monitor DDC—This setting determines how the monitor DDC is routed. Use this setting to select whether or not the DDC is routed for an Analog monitor or a DVI monitor. In order to use a DVI monitor, this setting must be set to DVI. An analog monitor, however, will function with this option set to either Analog or DVI. The DDC communication path is only enabled when set to Analog for an analog monitor, so certain advanced features of your analog monitor may only be enabled when routing DDC to Analog. The default setting is DVI.



**Note** After changing DDC routing settings, a power cycle is required to enable the change.

#### **PXI Setup Menu**

Use this menu to control and route certain signals on the PXI backplane. Normally, you do not need to modify these settings. However, other sections of this manual may indicate that modifications are necessary and may lead to unpredictable behavior.

- APIC Routing—This item is valid only for Windows XP and 2000
  and other modern operating systems. Select Enabled to initialize the
  IOAPIC and local APIC in uniprocessor mode. Select Disabled to use
  the legacy PIC for interrupt routing. The default setting is Enabled.
- PIRQx Routing—This setting selects the routing option for PXI/PCI devices connected to PIRQx. This settings affects OSes that do not use APIC routing. The default setting for all PIRQx options is IRQ10.

 Per-Slot Device Settings—This setting brings up the Per-Slot Device Settings submenu. Refer to the Per-Slot Device Settings Submenu section.

#### **Per-Slot Device Settings Submenu**

Use this menu to configure options that can be modified for individual PCI devices in a PXI chassis.



Note Scanning for Option ROMs on devices behind a PCI bridge cannot be disabled.

PCI Device x Option ROM Scan—This setting selects whether PCI device x will be scanned for an option ROM. Setting this option to Enabled allows the BIOS to scan for a PCI option ROM on this PCI device. Setting to Disabled prevents the BIOS from detecting option ROMs on this device. Setting to Disabled does not completely disable the PCI device, just the option ROM scan. The default is Enabled.

## **LabVIEW RT Options Setup Menu**

Use this menu to configure boot options for LabVIEW RT if it is installed on the controller. If you are not using LabVIEW RT, you should leave these settings at default.



**Note** The first three settings below override the behavior of the switches on SW2. Refer to *LabVIEW RT Configuration Switches* section for more information. To use the settings from the switches, select **Use Hardware Switch** for each option.

- Boot Configuration—This setting selects whether the controller should boot LabVIEW RT, LabVIEW RT Safe Mode, or an installed OS such as Windows XP. The default is Use Hardware Switch.
- **Disable Startup VI**—If the controller becomes inaccessible because of a startup VI, this switch can prevent VIs from automatically running at startup. The default is **Use Hardware Switch**.
- Reset IP Address—If the controller is deployed to a different subnet from which it was originally configured, or if the current IP address is invalid, use this switch to reset the IP address and other TCP/IP settings to their factory defaults during LabVIEW RT startup. The default is Use Hardware Switch.
- Video Output—This setting enables normal video display output when set to Auto. Set to Disable to disable video and reduce jitter with LabVIEW RT. Note that the video display is disabled only when the boot switch is also set to On to boot LabVIEW RT. The default setting is Auto.



**Note** By default, the target will automatically attempt to connect to the network using DHCP. If the target is unable to initiate a DHCP connection, the target connects to the network with a link-local IP address or 169.254.x.x.

#### **Security Menu**

Use this menu to enable BIOS security options.

- Set User Password—This setting allows you to specify a password
  that must be entered to boot the system. To activate this feature, you
  must first specify a Supervisor password and enable the Password on
  boot feature. By default, no password is specified.
- Set Supervisor Password—This setting allows you to specify a
  password that must be entered to access the BIOS setup options. By
  default, no password is specified.
- Password on Boot—This setting controls whether or not a password
  is required to boot the system. If enabled, the user must enter the User
  Password to boot the system. The default setting is Disabled.
- Write Protect Boot Sector—When set to Yes, this setting prevents modification of a hard disk boot sector via INT 13h services, which may help prevent certain computer viruses from infecting the controller. This setting does not prevent boot sector modification by 32-bit operating system drivers that access the hard disk directly. The default is No.

#### **Boot Setup Menu**

This screen displays the boot order of devices associated with the controller. The BIOS proceeds down the **Boot priority order list** in search of a bootable device. Devices under the **Excluded from boot order** list will not be used for booting. If the BIOS fails to find any bootable device, the message **Operating System Not Found** is displayed, and the system halts.

- IDE HDD—The internal hard drive.
- USB HDD—A USB based flash drive or hard disk drive.
- USB CDROM—A USB based CD-ROM drive.
- USB FDC—A USB based floppy disk drive.
- **PCI SCSI**—A SCSI drive (hard disk drive or CD-ROM) connected through a SCSI controller in the PXI chassis.
- PCI LAN—A PXE Network boot device, if PXE Network Boot is enabled on the Advanced menu.

### **Exiting BIOS Setup**

The **Exit** setup menu includes all available options for exiting, saving, and loading the BIOS default configuration. As an alternative to this screen, press <F9> to load BIOS default settings and <F10> to save changes and exit setup.

The **Exit** setup menu includes the following settings:

- Exit Saving Changes—Any changes made to BIOS settings are stored in the battery-backed System CMOS. The setup program then exits and reboots the controller.
- Exit Discarding Changes—Any changes made to BIOS settings during this session of the BIOS setup program are discarded. The setup program then exits and boots the controller without rebooting first.
- Load Setup Defaults—This setting restores all BIOS settings to the
  factory default. This is useful if the controller exhibits unpredictable
  behavior due to an incorrect or inappropriate BIOS setting. Notice
  that any nondefault settings such as boot order, passwords, and
  keyboardless operation are restored to their factory defaults. This may
  produce undesirable behavior, and in heavily customized cases, may
  cause the controller to malfunction or fail to boot.
- Discard Changes—Any changes made to BIOS settings during this session of the BIOS setup program are discarded. Unlike Exit Discarding Changes, however, the BIOS setup continues to be active.
- Save Changes—Changes made to BIOS settings during this session are committed to battery-backed System CMOS. The setup program remains active, allowing further changes.

# **System CMOS**

The NI PXI-8104 contains a backed-up memory used to store BIOS configuration information.

Complete the following steps to clear the CMOS contents:

- Power off the chassis.
- 2. Remove the controller from the chassis.
- 3. Move the jumper on W7 from pins 1–2 to pins 2–3, as shown in Figure 2-3.
- 4. Wait one second. Move the jumper back to pins 1–2.
- 5. Reinstall the controller in the chassis.



**Caution** Do *not* leave the jumper on pins 2–3. Doing so decreases battery life. In addition, leaving the jumper on the pins prevents the controller from booting.

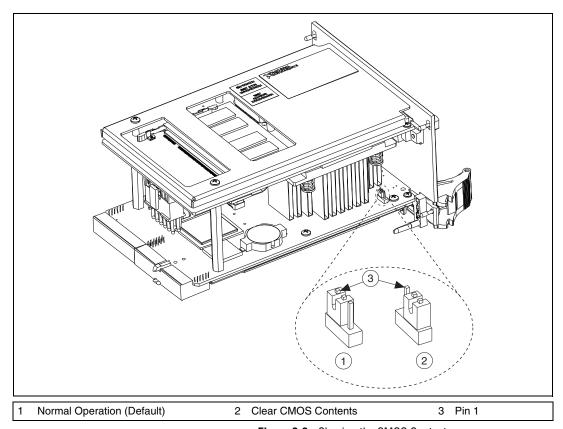


Figure 2-3. Clearing the CMOS Contents

## **LabVIEW RT Installation**

This section explains software installation and switch configuration for LabVIEW RT on your PXI controller.

#### **LabVIEW RT Software Installation**

The following section describes the necessary steps to get your PXI embedded controller setup to run LabVIEW Real-Time. In this section you will configure the boot mode of the controller, verify or change IP settings, and install LabVIEW Real-Time software.

Complete the following steps to install the LabVIEW RT software.

 Boot the NI PXI embedded controller into the real-time operating system. Refer to the *LabVIEW RT Configuration Switches* section or the *LabVIEW RT Options Setup Menu* section in this manual to configure the controller for booting into LabVIEW RT.

The PXI controller will automatically boot into LabVIEW RT Safe Mode when no software is installed. LabVIEW RT Safe Mode loads with the basic real-time operating system and will automatically attempt to connect to the network using DHCP. If DHCP is not available, it will then connect to the network with a link-local IP address.



**Tip** You can connect a monitor to the desktop PC to display startup messages such as the IP address and MAC address.

 Open Measurement & Automation Explorer (MAX) on another computer in the same subnet and expand the **Remote Systems** branch. MAX lists the PXI controller as the model name of the controller followed by the MAC address (for example, NI-PXI-8104 00802f108562).



**Tip** Record the PXI controller MAC address, located on the side of the controller, for use in identifying the controller. The label also can be removed and placed on the front of the controller for easier access.

- 3. Click on the appropriate PXI controller entry to access the **Network Settings** tab in the right pane view.
- 4. (Optional) Enter a name for the RT target in the Name text box.
- 5. Set the network configuration options of the RT target in the **IP Settings** section and click the **Apply** button.

For information about configuring network settings, refer to the *Configuring Network Settings* book, accessible by browsing to MAX Remote Systems Help»LabVIEW Real-Time Target Configuration»Configuring Network Settings from the Contents tab of *MAX Help*.



**Note** When any IP or identification settings are changed, you will be prompted to reboot the controller for the changes to take effect. Click **Yes** to automatically reboot the RT target. You may also reboot the controller by right-clicking on the target name under **Remote Systems** and selecting **Reboot**.

After rebooting the PXI controller it will appear in the **Remote Systems** category with the assigned name.

Figure 2-4 shows the RT Series PXI target, **PXI-8106**, configured to automatically obtain an IP address from a DHCP server, as indicated in the **IP Settings** section of the figure.

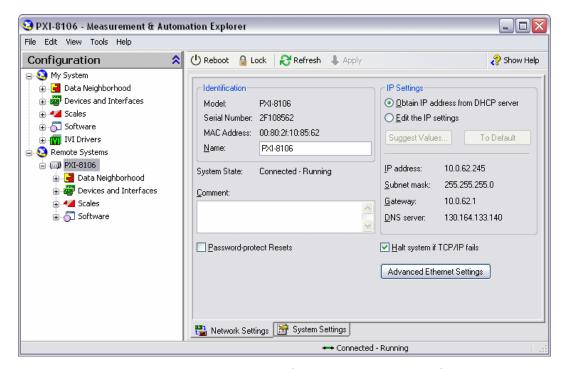


Figure 2-4. Configuring RT Target Network Settings

- 6. Expand the PXI controller view in the **Remote Systems** branch and select **Software**.
- 7. Click the **Add/Remove Software** button in the toolbar to launch the LabVIEW Real-Time Software Wizard.
- 8. Install the LabVIEW Real-Time software and device drivers that you require on the RT target. Refer to the NI Web site at ni.com/info and enter the info code etspc for the latest information about supported software.

After installation of the software the controller will automatically reboot and you will now be able to program it using LabVIEW Real-Time.



**Note** Refer to the *RT Getting Started Guide* available on your host computer for more information about setting up your RT target.

#### **LabVIEW RT Configuration Switches**

Use the LabVIEW RT configuration switches to configure LabVIEW RT if it is installed on the controller. If you are not using LabVIEW RT, these switches should remain in the OFF position. The controller reads these switches only after a system reset.



**Note** You must reboot the controller for any changes to take place.

The NI PXI-8104 controller includes the following LabVIEW RT configuration switches:

- Switch 1—Boot LabVIEW RT: Set this switch to ON to boot LabVIEW RT.
- Switch 2—Boot Safe Mode: Set this switch to ON to boot LabVIEW RT into safe mode to reconfigure TCP/IP settings and to download or update software from a host computer. This switch overrides the behavior of Switch 1. Booting the controller into safe mode does not start the embedded LabVIEW RT engine. After changing the settings or software, reboot the controller with this switch OFF to resume normal operation.
- Switch 3—**Disable Startup VI**: Set this switch to ON to prevent VIs from automatically running at startup if the controller becomes inaccessible because of a startup VI.
- Switch 4—Reset IP Address: Set this switch to ON to reset the
  IP address and other TCP/IP settings to their factory defaults. Use this
  switch if moving the controller to a different subnet or if the current
  TCP/IP settings are valid.



**Note** By default, the target will automatically attempt to connect to the network using DHCP. If the target is unable to initiate a DHCP connection, the target connects to the network with a link-local IP address or 169.254.x.x.

Figure 2-5 shows the location of the LabVIEW RT configuration switches. The switches are shown in the OFF position.

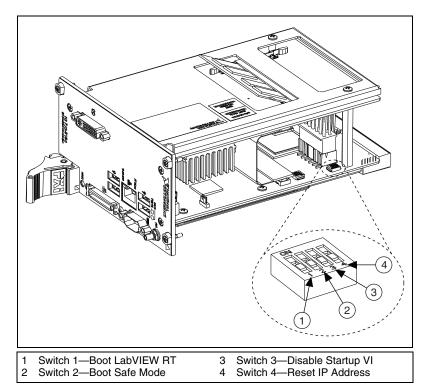


Figure 2-5. LabVIEW RT Configuration Switches

## **Drivers and Software**

#### Files and Directories Installed on Your Hard Drive

Your hard drive includes a directory called images in its root that contains software and soft copies of manuals for the installed devices. The directory structure under the images directory is logically organized into several levels.

In the images directory, you will find a manuals directory, an os directory, and a drivers directory.

The manuals directory contains quick reference guides, technical reference manuals, and National Instruments software manuals, all in Adobe Acrobat format. To access any manual, change your directory to c:\images\manuals and list the contents of that directory. You will see several files, one corresponding to each device.

The os directory contains a subdirectory corresponding to the operating system installed on your controller.

The rest of the directories correspond to each device in your controller. Within each of these directories are the drivers for the devices. These files and directories are copied exactly from the manufacturer distribution disks, so the naming conventions vary from device to device.

### **PXI** Features

#### **PXI Trigger Connectivity**

The SMB connector on the NI PXI-8104 front panel can connect to or from any PXI backplane trigger line. A trigger allocation process is needed to prevent two resources from connecting to the same trigger line, resulting in the trigger being double-driven and possibly damaging the hardware. At the time of this manual's publication, this software is not yet available for Windows. Contact National Instruments for more information.

## **Chassis Configuration**

Configuration of the PXI system is handled through Measurement & Automation Explorer (MAX), included with the software pre-installed on your controller. MAX creates the pxisys.ini file, which defines the layout and parameters of your PXI system.

The configuration steps for single or multiple-chassis systems are the same. An example of a multichassis configuration is shown in Figure 2-6.

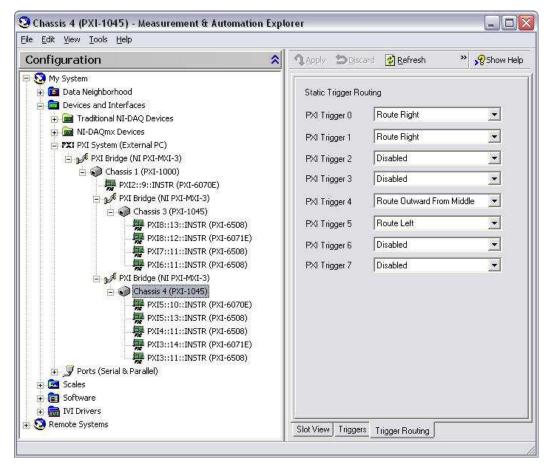


Figure 2-6. Multichassis Configuration in MAX

#### **Basic PXI System Configuration**

- Launch MAX.
- In the Configuration tree, click the Devices and Interfaces branch to expand it.
- If the PXI system controller has not yet been configured, it is labeled PXI System (Unidentified). Right-click this entry to display the pop-up menu, then select the appropriate controller model from the Identify As submenu.
- 4. Click the **PXI System** controller. The chassis (or multiple chassis, in a multichassis configuration) is listed below it. Identify each chassis by right-clicking its entry, then selecting the appropriate chassis model

through the **Identify As** submenu. Further expanding the **PXI System** branch shows all devices in the system that can be recognized by NI-VISA. When your controller and all your chassis are identified, the required pxisys.ini file is complete.

The PXI specification allows many combinations of PXI chassis and system modules. To assist system integrators, the manufacturers of PXI chassis and system modules must document the capabilities of their products. The minimum documentation requirements are contained in .ini files, which consist of ASCII text. System integrators, configuration utilities, and device drivers can use these .ini files.

The capability documentation for the chassis is contained in a chassis.ini file provided by the chassis manufacturer. The information in this file is combined with information about the system controller to create a single system initialization file called pxisys.ini (PXI System Initialization). The NI PXI-8104 uses MAX to generate the pxisys.ini file from the chassis.ini file.

Device drivers and other utility software read the pxisys.ini file to obtain system information. For detailed information about initialization files, refer to the PXI specification at www.pxisa.org.

## **Upgrading RAM**

You can change the amount of installed RAM on the NI PXI-8104 by upgrading the SO-DIMM.

To upgrade the RAM, remove the NI PXI-8104 from the PXI chassis. To optimize both memory capacity and system performance, use the same size and speed memory module in each of the two module slots. The use of different size modules in each slot is supported, but system performance will be slower than using two matched modules. However, two mismatched modules will result in better performance than using a single module.

National Instruments offers the following types of SO-DIMMs for use with the NI PXI-8104 controller.

- PC2-5300 512 MB, 64 MB × 64, CL 5, 1.18 in. max (National Instruments part number 779302-512)
- PC2-5300 1 GB, 128 MB × 64, CL 5, 1.18 in. max (National Instruments part number 779302-1024)



**Note** National Instruments has tested and verified that the DDR2 SO-DIMMs we sell work with the NI PXI-8104. We recommend you purchase your DDR2 SO-DIMM modules from National Instruments. Other off-the-shelf DDR2 SO-DIMM modules are not guaranteed to work properly.

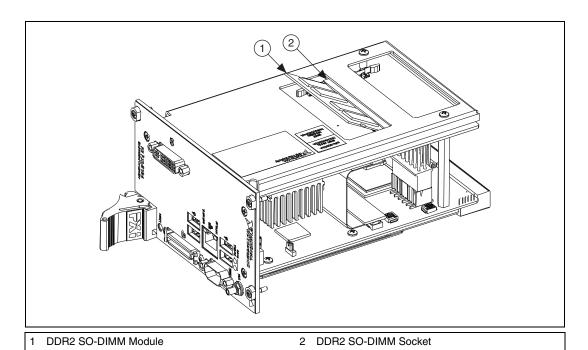


Figure 2-7. Installing a DDR2 SO-DIMM in an NI PXI-8104 Controller

# **Hard Drive Recovery**

NI PXI-8104 controllers include two methods of restoring the original factory condition of your hard drive. Hard drive-based recovery stores a factory backup on a separate portion of your hard drive allowing you to restore your controller without additional media. The NI PXI-8104 controller also ships with an OS Recovery CD that allows you to reinstall your operating system onto your hard drive through an external CD-ROM. For more information on these tools refer to KnowledgeBase 2ZKC02OK at ni.com/support.



**Note** Your system hot key is <F4>. To access the hard drive-based recovery tool, press and hold <F4> when video first appears during the boot process.

If you need to recover your factory-installed operating system from a CD, you can use the included OS re-installation CD with an external CD-ROM drive such as a USB CD-ROM drive. Boot the PXI controller using the OS re-installation CD to recover the OS. You also may need to reinstall other software after using the CD to recover the OS.



**Note** Recovering the OS erases the contents of your hard disk. Back up any files you want to keep prior to the recovery.

# Installing an OS

NI PXI-8104 controllers include a pre-installed OS. In some cases, you may want to install a different OS. When doing so, consider the following guidelines.

## **Installing from a CD-ROM**

The NI PXI-8104 supports the installation of Windows XP from a USB CD-ROM. However, many other operating systems do not support installation from a USB CD-ROM. For example, Windows 2000 aborts during the install process because it does not have drivers for the CD-ROM device.

With DOS drivers, you can install Windows 9x operating systems. However, only a few USB CD-ROM drives have DOS drivers.

As an alternative to a USB CD-ROM drive, you can use an external SCSI CD-ROM with a PXI-SCSI adapter.



**Note** For additional assistance with installing or changing an operating system, refer to KnowledgeBase 2ZKC02OK at ni.com/support.

# I/O Information

## **Front Panel Connectors**

Table 3-1 lists various I/O interfaces and their corresponding NI PXI-8104 external connectors, bus interfaces, and functions.

Table 3-1. NI PXI-8104 I/O Overview

I/O Interface	External Connector	Description
Video	DVI-I (24-pin DSUB)	Intel Extreme Graphics controller
Serial	COM1 (9-pin DSUB)	16550 RS-232 serial port
Ethernet	LAN (RJ45)	10/100/1000 Ethernet connection
Parallel	Parallel Port (36-pin champ)	IEEE 1284
USB (four ports)	USB 4-pin Series A stacked receptacle	Hi-Speed USB
PXI trigger	Trigger (SMB)	Routing PXI triggers to or from the backplane trigger bus

### **Front Panel**

Figure 3-1 shows the front panel layout and dimensions of the NI PXI-8104. Dimensions are in inches [millimeters].

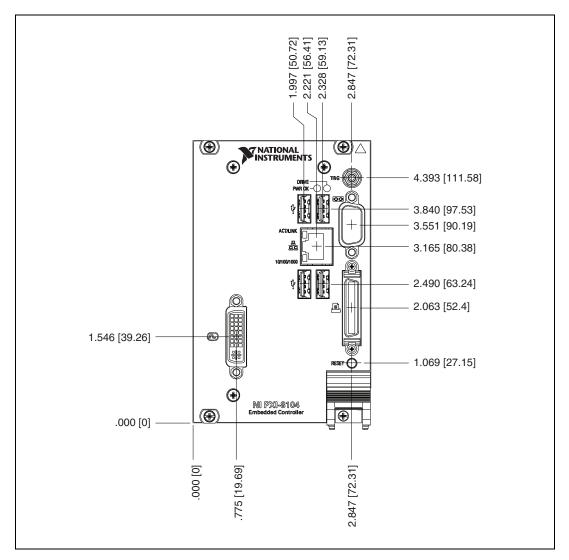


Figure 3-1. NI PXI-8104 Front Panel Layout and Dimensions

#### DVI-I

Figure 3-2 shows the location and pinouts for the DVI-I connector on the NI PXI-8104. Table 3-2 lists and describes the DVI-I connector signals.

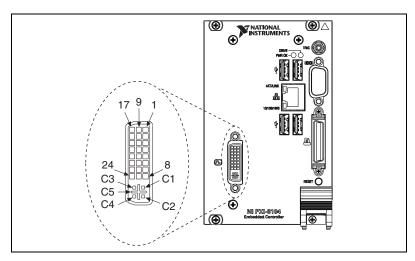


Figure 3-2. DVI-I Connector Location and Pinout

Table 3-2. DVI-I Connector Signals

Pin	Signal Name
1	TMDS Data2-
2	TMDS Data2+
3	TMDS Data2/4 Shield
4	Reserved
5	Reserved
6	DDC Clock [SCL]
7	DDC Data [SDA]
8	Analog vertical sync
9	TMDS Data1-
10	TMDS Data1+
11	TMDS Data1/3 Shield
12	Reserved

Table 3-2. DVI-I Connector Signals (Continued)

Pin	Signal Name
13	Reserved
14	+5 V Power
15	Ground (for +5 V)
16	Hot Plug Detect
17	TMDS Data0-
18	TMDSData0+
19	TMDS Data0/5 Shield
20	Reserved
21	Reserved
22	TMDS Clock Shield
23	TMDS Clock+
24	TMDS Clock-
C1	Analog Red
C2	Analog Green
C3	Analog Blue
C4	Analog Horizontal Sync
C5	Analog GND Return: (analog R, G, B)

#### COM<sub>1</sub>

Figure 3-3 shows the location and pinouts for the COM1 connector on the NI PXI-8104. Table 3-3 lists and describes the COM1 connector signal.

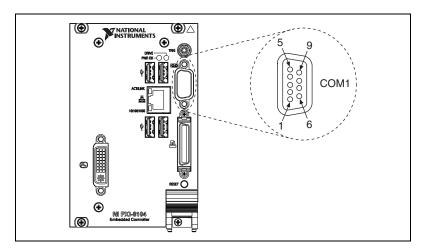


Figure 3-3. COM1 Connector Location and Pinout

Table 3-3. COM1 Connector Signals

Pin	Signal Name	Signal Description
1	DCD	Data Carrier Detect
2	RXD	Receive Data
3	TXD	Transmit Data
4	DTR	Data Terminal Ready
5	GND	Ground
6	DSR	Data Set Ready
7	RTS	Ready to Send
8	CTS	Clear to Send
9	RI	Ring Indicator

#### **Ethernet**

Figure 3-4 shows the location and pinouts for the Ethernet connector on the NI PXI-8104. Table 3-4 lists and describes the Ethernet connector signals.

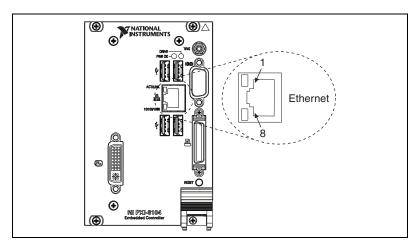


Figure 3-4. Ethernet Connector Location and Pinout

Table 3-4. Ethernet Connector Signals

Pin	Fast Ethernet	Gigabit Ethernet
1	TX+	TX_A+
2	TX-	TX_A-
3	RX+	RX_B+
4	NC	TX_C+
5	NC	TX_C-
6	RX-	RX_B-
7	NC	RX_D+
8	NC	RX_D-



**Note** The Ethernet controller can perform automatic crossover, thus eliminating the need for crossover cables.

Condition **LED** Color **LED State** Off LAN link is not established. Top Green LAN link is established. On (steady state) On (brighter and pulsing) The controller is communicating with another computer on the LAN. Unlit Off 10 Mbit/sec data rate is selected. Bottom Green On 100 Mbit/sec data rate is selected. Orange On 1000 Mbit/sec data rate is selected.

Table 3-5. 10/100/1000 LAN Connector LED States

#### **Parallel Port**

Figure 3-5 shows the location and pinouts for the IEEE 1284 (parallel) connector on the NI PXI-8104. Table 3-6 lists and describes the IEEE 1284 connector signals.

Parallel port adapter cables are available from National Instruments, part number 777169-01.

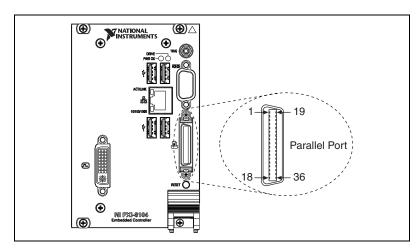


Figure 3-5. Parallel Port Connector Location and Pinout

Table 3-6. Parallel Port Connector Signals

	Default Configuration (LPT)	
Pin	Signal Name	Signal Description
1	BUSY	Device Busy
2	SLCT	Select
3	ACK#	Acknowledge
4	FAULT#(ERROR#)	Fault
5	PAPEREND	Paper End
6	PD0	Data Bit 0
7	PD1	Data Bit 1
8	PD 2	Data Bit 2
9	PD3	Data Bit 3
10	PD4	Data Bit 4
11	PD5	Data Bit 5
12	PD6	Data Bit 6
13	PD7	Data Bit 7
14	INIT#	Initialize Printer
15	STROBE#	Strobe
16	SLCTIN#	Select Input
17	AUTOFD#	Auto Line Feed
18	+5V	+5 V
19–35	GND	Ground
36	NC	Not Connected

#### **Universal Serial Bus**

Figure 3-6 shows the location and pinouts for the Universal Serial Bus (USB) connectors on the NI PXI-8104. Table 3-7 lists and describes the USB connector signals.

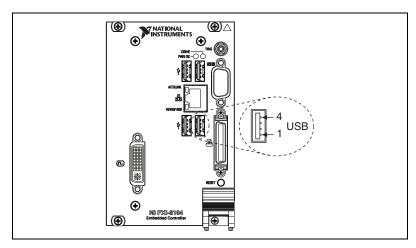


Figure 3-6. USB Connector Location and Pinout

Table 3-7. USB Connector Signals

Pin	Signal Name	Signal Description
1	VCC	Cable Power (+5 V)
2	D-	USB Data –
3	D+	USB Data +
4	GND	Ground

#### **Trigger**

The TRG connector is the software-controlled trigger connection for routing PXI triggers to or from the backplane trigger bus.

A trigger allocation process is needed to prevent two resources from connecting to the same trigger line, resulting in the trigger being double-driven and possibly damaging the hardware. At the time of this manual's publication, this software is not yet available for Windows. Contact National Instruments for more information.

Figure 3-7 shows the TRG connector location on the NI PXI-8104. Table 3-8 lists and describes the trigger connector signals.

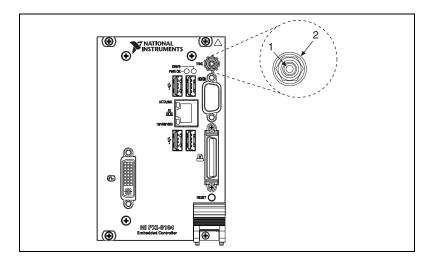


Figure 3-7. TRG Connector Location and Pinout

**Table 3-8.** TRG Connector Signals

Pin	Signal Name	Signal Description
1	TRIG	Trigger
2 (Shield)	GND	Ground

#### **Front Panel Features**

The NI PXI-8104 controller has the following front-panel features:

- A controller reset pushbutton (press the button to generate a reset to the controller)
- Two front panel LEDs that show PC status
  - The POWER OK LED indicates the power status of the controller. The LED will indicate one of the following states:
    - ON steady—PXI and onboard power is on and within regulation limits.
    - Blinking—One of the PXI or onboard supplies is operating outside of the normal limits, or is not functioning.
    - OFF—The power to the controller is off.
  - The **DRIVE** LED indicates when an access to the internal hard disk is occurring.

### **Data Storage**

The NI PXI-8104 has the following data storage features:

- Internal hard drive
  - 2.5 in. notebook hard drive
  - Supports SATA
- USB storage support—USB CD-ROM, mass storage device, or floppy drive

# Common Configuration Questions

This chapter answers common configuration questions you may have when using the NI PXI-8104 embedded controller.

### **General Questions**

#### What do the LEDs on the NI PXI-8104 front panel mean?

Refer to the LED status descriptions in the *Front Panel Features* section of Chapter 3, *I/O Information*.

### How do I check the configuration of the memory, hard drive, time/date, and so on?

You can view these parameters in the BIOS setup. To enter the BIOS setup, reboot the NI PXI-8104 and press < Delete > during the memory tests. Refer to the *Entering BIOS Setup* section of Chapter 2, *Installation and Configuration*, for more information.

### Can I use the internal hard drive and an external hard drive at the same time?

Yes.

### **Boot Options**

#### What devices can I boot from?

The NI PXI-8104 can boot from the following devices:

- The internal hard drive
- An external SCSI hard drive or CD-ROM if an SCSI adapter, such as the PXI-8214, is used
- A network PXE server on the same subnet

- An external USB mass storage device such as a USB hard drive or CD-ROM
- An external USB floppy drive
- Most PCI-based boards that provide an Option ROM



**Note** There are some limitations when booting from a USB device. Windows XP can be installed from a USB CD-ROM, but earlier versions of Windows cannot. The NI PXI-8104 BIOS configures the USB devices so that they will work in a DOS environment.

#### How do I configure the controller to boot from these devices?

There are two methods.

- Enter Setup and select the **Boot** menu. You will see a list of all bootable devices, ordered by device type. You can set the boot order using <+> and <->. Set the order by device type and set the order for the devices listed within the device type.
- To boot from a different device without permanently changing the boot order, press <F10> during POST. After the BIOS completes the POST and just before the controller boots the OS, the **Boot** menu is displayed. You can select the device type you want to boot from.

#### Cables and Connections

### How do I plug both a PS/2 mouse and PS/2 keyboard into the controller?

The NI PXI-8104 has no PS/2 connector, and you need to use a USB Y-splitter cable as shown in Figure 4-1, or a similar device, to connect both a PS/2 mouse and PS/2 keyboard. National Instruments part number 778713-01 is such a cable and is available through the online catalog at ni.com/products.

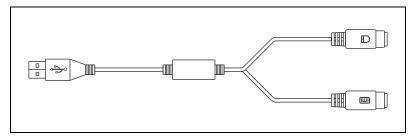


Figure 4-1. Y-Splitter Cable

### What if I don't have a Y-splitter cable? Can I still use a mouse and keyboard?

If you do not have a Y-splitter cable, plug a USB keyboard into any USB connector. You can also plug a USB mouse into any USB connector.

### How do I connect a standard 25-pin LPT cable to the NI PXI-8104?

The NI PXI-8104 uses a type C LPT connector. Most parallel port devices use a type A connector. To use a device with a standard type A LPT connector, you need to use a type C-to-type-A LPT adapter. Parallel port adapter cables, National Instruments part number 777169-01, are available through the online catalog at ni.com/products.

#### How do I connect a VGA monitor to the NI PXI-8104?

A VGA-to-DVI-I adapter (National Instruments part number 762559-01) is included with your kit. You can use this adapter to connect a VGA monitor to the DVI-I port.

#### **Software Driver Installation**

#### How do I install or reinstall the video driver?

Refer to KnowledgeBase 3H3COSD8 at ni.com/support.

#### How do I install or reinstall the Ethernet driver?

Refer to KnowledgeBase 3H3COSD8 at ni.com/support.

#### How do I install software from a CD?

The compact size of the NI PXI-8104 does not allow for an integrated CD-ROM drive. You have the following options:

- USB CD-ROM—You can install from a USB CD-ROM using a bootable installation CD.
- SCSI CD-ROM—Other types of CD-ROM drives are available.
   Check with the vendor to make sure Windows XP supports the drive.
- Mapped network drive—You can use the Ethernet to connect to another computer. If you share the CD-ROM drive on the other computer, you can map the shared CD-ROM drive to a drive letter on the NI PXI-8104.

### **Upgrade Information**

#### How do I upgrade system memory?

You can change the amount of installed RAM on the NI PXI-8104 by upgrading the DDR2 SO-DIMM.

To upgrade the RAM, remove the NI PXI-8104 from the PXI chassis. To optimize both memory capacity and system performance, use the same size and speed memory module in each of the two module slots. The use of different size modules in each slot is supported, but system performance will be slower than using two matched modules. However, two mismatched modules will result in better performance than using a single module.

National Instruments offers the following types of SO-DIMMs for use with the NI PXI-8104 controller.

- PC2-5300 512 MB, 64 MB × 64, CL 5, 1.18 in. max (National Instruments part number 779302-512)
- PC2-5300 1 GB, 128 MB × 64, CL 5, 1.18 in. max (National Instruments part number 779302-1024)



**Note** National Instruments has conducted thermal testing as well as mechanical shock and vibration testing, and has verified that the DDR2 SO-DIMMs we sell work with the NI PXI-8104. We recommend you purchase your DDR2 SO-DIMM modules from National Instruments. Other off-the-shelf DDR2 SO-DIMM modules are not guaranteed to work properly.

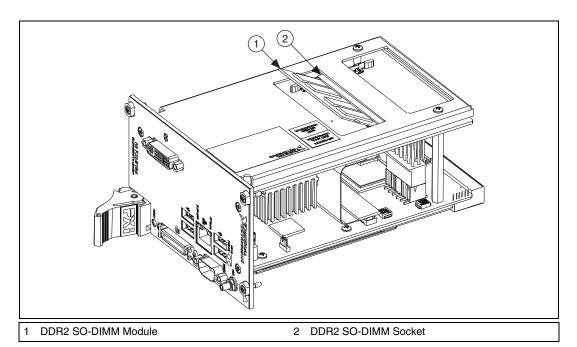


Figure 4-2. Installing a DDR2 SO-DIMM in an NI PXI-8104 Controller

#### How do I flash a new BIOS?

You can download the new BIOS from ftp.ni.com/support/pxi. For more information, refer to KnowledgeBase 3H3COSD8.

#### Where do I get the latest software drivers?

The latest National Instruments software is available from ni.com/downloads. For peripheral drivers, refer to KnowledgeBase 3H3COSD8 at ni.com.

### My NI PXI-8104 does not have an internal floppy drive. Is there a way to use an external drive?

Yes. The NI PXI-8104 controller supports and can boot from USB floppy drives. A USB floppy drive will not work with Windows NT4, but will work with Windows 2000 or Windows XP. Refer to the *Boot Options* section for more information.

A USB floppy drive is available from National Instruments, part number 778492-02.

### **PXI Configuration**

#### How do I use the SMB trigger on the front panel?

For details, refer to the PXI Features section of Chapter 2, Installation and Configuration.

#### Why doesn't the NI PXI-8104 work with the PXI-8220 or PXI-8221?

A serialized IRQ conflict with the PXI-8220/8221 and the NI PXI-8104 prevents PC cards using ISA interrupts from working with the NI PXI-8104 controller. For more information, refer to KnowledgeBase 2G3ED80Z at ni.com/support.

## **Troubleshooting**

This chapter answers common troubleshooting questions you may have when using the NI PXI-8104 embedded computer.

#### What if the NI PXI-8104 does not boot?

Several problems can cause a controller not to boot. Here are some things to look for and possible solutions.

#### Things to Notice:

- Which LEDs come on? The **Power OK** LED should stay lit. The **Drive** LED should blink during boot as the disk is accessed.
- Was the display installed prior to power-on? What appears on the display? Does it hang at some particular point (BIOS, Operating System, and so on)? If nothing appears on the screen, try a different monitor. Does your monitor work with a different PC? If it hangs, note the last screen output that you saw for reference when consulting National Instruments technical support.
- What has changed about the system? Did you recently move the system? Was there electrical storm activity? Did you recently add a new module, memory chip, or piece of software?

#### Things to Try:

- Make sure the chassis is plugged in to a working power source.
- Check any fuses or circuit breakers in the chassis or other power supply (possibly an uninterruptible power supply).
- Make sure the controller module is firmly seated in the chassis.
- Remove all other modules from the chassis.
- Remove any nonessential cables or devices.
- Try the controller in a different chassis.
- Try a similar controller in this same chassis.
- Clear the CMOS. (Refer to the *System CMOS* section of Chapter 2, *Installation and Configuration*.)
- Recover the hard drive on the controller. (Refer to the *Hard Drive Recovery* section of Chapter 2, *Installation and Configuration*.)

My controller boots fine until I get to Windows, at which point I cannot read the screen. This may include garbled output, white screen, black screen, or an out of synch message from the monitor.

This problem usually results from having the video card output set past the limits of the monitor. You will need to boot Windows in Safe Mode. To do this, reboot the controller. As Windows begins to boot, hold down <F8>. You should now be able to reset the video driver to lower settings. Try setting the resolution to  $640 \times 480$  and the refresh rate to 60 Hz. Once you reboot, you can raise these values again, using the test option in Windows. These settings are accessible through the **Advanced** tab of the **Display** item in the **Control Panel**. Alternately, you can try a different monitor, preferably a newer and larger one.

If the system has been booted to Windows without a monitor attached, the driver may have defaulted to the video output connector being disabled. Press <Ctrl-Alt-F1> to re-enable the video display in Windows. Press <Ctrl-Alt-F4> to re-enable a DVI display. For more information, refer to KnowledgeBase 3OHCFRD8 at ni.com/support.

### My system boots fine as long as a particular module is not in my chassis.

The most common cause of this is a damaged module. Try the module in a different chassis or with a different controller. Also, remove any external cables or terminal blocks connected to the system. If the module does not work in these cases, it is likely damaged. Contact the module manufacturer for further troubleshooting.

Refer to the KnowledgeBase or product manuals section at ni.com for more information specific to the chassis and controller with which you are having difficulties.

#### My CMOS is corrupted. How do I set it back to default?

- 1. Enter the BIOS setup program as described in the *Entering BIOS Setup* section of Chapter 2, *Installation and Configuration*.
- 2. Check the battery utility.
- 3. Press <F9> to load BIOS defaults.
- 4. Answer **Y** (Yes) to the verification prompt.
- 5. Select **Save and Exit Setup**.

As an alternative method, complete the following steps:

- 1. Power off the chassis.
- 2. Remove the controller from the chassis.
- 3. Move the jumper on W7 from pins 1–2 to pins 2–3 as shown in Figure 5-1.
- 4. Wait one second. Move the jumper back to pins 1–2.
- 5. Reinstall the controller in the chassis.



**Caution** Do *not* leave the jumper on pins 2–3. Doing so decreases battery life. Also, the controller will not boot.

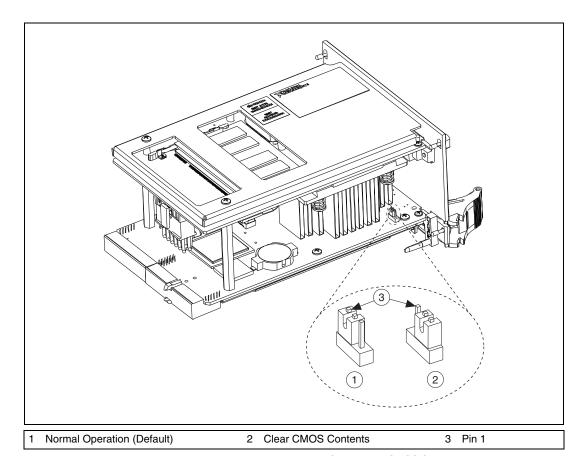


Figure 5-1. Clearing the CMOS Contents



# **Specifications**

This appendix lists the electrical, mechanical, and environmental specifications of the NI PXI-8104 embedded computer.

NI PXI-8104		
СРИ	Intel® Celeron® M 440 (1.86 GHz single-core processor)	
On-die L2 cache	1024 KB	
Dual-Channel DDR2 RAM	1 512 MB (533 MHz) Module Standard, 2 GB Maximum (2 slots available)	
Hard Drive	60 GB (SATA), minimum	
Ethernet	10/100/1000 BaseTX	
Video	DVI-I	
GPIB (IEEE 488.2 Interface)	No	
Serial Ports (RS-232)	Yes (1)	
Parallel Port	Yes (1)	
Hi-Speed USB (2.0) Ports	Yes (4)	
ExpressCard/34 Slot	No	
PS/2 Keyboard/Mouse Connector	No	
PXI Trigger Bus Input/Output	Yes	
Installed Operating System	Windows Vista Business downgraded to Windows XP Professional	

### **Electrical**

	Curre	ent (A)
Voltage (V)	Typical	Maximum
+3.3	2.5 A	3.25 A
+5	3.5 A	5.5 A
+12	5 mA	5 mA
-12	0 A	0 A

### **Physical**

Board dimensions	.PXI 3U-size module 8.1 cm × 13 cm × 21.6 cm (3.2 in. × 5.1 in. × 8.5 in.)
Slot requirements	One system slot plus three controller expansion slots
Compatibility	.Fully compatible with PXI specification
Weight	.0.94 kg (2.1 lb) typical

#### **Environment**

Maximum altitude	2,000 m (at 25 °C ambient
	temperature)
Pollution Degree	2
Indoor use only.	

#### **Operating Environment**

NI PXI-8104

Ambient temperature range ............ 5 to 50 °C in an NI PXI-1042 chassis

(Tested in accordance with IEC-60068-2-1 and IEC-60068-2-2). Refer to the datasheet linked from the NI PXI-8104 Embedded Controller product page at ni. com for other

NI PXI-8104 Extended Temp. Option

Ambient temperature range ...... 0 to 55 °C

(Tested in accordance with IEC-60068-2-1 and IEC-60068-2-2)

controller/chassis combinations.

IEC-60068-2-56.)



**Caution** Clean the NI PXI-8104 with a soft nonmetallic brush. Make sure that the device is completely dry and free from contaminants before powering-on the controller again.

#### Storage Environment

NI PXI-8104

Ambient temperature range ......—40 to 65 °C (Tested in accordance with IEC-60068-2-1 and IEC-60068-2-2.)

NI PXI-8104 Extended Temp. Option

Ambient temperature range ......-40 to 70 °C

(Tested in accordance with IEC-60068-2-1 and IEC-60068-2-2.)

#### **Shock and Vibration**

Operational shock	30 g peak, half-sine, 11 ms pulse
	(Tested in accordance with
	IEC-60068-2-27. Test profile
	developed in accordance with
	MIL-PRF-28800F.)

#### Ra

	MIL-PRF-28800F.)
andom vibration	
Operating	5 to 500 Hz, 0.3 g <sub>rms</sub> (with solid-state hard drive)
Nonoperating	



**Note** Specifications are subject to change without notice.

#### Safety

This product is designed to meet the requirements of the following standards of safety for electrical equipment for measurement, control, and laboratory use:

- IEC 61010-1, EN 61010-1
- UL 61010-1, CSA 61010-1



**Note** For UL and other safety certifications, refer to the product label or the *Online* Product Certification section.

#### **Electromagnetic Compatibility**

This product is designed to meet the requirements of the following standards of EMC for electrical equipment for measurement, control, and laboratory use:

- EN 61326 (IEC 61326): Class A emissions; Basic immunity
- EN 55011 (CISPR 11): Group 1, Class A emissions
- AS/NZS CISPR 11: Group 1, Class A emissions
- FCC 47 CFR Part 15B: Class A emissions
- ICES-001: Class A emissions



**Note** For the standards applied to assess the EMC of this product, refer to the *Online Product Certification* section.



**Note** For EMC compliance, operate this device with shielded cabling.

### CE Compliance (€

This product meets the essential requirements of applicable European Directives as follows:

- 2006/95/EC; Low-Voltage Directive (safety)
- 2004/108/EC; Electromagnetic Compatibility Directive (EMC)

#### **Online Product Certification**

Refer to the product Declaration of Conformity (DoC) for additional regulatory compliance information. To obtain product certifications and the DoC for this product, visit ni.com/certification, search by model number or product line, and click the appropriate link in the Certification column.

### **Environmental Management**

National Instruments is committed to designing and manufacturing products in an environmentally responsible manner. NI recognizes that eliminating certain hazardous substances from our products is beneficial not only to the environment but also to NI customers.

For additional environmental information, refer to the *NI and the Environment* Web page at ni.com/environment. This page contains the environmental regulations and directives with which NI complies, as well as other environmental information not included in this document.

#### **Waste Electrical and Electronic Equipment (WEEE)**



**EU Customers** At the end of their life cycle, all products *must* be sent to a WEEE recycling center. For more information about WEEE recycling centers and National Instruments WEEE initiatives, visit ni.com/environment/weee.htm.

#### 电子信息产品污染控制管理办法 (中国 RoHS)



中国客户 National Instruments 符合中国电子信息产品中限制使用某些有害物质指令 (RoHS)。 关于 National Instruments 中国 RoHS 合规性信息,请登录 ni.com/environment/rohs\_china。 (For information about China RoHS compliance, go to ni.com/environment/rohs\_china.)

### **Battery Replacement and Disposal**

This device contains a long-life coin cell battery. If you need to replace it, use the Return Material Authorization (RMA) process or contact an authorized National Instruments service representative.

After replacement, recycle the old battery. For additional information, visit ni.com/environment.



# Technical Support and Professional Services

Visit the following sections of the award-winning National Instruments Web site at ni.com for technical support and professional services:

- **Support**—Technical support at ni.com/support includes the following resources:
  - Self-Help Technical Resources—For answers and solutions, visit ni.com/support for software drivers and updates, a searchable KnowledgeBase, product manuals, step-by-step troubleshooting wizards, thousands of example programs, tutorials, application notes, instrument drivers, and so on.
     Registered users also receive access to the NI Discussion Forums at ni.com/forums. NI Applications Engineers make sure every question submitted online receives an answer.
  - Standard Service Program Membership—This program
    entitles members to direct access to NI Applications Engineers
    via phone and email for one-to-one technical support as well as
    exclusive access to on demand training modules via the Services
    Resource Center. NI offers complementary membership for a full
    year after purchase, after which you may renew to continue your
    benefits.

For information about other technical support options in your area, visit ni.com/services, or contact your local office at ni.com/contact.

- Training and Certification—Visit ni.com/training for self-paced training, eLearning virtual classrooms, interactive CDs, and Certification program information. You also can register for instructor-led, hands-on courses at locations around the world.
- System Integration—If you have time constraints, limited in-house technical resources, or other project challenges, National Instruments Alliance Partner members can help. To learn more, call your local NI office or visit ni.com/alliance.

- **Declaration of Conformity (DoC)**—A DoC is our claim of compliance with the Council of the European Communities using the manufacturer's declaration of conformity. This system affords the user protection for electromagnetic compatibility (EMC) and product safety. You can obtain the DoC for your product by visiting ni.com/certification.
- Calibration Certificate—If your product supports calibration, you can obtain the calibration certificate for your product at ni.com/calibration.

If you searched ni.com and could not find the answers you need, contact your local office or NI corporate headquarters. Phone numbers for our worldwide offices are listed at the front of this manual. You also can visit the Worldwide Offices section of ni.com/niglobal to access the branch office Web sites, which provide up-to-date contact information, support phone numbers, email addresses, and current events.

# **Glossary**

Symbol	Prefix	Value
n	nano	10-9
μ	micro	10-6
m	milli	10-3
k	kilo	103
M	mega	106
G	giga	109
T	tera	1012

### **Symbols**

° Degrees.

 $\Omega$  Ohms.

% Percent.

A

A Amperes.

AC Alternating Current.

ASIC Application-specific integrated circuit.

В

B Bytes.

backplane An assembly, typically a printed circuit board, with connectors and signal

paths that bus the connector pins.

BIOS Basic Input/Output System—BIOS functions are the fundamental level

of any PC or compatible computer. BIOS functions embody the basic operations needed for successful use of the computer's hardware resources.

C

C Celsius.

cache Small portion of high-speed memory used for temporary storage of

frequently used data.

CMOS Complementary Metal Oxide Semiconductor—A type of integrated circuit.

CompactPCI An adaptation of the PCI specification for industrial and/or embedded

applications that require a more robust mechanical form factor than desktop PCI. CompactPCI provides a standard form factor for those applications requiring the high performance of PCI as well as the small size and

ruggedness of a rack-mount system.

Controller An embedded computer module which configures and accesses a series of

devices connected to a chassis backplane.

D

DC Direct Current.

DDR2 Double Data Rate, 2<sup>nd</sup> generation.

DIMM Dual In-line Memory Module.

DMA Direct Memory Access—A method by which data is transferred between

devices and internal memory without intervention of the central processing

unit.

DRAM Dynamic RAM (Random Access Memory)—Storage that the computer

must refresh at frequent intervals.

DVI-I Direct Video Interface, Integrated—A video technology enabling the use of

both analog and digital video signals.

E

ECP Extended Capabilities Parallel.

EEPROM Electronically Erasable Programmable Read Only Memory.

EMC Electromagnetic Compatibility.

EMI Electromagnetic interference.

EPP Enhanced Parallel Port.

expansion ROM An onboard EEPROM that may contain device-specific initialization and

system boot functionality.

F

FCC Federal Communications Commission.

G

g 1. Grams.

2. A measure of acceleration equal to 9.8 m/s<sup>2</sup>.

g<sub>mms</sub> A measure of random vibration—The root mean square of acceleration

levels in a random vibration test profile.

H

Hz Hertz—Cycles per second.

ı

I/O Input/output—The techniques, media, and devices used to achieve

communication between machines and users.

IDE Integrated Drive Electronics—Hard disk and built-in controller.

IEEE Institute of Electrical and Electronics Engineers.

in. Inches.

instrument driver A set of routines designed to control a specific instrument or family of

instruments, and any necessary related files for LabWindows/CVI or

LabVIEW.

interrupt A means for a device to request service from another device.

interrupt level The relative priority at which a device can interrupt.

IRQ# Interrupt request signal.

ISA Industry Standard Architecture—The original PC bus architecture,

specifically the 16-bit AT bus.

K

kB Kilobytes of memory.

L

LAN Local Area Network—Communications network that serves users within

a confined geographical area. It is made up of servers, workstations,

a network operating system, and a communications link.

LED Light-emitting diode.

M

m Meters.

master A functional part of a PXI device that initiates data transfers on the

PXI backplane. A transfer can be either a read or a write.

MB Megabytes of memory.

MTBF Mean time between failure.

MTTR Mean time to repair.

N

NI-488 or NI-488.2 The National Instruments software for GPIB systems.

NI-DAQ The National Instruments software for data acquisition instruments.

NI-VISA The National Instruments implementation of the VISA standard—An

interface-independent software that provides a unified programming

interface for VXI, GPIB, and serial instruments.

NMI Non-maskable interrupt—High-priority interrupt that cannot be disabled.

It is used to report malfunctions such as parity, bus and math coprocessor

errors.

P

PCI Peripheral Component Interconnect—The PCI bus is a high-performance

32-bit or 64-bit bus with multiplexed address and data lines.

PCI Express Peripheral Component Interconnect Express—A faster, serialized version

of the PCI bus.

PCMCIA Personal Computer Memory Card International Association.

peripheral Any hardware device connected to a computer, such as a monitor,

keyboard, printer, plotter, disk or tape drive, graphics tablet, scanner,

mouse, and so on.

POSC Power On Self Configuration.

PXI PCI eXtensions for Instrumentation—An open implementation of

CompactPCI that adds electrical features that meet the high-performance requirements of instrumentation applications by providing triggering, local buses, and system clock capabilities. PXI also offers two-way

interoperability with CompactPCI products.

R

RAM Random Access Memory—the computer's primary workspace.

RAMDAC Random Access Memory Digital to Analog Converter—the VGA

controller chip that maintains the color palette and converts data from

memory into analog signals for the monitor.

resource Hardware settings used by devices in a computer system, including

ISA interrupt level, DMA channel, and I/O address.

RMS Root mean squared. See also g<sub>rms</sub>.

RTC Real Time Clock—An electronic circuit that maintains the time of day and

also can provide timing signals for timesharing operations.

S

s Seconds.

slave A functional part of a PXI device that detects data transfer cycles initiated

by a PXI bus master and responds to the transfers when the address

specifies one of the device's registers.

SO-DIMM Small Outline Dual In-line Memory Module.

SRAM Static RAM—A memory chip that requires power to hold its content. It

does not require refresh circuitry as a dynamic RAM chip, but it does take

up more space and uses more power.

U

USB Universal Serial Bus.

V

V Volts.

VGA Video Graphics Array—The minimum video display standard for all PCs.

W

W Watts.

## Index

A	COM1 connector
Advanced Setup menu, 2-6	connector locations and pinout
ATA-100 IDE logic block, 1-4	(figure), 3-5
,	connector signals (table), 3-5
_	common configuration questions
В	boot options, 4-1
battery replacement and disposal	cables and connections, 4-2
information, A-6	chassis configuration, 2-17
BIOS	figure, 2-18
checking settings, 4-1	driver installation, 4-3
flashing new BIOS, 4-5	general questions, 4-1
setup	PXI configuration, 4-6
Advanced Setup menu, 2-6	upgrade information, 4-4
Boot Setup menu, 2-10	CompactPCI specification, 1-1
entering, 2-5	configuration, common questions
Exiting BIOS Setup menu, 2-11	boot options, 4-1
Integrated Peripherals menu, 2-7	cables and connections, 4-2
Main Setup menu, 2-6	chassis configuration, 2-17
PXI Setup menu, 2-8	figure, 2-18
Per-Slot Device Settings, 2-9	driver installation, 4-3
boot options, configuring controller, 4-2	general questions, 4-1
Boot Setup menu, 2-10	PXI configuration, 4-6
1	upgrade information, 4-4
	connectors
C	COM1 connector and signals, 3-5
calibration certificate (NI resources), B-2	DVI-I connector and signals, 3-3
CD-ROM drive, installing software from, 4-3	Ethernet connector and signals, 3-6
CE compliance, specifications, A-5	parallel port connector and signals, 3-7
chipset	peripheral expansion overview
945GMCH, 1-4	(table), 3-1
ICH7M, 1-4	trigger connector and signals, 3-10
CMOS	Universal Serial Bus (USB) connector
clearing contents (figure), 2-12, 5-3	and signals, 3-9
setting back to default, 5-2	conventions used in the manual, vii

D	examples (NI resources), B-1
data storage, 3-11	Exiting BIOS Setup menu, 2-11
DDR SO-DIMMs	
installing, 2-19, 4-4	F
figure, 2-20, 4-5	-
DDR2 SO-DIMMs	files and directories installed on hard drive, 2-16
from National Instruments (note), 2-20, 4-4	floppy drive, using external floppy drive, 4-5 front panel
Declaration of Conformity (NI resources), B-2 diagnostic tools (NI resources), B-1 directories and files installed on hard drive, 2-16 documentation	connectors, 3-1 dimensions, 3-2 features, 3-11 functional overview of NI PXI-8104, 1-2
conventions used in manual, vii	
how to use this documentation set, <i>vii</i> NI resources, B-1 related documentation, <i>viii</i> DRIVE LED, 3-11	H hard drive, 3-11 files and directories installed on, 2-16
drivers, 2-16	recovery, 2-20
installation	help, technical support, B-1
video, 4-3	
NI resources, B-1	1
obtaining latest drivers, 4-5	IDE
DVI-I	IDE controller, using SCSI hard drive in addition, 4-1
connector signals (table), 3-3	images directory, 2-16
location and pinout (figure), 3-3 overview (table), 3-1	installation  See also configuration
E	injector/ejector handle position (caution), 2-2
electrical specifications, A-2	NI PXI-8104 installed in a PXI chassis
electromagnetic compatibility, A-4	(figure), 2-4
environmental management,	procedure, 2-1
specifications, A-5	removing NI PXI-8104 from PXI chassis, 2-4
battery replacement and disposal, A-6 WEEE information, A-5	removing protective screw caps (figure), 2-2
Ethernet, connector	instrument drivers (NI resources), B-1
location and pinout (figure), 3-6 signals (table), 3-6	Integrated Peripherals menu, 2-7

K	connectors, 3-1
keyboard, plugging PS/2 mouse and keyboard	COM1 connector and signals, 3-5
into controller, 4-2	DVI-I connector and signals
KnowledgeBase, B-1	(table), 3-3
	parallel port connector and
_	signals, 3-7
L	trigger connector and signals, 3-10
LabVIEW, 1-5	Universal Serial Bus (USB)
RT configuration switches, 2-15, 2-16	connector and signals, 3-9
RT installation and configuration, 2-12	data storage, 3-11
RT network settings (figure), 2-14	front panel
RT software installation, 2-12	dimensions, 3-2
LabWindows/CVI, 1-5	features, 3-11
LEDs, front panel LEDs, 3-11, 4-1	LEDs, 4-1
Linux support, 1-6	system reset pushbutton, 3-11
LPT cable, connecting to NI PXI-8104, 4-3	functional overview, 1-2
-	hard drive recovery, 2-20
N/I	installing DDR SO-DIMMs (figure),
M	2-20, 4-5
Main Setup menu, 2-6	installing in a PXI chassis, 2-1
manuals directory, 2-16	figure, 2-4
mating connector	logic blocks, 1-3
parallel port, 3-7	peripheral expansion overview
Measurement Studio, 1-5	(table), 3-1
modular instruments, 1-5	PXI trigger connectivity, 2-17 removing from a PXI chassis, 2-4
mouse, plugging PS/2 mouse and keyboard	software, 1-4
into controller, 4-2	
multichassis configuration in MAX	specifications, A-1
(figure), 2-18	troubleshooting, 5-1
	upgrading RAM, 2-19, 4-4
N	NI support and services, B-1
	NI-DAQmx, 1-5
National Instruments	NI-VISA, 1-6
software, 1-4	
support and services, B-1 NI PXI-8104	0
	operating environment specifications, A-2,
benefits of PXI, 1-1 BIOS setup, 2-5	A-3
block diagram, 1-3	os directory, 2-16
block diagram, 1-3	OS installation from CD-ROM, 2-21
	, -

S

safety, specifications, A-4

See also COM1 connector

serial port, 3-1

P	shock and vibration specifications, A-4
parallel port	socket 479 1 CPU, 1-3
connector location and pinout	SO-DIMM logic block, 1-3
(figure), 3-7	software
connector signals (table), 3-8	See also drivers
overview (table), 3-1	installed on your hard drive, 2-16
PCI bus, standard for desktop computer	installing from CD-ROM, 4-3
designs, 1-1	LabVIEW, 1-5
peripheral expansion overview (table), 3-1	LabWindows/CVI, 1-5
Per-Slot Device Settings menu, 2-9	Measurement Studio, 1-5
physical specifications, A-2	National Instruments software, 1-4
POWER OK LED, 3-11	NI resources, B-1
programming examples (NI resources), B-1	NI-DAQmx, 1-5
protective screw caps, removing (figure), 2-2	NI-VISA, 1-6
PS/2, plugging PS/2 mouse and keyboard into	specifications
controller, 4-2	CE compliance, A-5
PXI	electrical, A-2
connectors, function (logic block), 1-4	electromagnetic compatibility, A-4
features, 2-17	environmental management, A-5
Setup menu, 2-8	battery replacement and
Per-Slot Device Settings, 2-9	disposal, A-6
trigger connectivity, 2-17	WEEE information, A-5
PXI-8220/PXI-8221, using NI PXI-8104	online product certification, A-5
with, 4-6	operating environment, A-2, A-3
pxisys.ini file, 2-17	physical, A-2
	safety, A-4
D	shock and vibration, A-4
R	storage environment, A-3
RAM	super I/O logic block, 1-4
DDR2 SO-DIMMs from National	support, technical, B-1
Instruments (note), 2-20, 4-4	switches
upgrading, 2-19, 4-4	LabVIEW RT configuration (figure), 2-16
recycling hardware, A-5	system CMOS, 2-11
recycling the battery, A-6	system reset pushbutton, 3-11
related documentation, viii	

keyboard, 2-3

T	V
technical support, B-1 training and certification (NI resources), B-1 trigger, 3-10, 4-6	video, 3-1 driver installation, 4-3
connector location and pinout (figure), 3-10 connector signals (table), 3-10 troubleshooting CMOS reset, 5-2	W Web resources, B-1 WEEE information, A-5
controller does not boot, 5-1 damaged module, 5-2 NI resources, B-1 video display, 5-2	Y Y-splitter cable figure, 4-2 using mouse and keyboard without, 4-3 using with PS/2 mouse and keyboard, 2-
Universal Serial Bus (USB), 3-1, 3-9 connector function, 1-4 connector location and pinout (figure), 3-9 connector signals (table), 3-9 overview (table), 3-1	