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Manufacturer: National Instruments

Board Assembly Part Numbers (Refer to Procedure 1 for identification procedure):

Part Number and Revision	Description
189876A-0x or later	**PXI-6551/6552**
190941A-0x or later	**PCI-6551/6552**

Volatile Memory

Target Data	*Type*	*Size*	*Battery Backup*	*User[1] Accessible*	*System Accessible*	*Sanitization Procedure*
Waveform data	DRAM	512 MB	No	Yes	Yes	Cycle Power
PCI communication	CHINCH FIFO	208 bytes	No	Yes	Yes	Cycle Power
Buffered data	FPGA Block RAM (x1)	720 KB	No	Yes	Yes	Cycle Power
Memory control	FPGA Block RAM (x2)	112 KB	No	Yes	Yes	Cycle Power
daughter card data transfer	FPGA Block RAM (x1)	864 KB	No	Yes	Yes	Cycle Power

Non-Volatile Memory (*incl. Media Storage*)

Target Data	*Type*	*Size*	*Battery Backup*	*User Accessible*	*System Accessible*	*Sanitization Procedure*
Device configuration	EEPROM	64 KB	No	No	No	None
Card info	Flash	32 MB	No	No	Yes	None
Program FPGAs	CPLD	64 Macro-cells	No	No	No	None

[1] Refer to *Terms and Definitions* section for clarification of *User* and *System Accessible*

Contact: 866-275-6964
support@ni.com

Procedures

Procedure 1 – Board Assembly Part Number identification:
To determine the Board Assembly Part Number and Revision, refer to the label applied to the surface of your product. The Assembly Part Number should be formatted as "P/N: ######-0#".

Terms and Definitions

Cycle Power:
The process of completely removing power from the device and its components and allowing for adequate discharge. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

Volatile Memory:
Requires power to maintain the stored information. When power is removed from this memory, its contents are lost. This type of memory typically contains application specific data such as capture waveforms.

Non-Volatile Memory:
Power is not required to maintain the stored information. Device retains its contents when power is removed. This type of memory typically contains information necessary to boot, configure, or calibrate the product or may include device power up states.

User Accessible:
The component is read and/or write addressable such that a user can store arbitrary information to the component from the host using a publicly distributed NI tool, such as a Driver API, the System Configuration API, or MAX.

System Accessible:
The component is read and/or write addressable from the host without the need to physically alter the product.

Clearing:
Per *NIST Special Publication 800-88 Revision 1*, "clearing" is a logical technique to sanitize data in all User Accessible storage locations for protection against simple non-invasive data recovery techniques using the same interface available to the user; typically applied through the standard read and write commands to the storage device.

Sanitization:
Per *NIST Special Publication 800-88 Revision 1*, "sanitization" is a process to render access to "Target Data" on the media infeasible for a given level of effort. In this document, clearing is the degree of sanitization described.

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