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**PXI-8108**

**Board Assembly Part Number(s)**

| Part Number  | Description |
|--|-------------|
| 196575E-***L and all previous letter revisions and permutations of '***' | NI PXI-8101 |
| 196576E-***L and all previous letter revisions and permutations of '***' | NI PXI-8102 |
| 198410K-***L and all previous letter revisions and permutations of '***' | NI PXI-8108 |
| 193493H-***L and all previous letter revisions and permutations of '***' | NI PXI-8110 |

**Manufacturer:** National Instruments

**Volatile Memory**

| Type <sup>1</sup> | Size  | User Accessible/<br>System Accessible <sup>2</sup> | Battery<br>Backup? | Purpose           | Method of Clearing <sup>3</sup> |
|-------------------|-------|--|--------------------|-------------------|---------------------------------|
| DDR2              | 2+ GB | Yes/Yes  | No                 | Controller RAM    | Cycle Power                     |
| CMOS              | 256 B | Yes/Yes  | Yes                | ICH9 Chipset CMOS | Remove CMOS battery             |

**Non-Volatile Memory**

| Type   | Size           | User Accessible/<br>System Accessible | Battery<br>Backup? | Purpose                                       | Method of Clearing     |
|--------|----------------|---------------------------------------|--------------------|---|------------------------|
| EEPROM | 1 Kbit         | No/No                                 | No                 | GPIB configuration                            | Utility <sup>4</sup>   |
| CPLD   | 512 MacroCells | No/No                                 | No                 | Power Management/<br>Trigger routing/watchdog | None Available to User |
| Flash  | (x2) 16 Mbit   | Yes/No                                | No                 | BIOS Configuration                            | Factory reset in BIOS  |

**Media Storage**

| Type       | Size   | User Accessible/<br>System Accessible | Battery<br>Backup? | Purpose            | Method of Clearing                  |
|------------|--------|---------------------------------------|--------------------|--------------------|-------------------------------------|
| Hard Drive | 80+ GB | Yes/Yes                               | No                 | Primary Disk Drive | Remove from controller <sup>5</sup> |

<sup>1</sup> Calibration constants that are stored in device EEPROMs include information for the device's full operating range. Calibration constants do not maintain any unique data for specific configurations at which the device is used unless otherwise specified.

<sup>2</sup> Items are designated **No** for the following reason(s):

- a) Hardware changes or a unique software tool from National Instruments are required to modify contents of the memory listed.
- b) Hardware-modifying software tools are not distributed to customers for any personal access or customization, also known as non-normal use.

<sup>3</sup> The designation *None Available to User* indicates that the ability to clear this memory is not available to the user under normal operation. The utilities required to clear the memory are not distributed by National Instruments to customers for normal use.

<sup>4</sup> Utility for clearing available through National Instruments by request.

<sup>5</sup> Since a hard drive cannot be cleared, to declassify a system containing a PXI embedded controller, the controller's hard drive must be removed as part of the declassification procedure. This can be done by removing the controller from the system or removing the hard drive from the controller during declassification. Alternatively, the hard drive can be permanently removed from the controller and a CompactPCI (cPCI) hard drive carrier/interface can be used to provide an easily-removable, bootable hard drive.

**Terms and Definitions**

**User Accessible** Allows the user to directly write or modify the contents of the memory during normal instrument operation.

**System Accessible** Does not allow the user to access or modify the memory during normal instrument operation. However, system accessible memory may be accessed or modified by background processes. This can be something that is not deliberate by the user and can be a background driver implementation, such as storing application information in RAM to increase speed of use.

**Cycle Power** The process of completely removing power from the device and its components. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

**Volatile Memory** Requires power to maintain the stored information. When power is removed from this memory, its contents are lost.

**Non-Volatile** Retains its contents when power is removed. This type of memory typically contains calibration or chip configuration information, such as power up states.