Board Assembly Part Number(s)

Part Number	Description
194923F-01L	**PXI-8512/1**
194923E-02L	**PXI-8512/2**
198980E-01L	**PCI-8512/1**
198980E-02L	**PCI-8512/2**
194923F-41L	**PXI-8513/1**
194923E-42L	**PXI-8513/2**
198980E-41L	**PCI-8513/1**
198980E-42L	**PCI-8513/2**
194923F-21L	**PXI-8511/1**
194923E-22L	**PXI-8511/2**
198980E-21L	**PCI-8511/1**
198980E-22L	**PCI-8511/2**
194923F-81L	**PXI-8531**
198980E-81L	**PCI-8531**
194923F-11L	**PXI-8532**
198980E-11L	**PCI-8532**

Manufacturer: National Instruments

Volatile Memory

Type	Size	User Accessible/ System Accessible	Battery Backup?	Purpose	Method of Clearing
SRAM 256MB	*4MB/Port*	*No/Yes*	*No*	*Data buffering*	*Cycle Power*
FPGA Block Memory	*92 Kbit*	*No/Yes*	*No*	*Data Buffering*	*Cycle Power*

Non-Volatile Memory

Type	Size	User Accessible/ System Accessible	Battery Backup?	Purpose	Method of Clearing
Flash	*16 Mbit*	*No/Yes*	*No*	*Configuration, stores FPGA bit file and programmable power-up states*	*None available to user*

Media Storage

Type	Size	User Accessible/ System Accessible	Battery Backup?	Purpose	Method of Clearing
NONE					

Contact: 866-275-6964
support@ni.com

Terms and Definitions

User Accessible Allows the user to directly write or modify the contents of the memory during normal instrument operation.

System Accessible Does not allow the user to access or modify the memory during normal instrument operation. However, system accessible memory may be accessed or modified by background processes. This can be something that is not deliberate by the user and can be a background driver implementation, such as storing application information in RAM to increase speed of use.

Cycle Power The process of completely removing power from the device and its components. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

Volatile Memory Requires power to maintain the stored information. When power is removed from this memory, its contents are lost.

Non-Volatile Retains its contents when power is removed. This type of memory typically contains calibration or chip configuration information, such as power up states.

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