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PXIe-4468



Manufacturer: NI

Board Assembly Part Numbers (Refer to Procedure 1 for identification procedure):

Part Number and Revision	Description
131136B-21L or later	MODULE ASSEMBLY, PXIE-4468, MXLR
131136B-22L or later	MODULE ASSEMBLY, PXIE-4468, BNC

Volatile Memory

<i>Target Data</i>	<i>Type</i>	<i>Size</i>	<i>Battery Backup</i>	<i>User¹ Accessible</i>	<i>System Accessible</i>	<i>Sanitization Procedure</i>
Digital logic and board control	FPGA	Xilinx XC7A200T	No	Yes	Yes	Cycle Power

Non-Volatile Memory (*incl. Media Storage*)

<i>Target Data</i>	<i>Type</i>	<i>Size</i>	<i>Battery Backup</i>	<i>User Accessible</i>	<i>System Accessible</i>	<i>Sanitization Procedure</i>
Device configuration	Flash	16 MB	No			
<ul style="list-style-type: none"> • Device information • FPGA bitstream • Calibration metadata • Calibration data² 				No No Yes No	Yes Yes Yes Yes	None None Procedure 2 None
Analog Configuration	CPLD	Lattice LCMXO2- 2000HC	No	No	No	None

¹ Refer to *Terms and Definitions* section for clarification of *User* and *System Accessible*

² Calibration constants that are stored on the device include information for the device's full operating range. Any implications resulting from partial self-calibration can be eliminated by running the full self-calibration procedure.



Procedures

Procedure 1 – Board Assembly Part Number identification:

To determine the Board Assembly Part Number and Revision, refer to the label applied to the surface of your product. The Assembly Part Number should be formatted as “P/N: 131136a-2#L” where “a” is the letter revision of the assembly (e.g. A, B, C...) and “#” is the number that identifies the model from the Board Assembly Part Number table.

Procedure 2 – Device Configuration Flash (Calibration Metadata):

Requirements: LabVIEW 2022 or later, and NI-DAQmx version 22.0 or later that supports this model.

The user-accessible areas of the Device Configuration Flash can be cleared using the NI DAQmx API. For instruction on how to clear these areas, go to www.ni.com/info and enter info code *DAQmxLOV*.



Terms and Definitions

Cycle Power:

The process of completely removing power from the device and its components and allowing for adequate discharge. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

Volatile Memory:

Requires power to maintain the stored information. When power is removed from this memory, its contents are lost. This type of memory typically contains application specific data such as capture waveforms.

Non-Volatile Memory:

Power is not required to maintain the stored information. Device retains its contents when power is removed. This type of memory typically contains information necessary to boot, configure, or calibrate the product or may include device power up states.

User Accessible:

The component is read and/or write addressable such that a user can store arbitrary information to the component from the host using a publicly distributed NI tool, such as a Driver API, the System Configuration API, or MAX.

System Accessible:

The component is read and/or write addressable from the host without the need to physically alter the product.

Clearing:

Per *NIST Special Publication 800-88 Revision 1*, “clearing” is a logical technique to sanitize data in all User Accessible storage locations for protection against simple non-invasive data recovery techniques using the same interface available to the user; typically applied through the standard read and write commands to the storage device.

Sanitization:

Per *NIST Special Publication 800-88 Revision 1*, “sanitization” is a process to render access to “Target Data” on the media infeasible for a given level of effort. In this document, clearing is the degree of sanitization described.