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Manufacturer: National Instruments

Board Assembly Part Number(s):

Part Number and Revision	Description
158215B-01L or later	**PXIe-4480**
158215B-02L or later	**PXIe-4481**

The part number and revision for these devices is printed with the serial number on the circuit board on the back side of the product near the backplane connector.

Volatile Memory

Purpose ("Target Data")	*Type*	*Size*	*Battery Backup*	*User[1] Accessible*	*System Accessible*	*Clearing Procedure*
Digital Logic/Board Control	FPGA – Xilinx Artix 7	75T	No	Yes	Yes	Cycle Power
PCIe Bus Interface	STC3 ASIC	N/A	No	Yes	Yes	Cycle Power

Non-Volatile Memory (*incl. Media Storage*)

Purpose ("Target Data")	*Type*	*Size*	*Battery Backup*	*User Accessible*	*System Accessible*	*Clearing Procedure*
Device configuration	Flash	8 MB	No			
• Device information				No	Yes	None
• Calibration metadata				Yes	Yes	Cal. Data
• Calibration data[2]				No	Yes	None
TEDS/IEPE Configuration	CPLD – Lattice MachX02	X02-2000	No	No	No	None
Analog Configuration	CPLD – Lattice MachX02	X02-1200	No	No	No	None

Clearing Procedures

Calibration Data – Device Configuration Flash (Calibration Metadata):

The only section of the EEPROM modifiable by users is the calibration area. To clear this area, do one of the following:

- Overwrite the existing User-Defined Information TWICE IN A ROW with non-sensitive information using the User-Defined Information property in the NI-DAQmx Calibration API.
- Overwrite the existing Calibration Password TWICE IN A ROW with a non-sensitive password using the DAQmx Change External Calibration Password function in the NI-DAQmx Calibration API.

[1] Refer to *Terms and Definitions* section for clarification of *User* and *System Accessible*
[2] Calibration constants that are stored on the device include information for the device's full operating range.

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support@ni.com

Terms and Definitions

Cycle Power:
The process of completely removing power from the device and its components and allowing for adequate discharge. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

Volatile Memory:
Requires power to maintain the stored information. When power is removed from this memory, its contents are lost. This type of memory typically contains application specific data such as capture waveforms.

Non-Volatile Memory:
Power is not required to maintain the stored information. Device retains its contents when power is removed. This type of memory typically contains information necessary to boot, configure, or calibrate the product or may include device power up states.

User Accessible:
The component is read/write addressable such that a user can store arbitrary information to the component from the host using a publicly distributed NI tool, such as a Driver, the System Configuration, or NI Measurement and Automation Explorer (MAX).

System Accessible:
The component is read/write addressable from the host or through other, unaltered NI components of the system.

Clearing: Per NIST Special Publication 800-88 Revision 1
Logical techniques to sanitize data in all User Accessible storage locations for protection against simple non-invasive data recovery techniques using the same interface available to the user; typically applied through the standard read and write commands to the storage device.

Sanitize: Per NIST Special Publication 800-88 Revision 1
A process to render access to “Target Data” on the media infeasible for a given level of effort.

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