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PXIe-6555

GETTING STARTED GUIDE

NI PXIe-6555/6556

200 MHz Digital Waveform Generator/Analyzer with PPMU

Note Before you begin, install and configure your chassis and controller.

This document explains how to install, configure, and test the NI PXIe-6555/6556
(NI 6555/6556). The NI 6555/6556 is a 200 MHz digital waveform generator/analyzer with
per-pin parametric unit (PPMU).

To access NI 6555/6556 documentation, go to **Start**»**All Programs**»**National Instruments**»
NI-HSDIO»**Documentation**.

Note The protection provided by the NI 6555/6556 may be impaired if it is used in
a manner not described in this document.

Hot Surface If the NI 6555/6556 has been in use, the device may exceed safe
handling temperatures and may cause discomfort. Allow the NI 6555/6556 to cool
before touching the device or removing the device from the chassis.

Contents

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Electromagnetic Compatibility Guidelines

This product was tested and complies with the regulatory requirements and limits for electromagnetic compatibility (EMC) stated in the product specifications. These requirements and limits provide reasonable protection against harmful interference when the product is operated in the intended operational electromagnetic environment.

This product is intended for use in industrial locations. However, harmful interference may occur in some installations, when the product is connected to a peripheral device or test object, or if the product is used in residential or commercial areas. To minimize interference with radio and television reception and prevent unacceptable performance degradation, install and use this product in strict accordance with the instructions in the product documentation.

Furthermore, any changes or modifications to the product not expressly approved by National Instruments could void your authority to operate it under your local regulatory rules.

Caution To ensure the specified EMC performance, operate this product only with shielded cables and accessories. Do not use unshielded cables or accessories unless they are installed in a shielded enclosure with properly designed and shielded input/output ports and connected to the product using a shielded cable. If unshielded cables or accessories are not properly installed and shielded, the EMC specifications for the product are no longer guaranteed.

Note To ensure the specified EMC performance, the length of all I/O cables must be no longer than 3 m (10 ft).

Note To ensure the specified EMC performance, you must install PXI EMC Filler Panels, National Instruments part number 778700-01, in all open chassis slots.

Verifying the System Requirements

To use the NI-HSDIO instrument driver, your system must meet certain requirements.

Refer to the product readme, which is available on the driver software media or online at *ni.com/manuals*, for more information about minimum system requirements, recommended system, and supported application development environments (ADEs).

Unpacking the Kit

⚠️ **Caution** To prevent electrostatic discharge (ESD) from damaging the device, ground yourself using a grounding strap or by holding a grounded object, such as your computer chassis.

1. Touch the antistatic package to a metal part of the computer chassis.
2. Remove the device from the package and inspect the device for loose components or any other sign of damage.

   ⚠️ **Caution** Never touch the exposed pins of connectors.

   📝 **Note** Do not install a device if it appears damaged in any way.

3. Unpack any other items and documentation from the kit.

Store the device in the antistatic package when the device is not in use.

Kit Contents

The kit contains the following items:

- NI-HSDIO instrument driver software media, which installs the following:
    - NI-HSDIO driver
    - NI Script Editor
    - *NI Digital Waveform Generator/Analyzer Help*
    - *NI PXIe-6555/6556 Getting Started Guide* (this document)
- NI Digital Waveform Editor software media
- NI 6555/6556 device

Preparing the Environment

Ensure that the environment in which you are using the NI 6555/6556 meets the following specifications:

PXI Express Modules

Operating Environment

    Ambient temperature range........................Ambient temperature range 0 °C to 45 °C (Tested in accordance with IEC 60068-2-1 and IEC 60068-2-2. Meets MIL-PRF-28800F Class 3 low temperature limit and MIL-PRF-28800F Class 2 high temperature limit.

    Relative humidity range.............................10% to 90%, noncondensing (Tested in accordance with IEC 60068-2-56.)

Maximum altitude..2,000 m (800 mbar) (at 25 °C ambient temperature)

Pollution Degree...2

Indoor use only.

Installing the Software

You must be an Administrator to install NI software on your computer.

1. Install an ADE, such as LabVIEW or LabWindows™/CVI™.
2. Insert the driver software media into your computer. The installer should open automatically.

   If the installation window does not appear, navigate to the drive, double-click it, and double-click autorun.exe.
3. Follow the instructions in the installation prompts.

   📝 **Note** Windows users may see access and security messages during installation. Accept the prompts to complete the installation.
4. When the installer completes, select **Restart** in the dialog box that prompts you to restart, shut down, or restart later.

NI Script Editor

The NI Script Editor helps you develop linking and looping waveform generation operations.

The NI Script Editor is installed when you install NI-HSDIO. For more information, launch the NI Script Editor and select **Help**»**NI Script Editor Help** from the toolbar.

NI Digital Waveform Editor

The NI Digital Waveform Editor (NI DWE) allows you to create, edit, and import digital test vectors.

Note To use the NI DWE, install it after you install NI-HSDIO.

Installing the NI 6555/6556

Caution To prevent damage to the device caused by ESD or contamination, handle the device using the edges or the metal bracket.

1. Ensure the AC power source is connected to the chassis before installing the modules.

   The AC power cord grounds the chassis and protects it from electrical damage while you install the modules.

2. Power off the chassis.

3. Inspect the slot pins on the chassis backplane for any bends or damage prior to installation. Do not install a module if the backplane is damaged.

4. Remove the black plastic connectors from all the captive screws on the module front panel.

5. Identify the slots you will use in the chassis.

   The NI 6555/6556 must be installed into two adjacent PXI Express-compatible slots. The following figure shows the symbols that indicate the slot types.

Figure 1. Chassis Compatibility Symbols
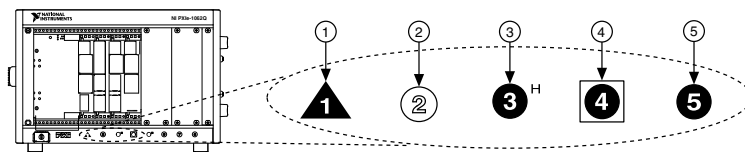
1. PXI Express System Controller Slot
2. PXI Peripheral Slot
3. PXI Express Hybrid Peripheral Slot
4. PXI Express System Timing Slot
5. PXI Express Peripheral Slot

6. Touch any metal part of the chassis to discharge static electricity.

7. Place the module edges into the module guides at the top and bottom of the chassis. Slide the module into the slots until it is fully inserted.

Figure 2. NI 6555/6556 Installation
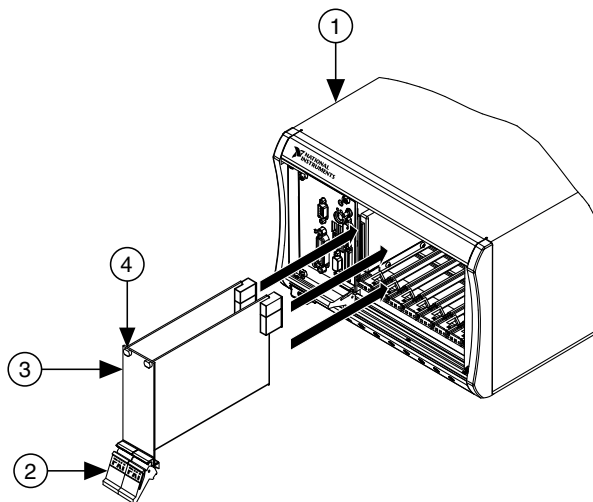
1.	PXI Express Chassis	3.	NI PXI Express Two-Slot Module
2.	Ejector Handles in Down Position	4.	Captive Screw

8. Secure the device front panel to the chassis using the front-panel mounting screws.

> **Note** Tightening the top and bottom mounting screws increases mechanical stability and also electrically connects the front panel to the chassis, which can improve the signal quality and electromagnetic performance.

9. Cover all empty slots using filler panels or slot blockers to maximize cooling air flow.

10. Power on the chassis.

Connecting Signals

NI 6555/6556 Front Panels

The NI 6555/6556 front panels exposes the following connections:

- Three SMA connectors named CLK IN, PFI 0, and CLK OUT
- Two 68-pin VHDCI connectors named DIGITAL DATA & CONTROL (DDC) and REMOTE SENSE
- (NI 6556 only) One COMBICON connector named AUX I/O

> **Note** On the NI 6555, AUX I/O pins are located on the VHDCI connector.

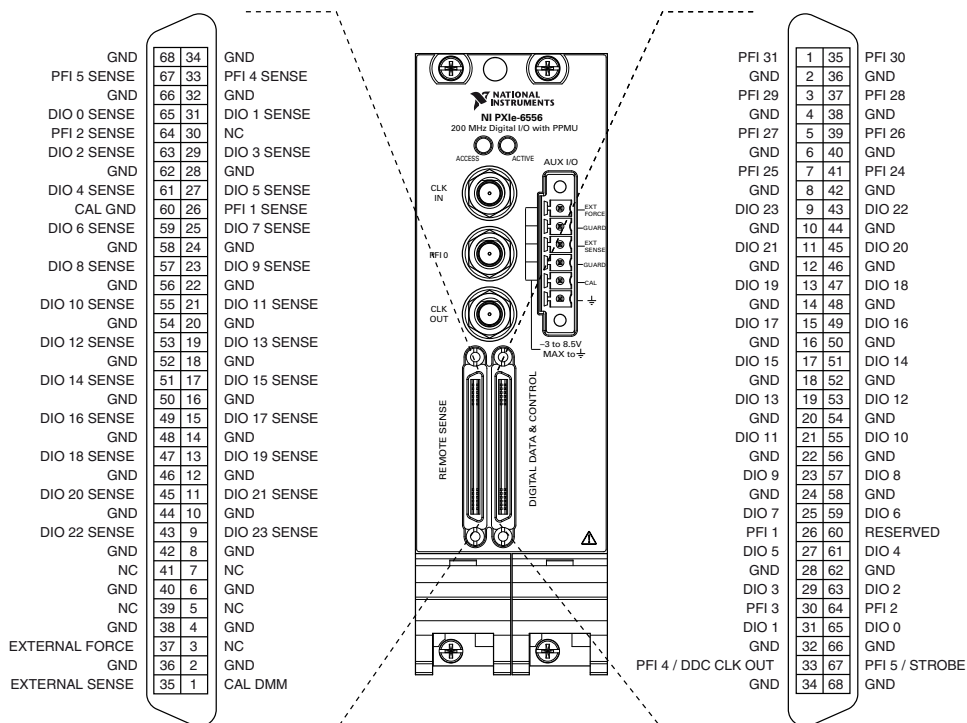**Figure 3.** NI 6555/6556 Front Panel with VHDCI Connector Pinout

Left connector:

Signal	Pin	Pin	Signal
GND	68	34	GND
PFI 5 SENSE	67	33	PFI 4 SENSE
GND	66	32	GND
DIO 0 SENSE	65	31	DIO 1 SENSE
PFI 2 SENSE	64	30	NC
DIO 2 SENSE	63	29	DIO 3 SENSE
GND	62	28	GND
DIO 4 SENSE	61	27	DIO 5 SENSE
CAL GND	60	26	PFI 1 SENSE
DIO 6 SENSE	59	25	DIO 7 SENSE
GND	58	24	GND
DIO 8 SENSE	57	23	DIO 9 SENSE
GND	56	22	GND
DIO 10 SENSE	55	21	DIO 11 SENSE
GND	54	20	GND
DIO 12 SENSE	53	19	DIO 13 SENSE
GND	52	18	GND
DIO 14 SENSE	51	17	DIO 15 SENSE
GND	50	16	GND
DIO 16 SENSE	49	15	DIO 17 SENSE
GND	48	14	GND
DIO 18 SENSE	47	13	DIO 19 SENSE
GND	46	12	GND
DIO 20 SENSE	45	11	DIO 21 SENSE
GND	44	10	GND
DIO 22 SENSE	43	9	DIO 23 SENSE
GND	42	8	GND
NC	41	7	NC
GND	40	6	GND
NC	39	5	NC
GND	38	4	GND
EXTERNAL FORCE	37	3	NC
GND	36	2	GND
EXTERNAL SENSE	35	1	CAL DMM

Center module: NI PXIe-6556, 200 MHz Digital I/O with PPMU, ACCESS, ACTIVE, AUX I/O, CLK IN, RFIO, CLK OUT, REMOTE SENSE, DIGITAL DATA & CONTROL, EXT FORCE, GUARD, EXT SENSE, GUARD, CAL, –3 to 8.5V MAX to ⏚, NATIONAL INSTRUMENTS

Right connector:

Signal	Pin	Pin	Signal
PFI 31	1	35	PFI 30
GND	2	36	GND
PFI 29	3	37	PFI 28
GND	4	38	GND
PFI 27	5	39	PFI 26
GND	6	40	GND
PFI 25	7	41	PFI 24
GND	8	42	GND
DIO 23	9	43	DIO 22
GND	10	44	GND
DIO 21	11	45	DIO 20
GND	12	46	GND
DIO 19	13	47	DIO 18
GND	14	48	GND
DIO 17	15	49	DIO 16
GND	16	50	GND
DIO 15	17	51	DIO 14
GND	18	52	GND
DIO 13	19	53	DIO 12
GND	20	54	GND
DIO 11	21	55	DIO 10
GND	22	56	GND
DIO 9	23	57	DIO 8
GND	24	58	GND
DIO 7	25	59	DIO 6
PFI 1	26	60	RESERVED
DIO 5	27	61	DIO 4
GND	28	62	GND
DIO 3	29	63	DIO 2
PFI 3	30	64	PFI 2
DIO 1	31	65	DIO 0
GND	32	66	GND
PFI 4 / DDC CLK OUT	33	67	PFI 5 / STROBE
GND	34	68	GND

Table 1. NI 6555/6556 DDC Connector Pins

Pins	Signal Name	Signal Type	Signal Description
9, 11, 13, 15, 17, 19, 21, 23, 25, 27, 29, 31, 43, 45, 47, 49, 51, 53, 55, 57, 59, 61, 63, 65	DIO <0..23>	Data	Bidirectional PPMU-capable digital I/O data channels 0 through 23.
26, 64	Programmable Function Interface (PFI) <1..2>	Control	PPMU-capable input terminals to the device for external triggers, or output terminals from the device for events.

Table 1. NI 6555/6556 DDC Connector Pins (Continued)

Pins	Signal Name	Signal Type	Signal Description
1, 3, 5, 7, 30, 35, 37, 39, 41	PFI 3, PFI <24..31>	Control	Single-ended input terminals to the device for static I/O, for external triggers, or for output terminals from the device for events. Refer to the device specifications for signal voltage levels.
33	PFI 4 / DDC CLK OUT	Control	Bidirectional PPMU-capable terminal for an exported Sample Clock or for trigger and event routing.
67	PFI 5 / STROBE	Control	Bidirectional PPMU-capable terminal for external Sample clock source that can be used only for dynamic acquisition or for trigger and event routing.
2, 4, 6, 8, 10, 12, 14, 16, 18, 20, 22, 24, 28, 32, 34, 36, 38, 40, 42, 44, 46, 48, 50, 52, 54, 56, 58, 62, 66, 68	GND	Ground	Ground reference for signals.
60	RESERVED	N/A	These terminals are reserved for future use. Do not connect to these pins.

Table 2. NI 6555/6556 REMOTE SENSE Connector Pins

Pins	Signal Name	Signal Type	Signal Description
9, 11, 13, 15, 17, 19, 21, 23, 25, 27, 29, 31, 43, 45, 47, 49, 51, 53, 55, 57, 59, 61, 63, 65	DIO <0..23> SENSE	Analog	Remote sense pins for PPMU channels 0 through 23.
26, 64	PFI <1..2> SENSE	Analog	Remote sense pins for PFI <1..2>.

Table 2. NI 6555/6556 REMOTE SENSE Connector Pins (Continued)

Pins	Signal Name	Signal Type	Signal Description
33, 67	PFI <4..5> SENSE	Analog	Remote sense pin for PFI <4..5>.
3, 5, 7, 30, 39, 41	NC	No connect	Do not connect to these terminals.
60	CAL GND	Analog ground	Ground reference for external calibrator.
2, 4, 6, 8, 10, 12, 14, 16, 18, 20, 22, 24, 28, 32, 34, 36, 38, 40, 42, 44, 46, 48, 50, 52, 54, 56, 58, 62, 66, 68	GND	Ground	Ground reference for signals.
1	CAL DMM	Analog	Resource used during external calibration.
35	EXTERNAL SENSE	Analog	Resource used for external SMU or DC sense functions.
37	EXTERNAL FORCE	Analog	Resource used for external SMU or DC force functions.

Table 3. NI 6555/6556 SMA Connectors

Connector	Signal Name	Signal Type	Description
CLK IN	Reference Clock Input	Control	Terminal for the external Reference clock used for the PLL or for the external Sample clock used for dynamic generation and/or acquisition.
PFI 0	PFI 0	Control	Single-ended input terminals to the device for static I/O, for external triggers, or for output terminals from the device for events. Refer to the device specifications for signal voltage levels.
CLK OUT	Reference Clock Output	Control	Terminal for the exported PLL Reference clock or the exported Sample clock.

Table 4. NI 6555/6556 AUX I/O Connector Pins

Pins	Signal Name	Signal Type	Signal Description
1	External Force	Analog	Resource for external (SMU or DC) force functions.
2, 4	Guard	Analog	Guard connection for external (SMUs or DC) sources.
3	External Sense	Analog	Resource for external (SMU or DC) sense functions.
5	Cal	Analog	Resource used during external calibration.
6	GND	Analog	Ground reference for external signals.

Table 5. ACTIVE LED Indicators

Color	Indications
Off	Device not armed and not triggered.
Amber	Device armed and awaiting Start Trigger. If performing a dynamic acquisition operation, the device may be acquiring pretrigger samples.
Green	Device received Start Trigger.
Red	Error condition.

Table 6. ACCESS LED Indicators

Color	Indications
Off	Device not ready.
Amber	Device being accessed by software.
Green	Device ready to be programmed
Red	Running the niHSDIO Self Test VI or calling the `niHSDIO_self_test` function produced a failure.

Cabling Considerations

Cables for DDC and REMOTE SENSE connector

⚠️ **Caution** To ensure the specified EMC performance, operate this product only with shielded cables and accessories. Do not use unshielded cables or accessories unless they are installed in a shielded enclosure with properly designed and shielded input/output ports and connected to the product using a shielded cable. If unshielded

cables or accessories are not properly installed and shielded, the EMC specifications for the product are no longer guaranteed.

Use one of the following cables for connections to the Digital Data and Control (DDC) connector and the REMOTE SENSE connector on the NI 6555/6556.

- NI 6555/6556 shielded single-ended cable with individual microcoaxial 50 Ω lines for each signal
- NI 6555/6556 unshielded single-ended digital cable
- NI 6555/6556 shielded low-leakage cable. Use this cable if current leakage is a concern.
- NI 6555/6556 shielded flying lead cable

Figure 4. Single-Ended Flying Lead Cable Parts Locator Diagram
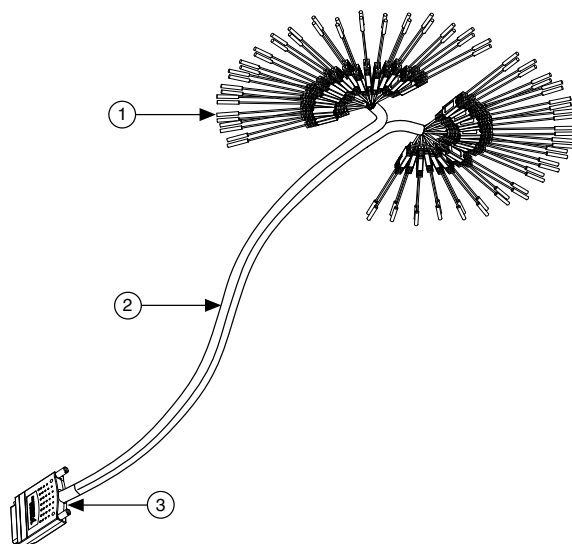
1. Lead Pairs
2. Removable Sleeving
3. 68-Pin VHDCI Connector

Note Always terminate cables properly to avoid improper measurements caused by signal reflections, overshoot, and undershoot. Refer to the *NI Digital Waveform Generator/Analyzer Help* for more information about signal termination.

Connecting the Backshell for the AUX I/O Connector on the NI 6556

For ease of use and safety, always operate connections to the AUX I/O connector on the device with the backshell provided in the shipping kit. You can purchase additional backshell kits from NI (part number 762916-01).

Connect the backshell to the device, as shown in the following figure.

Figure 5. Connecting the Backshell to the NI 6556
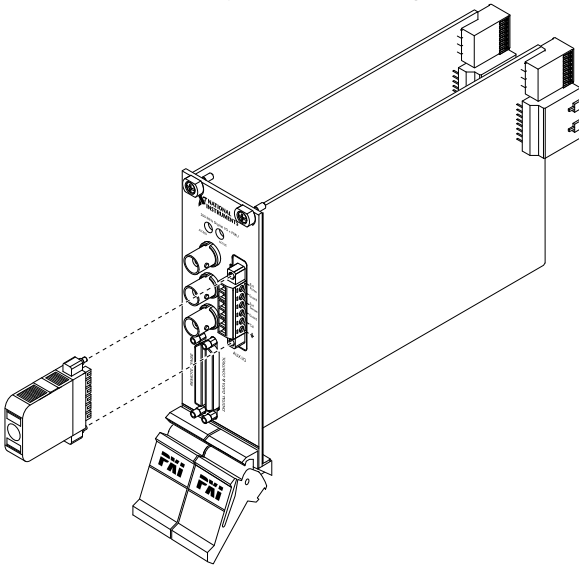
> 📝 **Note** Operating the device without the provided backshell can expose users to high voltage. Refer to the specifications for your device for the working voltage levels of the AUX I/O connector.
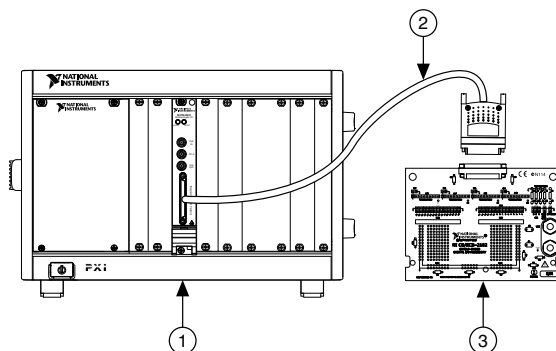
Connecting Accessories

• Connect the NI CB-2162 single-ended digital I/O accessory to access the DDC connector on the device, to terminate the DIO channels on the device, or to develop a platform for prototyping circuits and testing DUTs. Refer to the following figure to the connect the NI CB-2162 to the NI 6555/6556.

> ⚠ **Caution** To ensure the specified EMC performance, operate this product only with shielded cables and accessories. Do not use unshielded cables or accessories unless they are installed in a shielded enclosure with properly designed and shielded input/output ports and connected to the product using a

shielded cable. If unshielded cables or accessories are not properly installed and shielded, the EMC specifications for the product are no longer guaranteed.

Figure 6. Connecting the NI CB-2162 and an NI-HSDIO device

1. Chassis with NI-HSDIO device
2. NI SHC68-C68-D4 Cable
3. NI CB-2162 Accessory

• Connect the NI SMB-2163 accessory to the NI 6555/6556 in order to interface to other devices for testing and debugging. Refer to the following figure to the connect the NI SMB-2163 to the NI 6555/6556.

⚠ **Caution** To ensure the specified EMC performance, operate this product only with shielded cables and accessories. Do not use unshielded cables or accessories unless they are installed in a shielded enclosure with properly designed and shielded input/output ports and connected to the product using a

shielded cable. If unshielded cables or accessories are not properly installed and shielded, the EMC specifications for the product are no longer guaranteed.

Figure 7. Connecting the NI SMB-2163 and an NI-HSDIO device
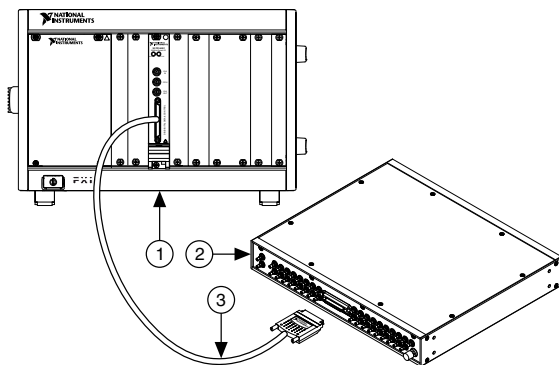
1. Chassis with NI-HSDIO device
2. NI SMB-2163 Accessory
3. NI SHC68-C68-D4 Cable

Connecting a Custom Accessory

⚠️ **Caution** To ensure the specified EMC performance, operate this product only with shielded cables and accessories. Do not use unshielded cables or accessories unless they are installed in a shielded enclosure with properly designed and shielded input/output ports and connected to the product using a shielded cable. If unshielded cables or accessories are not properly installed and shielded, the EMC specifications for the product are no longer guaranteed.

Many common NI digital waveform generator/analyzer applications require you to create a custom accessory to access the signals of your device under test (DUT).

To connect a custom accessory to the VHDCI DDC connector on the NI 6555/6556, purchase a mating connector for the VHDCI cable from NI.

For more information, refer to the *Interfacing to the NI Digital Waveform Generator/ Analyzer using the VHDCI Connector* application note, available by entering the info code rdinwa at *ni.com/info*.

Wiring Considerations for Common Applications

Dynamic Generation Wiring Considerations

You may need additional parallel termination resistance at the destination for optimal signal quality during dynamic generation, depending on the loading of the device under test (DUT). Refer to the *NI Digital Waveform Generator/Analyzer Help* for information about signal

termination. The following figure shows how the NI 6555/6556 generates data through a matched impedance system.

**Figure 8.** Dynamic Generation Functional Diagram
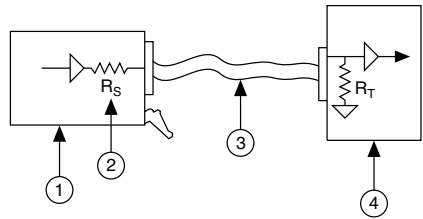
1. NI-HSDIO device	3. 50 Ω cable
2. 50 Ω source impedance (Rs)	4. Device under test (DUT)

Dynamic Acquisition Wiring Considerations

During dynamic acquisition, the device under test (DUT) generating the signals must have a matched source impedance as close to 50 Ω as possible in order to minimize signal reflections and maintain optimal signal quality. The following figure shows how the NI 6555/6556 acquires data through a matched impedance system. Refer to the specifications document for your device for supported input impedances (R_T).

Figure 9. Dynamic Acquisition Functional Diagram
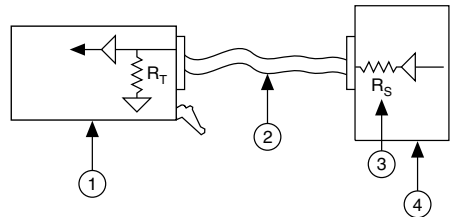
1. NI-HSDIO device	3. 50 Ω source impedance (Rs)
2. 50 Ω cable	4. Device under test (DUT)

Configuring the NI 6555/6556 in MAX

Use Measurement & Automation Explorer (MAX) to configure your National Instruments hardware. MAX informs other programs about which devices reside in the system and how they are configured. MAX is automatically installed with NI-HSDIO.

1. Launch MAX.

2. In the configuration tree, double-click **Devices and Interfaces** to see the list of installed devices.

> **Note** If you are using the NI 6555/6556 with the LabVIEW Real-Time Module, expand **Remote Systems**. Find your target IP address or name, expand it, and then expand **Devices and Interfaces**.

3. Expand your **Chassis** tree item.

MAX lists all devices installed in the chassis. Your default device names may vary.

> **Note** If you do not see your device listed, press <F5> to refresh the list of installed devices. If the device is still not listed, power off the system, ensure the device is correctly installed, and restart.

4. Record the device identifier MAX assigns to the hardware. Use this identifier when programming the NI 6555/6556.

5. Self-test the device by selecting the device in the configuration tree and clicking **Self-Test** in the MAX toolbar.

The MAX self-test performs a basic verification of hardware resources.

Using the Test Panel to Generate Digital Data

To verify your device configuration, use the device test panel in MAX to generate simple digital data using the NI 6555/6556.

1. Remove any signal connections to the device.

2. Right-click the device under the appropriate **Chassis**, and select **Test Panels**.

3. Select the **Dynamic** tab on the side of the pane.

4. Click **Select All** to select all of your channels. The digital waveform now appears in the graph.

5. Enter the frequency in the **Clock Frequency** control, located on the left side of the test panel.

6. Enter values for the generation and acquisition voltages in the controls below the **Clock Frequency** control. You can enter High and Low values to create your own custom levels for the device.

7. Choose a fill pattern. The following figure shows the pattern control and the first few signals in the window.
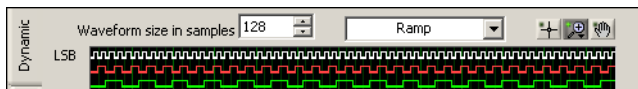
> **Note** When generating a Marching Ones or Marching Zeroes waveform, make the waveform size a multiple of the number of selected channels.

8. Click **Play** to generate the waveform shown in the graph on the selected channels. The default generation option is **Continuous**, but you can toggle this setting using the control below the graph.

Because the data channels on the NI 6555/6556 are bidirectional, while the data is being continuously generated on all of the channels, as in the previous step, you can acquire the data being generated.

> **Note** Refer to the specifications document for information about the setup and hold times and propagation delays associated with generation operations of the device. At some frequencies, generating data on the same channels when no cable is attached to the DIGITAL DATA & CONTROL (DDC) connector violates these timing parameters and results in incorrectly sampled data. For information, refer to the *NI Digital Waveform Generator/Analyzer Help*.

Using the Test Panel to Acquire Digital Data

To verify your device configuration, use the device test panel in MAX to acquire simple digital data using the NI 6555/6556.

1. Click the **Acquisition** tab.
2. Click **Play**.

   The device acquires the digital data that you generated in the previous steps, and the data is displayed on the graph.
3. Click **Close** when you are finished.

Programming the NI-HSDIO

You can generate or acquire data using the NI-HSDIO instrument driver to program the NI 6555/6556 in the supported ADE of your choice.

Table 7. NI 6555/6556 Programming Options

Application Programming Interface (API)	Location	Description
NI-HSDIO Instrument Driver	LabVIEW—Available on the LabVIEW Functions palette at **Measurement I/O»NI-HSDIO**.	NI-HSDIO configures and operates the device hardware and performs basic waveform generation and acquisition using LabVIEW VIs or LabWindows/CVI functions.
	C or LabWindows/CVI—Available at **Program Files»IVI Foundation»IVI»Drivers» niHSDIO**.	
	Microsoft Visual C/C++—Use examples located in the `<NIDocDir>`\NI-HSDIO \examples directory, where `<NIDocDir>` is one of the following directories: • Windows 8/7/Vista—Users \Public\Documents \National Instruments • Windows XP—Documents and Settings\All Users\Shared Documents\National Instruments	You can modify an NI-HSDIO C example to create an application with Microsoft Visual C/C++. Copy an NI-HSDIO example to copy required project settings for include paths and library files. Alternatively, refer to the *Creating an Application with Microsoft Visual C and C++* topic of the *NI Digital Waveform Generator/ Analyzer Help* to manually add all required include and library files to your project.

NI-HSDIO Examples

To locate programming examples, refer to the following table.

Table 8. NI-HSDIO Example Locations

Application Development Environment (ADE)	Location
LabVIEW or LabWindows/CVI	Within LabVIEW or LabWindows/CVI, select **Help»Find Examples**, and navigate to **Hardware Input and Output»Modular Instruments**.
ANSI C	Available at `<NIDocDir>`\NI-HSDIO\examples\ directory, where `<NIDocDir>` is one of the following directories: • Windows 8/7/Vista—`Users\Public\Public Documents\National Instruments` • Windows XP—`Documents and Settings\All Users\Shared Documents\National Instruments`

Troubleshooting

If an issue persists after you complete a troubleshooting procedure, contact NI technical support or visit *ni.com/support*.

What Should I Do if the NI 6555/6556 Doesn't Appear in MAX?

1. In the MAX configuration tree, click **Devices and Interfaces**.
2. Expand the **Chassis** tree to see the list of installed devices, and press <F5> to refresh the list.
3. If the module is still not listed, power off the system, ensure that all hardware is correctly installed, and restart the system.
4. Navigate to the Device Manager.

Operating System	Description
Windows 8	Right-click the Start screen, and select **All apps»Control Panel»Hardware and Sound»Device Manager**.
Windows 7	Select **Start»Control Panel»Device Manager**.
Windows Vista	Select **Start»Control Panel»System and Maintenance»Device Manager**.
Windows XP	Select **Start»Control Panel»System»Hardware»Device Manager**.

5. If you are using a PXI controller, verify that a **National Instruments** entry appears in the system device list. Reinstall NI-HSDIO and the device if error conditions appear in the list. If you are using an MXI controller, right-click **PCI-to-PCI Bridge**, and select **Properties** from the shortcut menu to verify that the bridge is enabled.

What Should I Do if the Module Fails the Self-Test?

1. Restart the system.
2. Launch MAX, and perform the self-test again.
3. Power off the chassis.
4. Reinstall the failed module in a different slot.
5. Power on the chassis.
6. Perform the self-test again.

Why Is the ACCESS LED Off When the Chassis is On?

The LEDs may not illuminate until the device has been configured in MAX. Before proceeding, verify that the NI 6555/6556 appears in MAX.
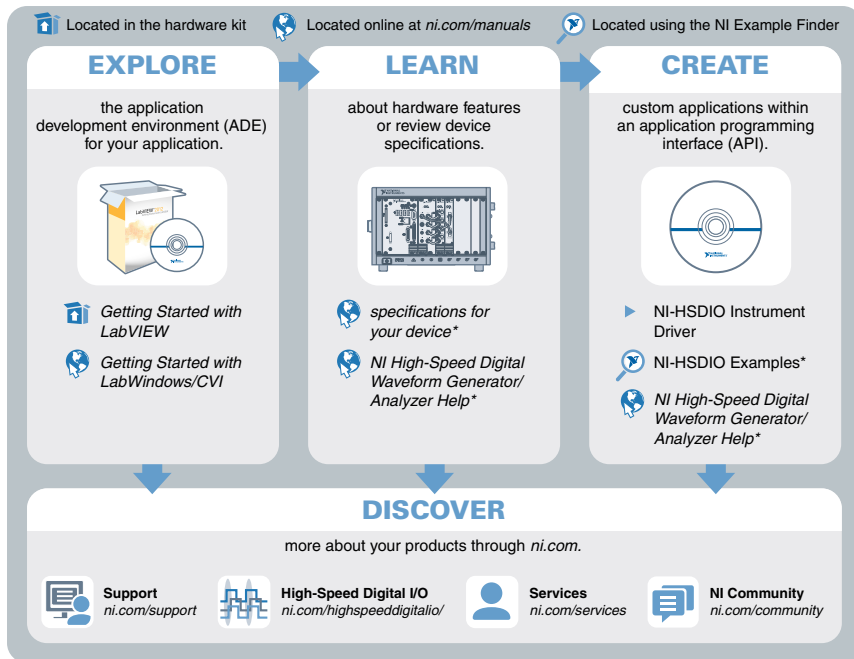
If the ACCESS LED fails to illuminate after you power on the PXI Express chassis, a problem may exist with the PXI Express power rails, a hardware module, or the LED.

1. ⚠ **Caution** Apply external signals only while the NI 6555/6556 is powered on. Applying external signals while the device is powered off may cause damage.

   Disconnect any signals from the PXI Express module front panels.
2. Remove any front panel connections from the NI 6555/6556.
3. Power off the PXI Express chassis.
4. Remove the module from the PXI Express chassis and inspect it for damage. Do not reinstall a damaged device.
5. Install the module in a different PXI Express chassis slot from which you removed it.
6. Power on the PXI Express chassis.
7. Verify that the device appears in MAX.
8. Reset the device in MAX and perform a self-test.

If the ACCESS LED still fails to illuminate and failures continue, contact NI technical support or visit *ni.com/support*.

Where to Go Next

The following figure describes additional documentation and resources as you develop an application with the NI 6555/6556.

EXPLORE

the application development environment (ADE) for your application.

- *Getting Started with LabVIEW*
- *Getting Started with LabWindows/CVI*

LEARN

about hardware features or review device specifications.

- *specifications for your device**
- *NI High-Speed Digital Waveform Generator/ Analyzer Help**

CREATE

custom applications within an application programming interface (API).

- ▶ NI-HSDIO Instrument Driver
- NI-HSDIO Examples*
- *NI High-Speed Digital Waveform Generator/ Analyzer Help**

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*This item is also installed with the driver software.

Worldwide Support and Services

The National Instruments website is your complete resource for technical support. At *ni.com/support*, you have access to everything from troubleshooting and application development self-help resources to email and phone assistance from NI Application Engineers.

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National Instruments corporate headquarters is located at 11500 North Mopac Expressway, Austin, Texas, 78759-3504. National Instruments also has offices located around the world. For telephone support in the United States, create your service request at *ni.com/support* or

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