



Manufacturer: NI

**Board Assembly Part Numbers** (Refer to Procedure 1 for identification procedure):

Part Number and Revision	Description
134956A-01L or later	MODULE ASSY,PXIE-6569,FLEXRIO DIGITAL I/O,32 CH IN,32 CH OUT LVDS 1.25GBPS,8 CH SE I/O,KU035
134956A-02L or later	MODULE ASSY,PXIE-6569,FLEXRIO DIGITAL I/O,32 CH IN,32 CH OUT LVDS 1.25GBPS,8 CH SE I/O,KU060
134956A-03L or later	MODULE ASSY,PXIE-6569,FLEXRIO DIGITAL I/O,64 CH IN LVDS 1.25GBPS,8 CH SE I/O,KU035
134956A-04L or later	MODULE ASSY,PXIE-6569,FLEXRIO DIGITAL I/O,64 CH IN LVDS 1.25GBPS,8 CH SE I/O,KU060
134956A-05L or later	MODULE ASSY,PXIE-6569,FLEXRIO DIGITAL I/O,64 CH OUT LVDS 1.25GBPS,8 CH SE I/O,KU035
134956A-06L or later	MODULE ASSY,PXIE-6569, FLEXRIO DIGITAL I/O,64 CH OUT LVDS 1.25GBPS,8 CH SE I/O,KU060

**Volatile and Non-Volatile Memory of Component Models**

**Volatile Memory**

<i>Target Data</i>	<i>Type</i>	<i>Size</i>	<i>Battery Backup</i>	<i>User<sup>1</sup> Accessible</i>	<i>System Accessible</i>	<i>Sanitization Procedure</i>
Data storage (PN –01, –03 and –05 only)	DRAM	2 GB	No	Yes	Yes	Cycle Power
Data storage (PN –02, –04 and –06 only)	DRAM	4 GB	No	Yes	Yes	Cycle Power
User FPGA Bitfile Storage	SDRAM	1 GB	No	Yes	Yes	Cycle Power
Customer Program	FPGA	Xilinx KU035 or KU060	No	Yes	Yes	Cycle Power

**Non-Volatile Memory (incl. Media Storage)**

<i>Target Data</i>	<i>Type</i>	<i>Size</i>	<i>Battery Backup</i>	<i>User Accessible</i>	<i>System Accessible</i>	<i>Sanitization Procedure</i>
Adapter Module Configuration	EEPROM	2 KB	No	No	Yes	None
FPGA Configuration Logic	CPLD	Intel 10M04	No	No	Yes	None

<sup>1</sup> Refer to *Terms and Definitions* section for clarification of *User* and *System Accessible*



User Persistent FPGA Image	Flash	64 MB	No	No	Yes	None
Default Persistent FPGA Image	Flash	64 MB	No	No	Yes	None
Golden FPGA Image	Flash	64 MB	No	No	Yes	None
Calibration constants	EEPROM	2 KB	No	Yes	Yes	Procedure 2



## Procedures

### Procedure 1 – Board Assembly Part Number identification:

To determine the Board Assembly Part Number and Revision, refer to the label applied to the surface of your product. The Assembly Part Number should be formatted as “P/N: 134956a-##L” where “a” is the letter revision of the assembly (e.g. A, B, C...) and “##” is the dash number of the assembly (e.g. -01, -02, -03...).

### Procedure 2 – Calibration Constants EEPROM:

Requirements: LabVIEW 2018 or later and FlexRIO 18.7 or later

The calibration constants EEPROM can be cleared by using the FlexRIO API to overwrite the memory space with arbitrary values. To clear the storage through LabVIEW, complete the following steps:

1. Find and open the example LabVIEW Project “Read-Write Calibration Data” at “C:\<Program Files>\National Instruments\<LabVIEW>\examples\FlexRIO\System Calibration\” (replace <LabVIEW> with version of LabVIEW running on system).
  - a. Alternatively, create a new VI and drop the “Write Calibration Data” VI from the FlexRIO API palette.
2. Select your FlexRIO device from **FPGA Resource** dropdown and set **Calibration Operation** to **Write**.
3. Run VI with **Calibration Data** set to 0, or other arbitrary value, to clear values in flash memory.
4. Repeat Step 3 for entire memory space to clear entire EEPROM memory. Set the **Read** option for the **Calibration Operation** to verify data has been cleared or to check sections of memory for other unintended values.

This memory space is also exposed to the C API. Use the following code as reference to clear it:

```
// Open session to the resource
niFlexRIO_OpenSession(resource, bitfile, 0, &session)
// Determine calibration storage size
niFlexRIO_GetAttributeInt32(session, NULL, kFlexRIO_CalibrationStorageSize, &size)
// Allocate byte buffer of zeros
zeros = calloc(size, 1);
// Write zeros to the device
niFlexRIO_WriteCalibrationData(session, 0, zeros, size)
```



## Terms and Definitions

### **Cycle Power:**

The process of completely removing power from the device and its components and allowing for adequate discharge. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

### **Volatile Memory:**

Requires power to maintain the stored information. When power is removed from this memory, its contents are lost. This type of memory typically contains application specific data such as capture waveforms.

### **Non-Volatile Memory:**

Power is not required to maintain the stored information. Device retains its contents when power is removed. This type of memory typically contains information necessary to boot, configure, or calibrate the product or may include device power up states.

### **User Accessible:**

The component is read and/or write addressable such that a user can store arbitrary information to the component from the host using a publicly distributed NI tool, such as a Driver API, the System Configuration API, or MAX.

### **System Accessible:**

The component is read and/or write addressable from the host without the need to physically alter the product.

### **Clearing:**

Per *NIST Special Publication 800-88 Revision 1*, “clearing” is a logical technique to sanitize data in all User Accessible storage locations for protection against simple non-invasive data recovery techniques using the same interface available to the user; typically applied through the standard read and write commands to the storage device.

### **Sanitization:**

Per *NIST Special Publication 800-88 Revision 1*, “sanitization” is a process to render access to “Target Data” on the media infeasible for a given level of effort. In this document, clearing is the degree of sanitization described.