

Manufacturer: National Instruments

Board Assembly Part Numbers (Refer to Procedure 1 for identification procedure):

Part Number and Revision	Description
147555A-01L or later	PXIe-6571

Volatile Memory

<i>Target Data</i>	<i>Type</i>	<i>Size</i>	<i>Battery Backup</i>	<i>User¹ Accessible</i>	<i>System Accessible</i>	<i>Sanitization Procedure</i>
Device operation	FPGA	Xilinx XC7VX415T	No	Yes	Yes	Cycle Power
Data storage	DRAM	4 GB	No	Yes	Yes	Cycle Power
FPGA image storage	SDRAM	256 MB	No	Yes	Yes	Cycle Power

Non-Volatile Memory (incl. Media Storage)

<i>Target Data</i>	<i>Type</i>	<i>Size</i>	<i>Battery Backup</i>	<i>User Accessible</i>	<i>System Accessible</i>	<i>Sanitization Procedure</i>
Clock Configuration	Internal EEPROM	512 Bytes	No	No	Yes	None
FPGA Configuration and Logic	CPLD	Intel 10M16SCU	No	No	Yes	None
Board Control	CPLD	Lattice LCMX02-1200HS-4MG	No	No	Yes	None
Primary Storage	Flash	1 GB	No			
• FPGA boot Image				No	Yes	None
• Device Identification				No	Yes	None
• Calibration Data				No	Yes	None
• Calibration Metadata				Yes	Yes	Procedure 2

¹ Refer to *Terms and Definitions* section for clarification of *User* and *System Accessible*

Terms and Definitions

Cycle Power:

The process of completely removing power from the device and its components and allowing for adequate discharge. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

Volatile Memory:

Requires power to maintain the stored information. When power is removed from this memory, its contents are lost. This type of memory typically contains application specific data such as capture waveforms.

Non-Volatile Memory:

Power is not required to maintain the stored information. Device retains its contents when power is removed. This type of memory typically contains information necessary to boot, configure, or calibrate the product or may include device power up states.

User Accessible:

The component is read and/or write addressable such that a user can store arbitrary information to the component from the host using a publicly distributed NI tool, such as a Driver API, the System Configuration API, or MAX.

System Accessible:

The component is read and/or write addressable from the host without the need to physically alter the product.

Clearing:

Per *NIST Special Publication 800-88 Revision 1*, “clearing” is a logical technique to sanitize data in all User Accessible storage locations for protection against simple non-invasive data recovery techniques using the same interface available to the user; typically applied through the standard read and write commands to the storage device.

Sanitization:

Per *NIST Special Publication 800-88 Revision 1*, “sanitization” is a process to render access to “Target Data” on the media infeasible for a given level of effort. In this document, clearing is the degree of sanitization described.