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PXIe-7866 Getting Started



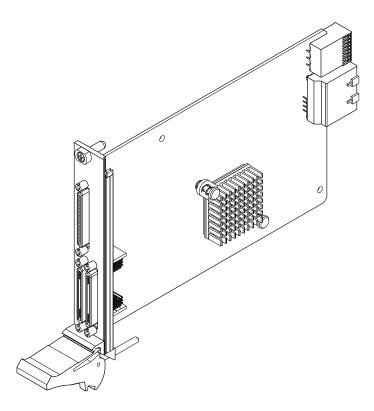


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Getting Started

This document describes how to begin using the NI PXIe-7866.



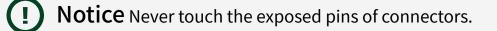
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Unpacking the Kit

Notice To prevent electrostatic discharge (ESD) from damaging the device, ground yourself using a grounding strap or by holding a grounded object, such as your computer chassis.

1. Touch the antistatic package to a metal part of the computer chassis.

2. Remove the device from the package and inspect the device for loose components or any other sign of damage.



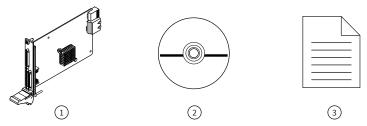
Note Do not install a device if it appears damaged in any way.

3. Unpack any other items and documentation from the kit.

Store the device in the antistatic package when the device is not in use. Verifying the Kit Contents

Verify that the following items are included in the NI PXIe-7866 kit.

Figure 1. NI PXIe-7866 Kit Contents



- 1. Hardware
- 2. NI-RIO Media
- 3. Getting Started Guide

Installing Software on the Host Computer

Before using the NI PXIe-7866, you must install the following application software and device drivers on the host computer.

- 1. LabVIEW 2019 SP1 or later
- 2. LabVIEW Real-Time Module 2019 or later [1]
- 3. LabVIEW FPGA Module 2019 or later
- 4. NI R Series Multifunction RIO Device Drivers January 2020 or later

Visit <u>ni.com/info</u> and enter the Info Code softwareversion for minimum software support information.

¹ LabVIEW Real Time Module is only required when the R Series board is used in a chassis where the PXIe Controller is running a real-time operating system. **Installing the NI PXIe-7866**

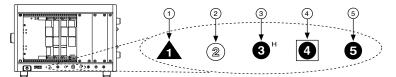
Notice To prevent damage to the NI PXIe-7866 caused by ESD or contamination, handle the module using the edges or the metal bracket.

1. Ensure the AC power source is connected to the chassis before installing the module.

The AC power cord grounds the chassis and protects it from electrical damage while you install the module.

- 2. Power off the chassis.
- 3. Inspect the slot pins on the chassis backplane for any bends or damage prior to installation. Do not install a module if the backplane is damaged.
- 4. Remove the black plastic covers from all the captive screws on the module front panel.
- 5. Identify a supported slot in the chassis. The following figure shows the symbols that indicate the slot types.

Figure 2. Chassis Compatibility Symbols



- 1. PXI Express System Controller Slot
- 2. PXI Peripheral Slot
- 3. PXI Express Hybrid Peripheral Slot
- 4. PXI Express System Timing Slot
- 5. PXI Express Peripheral Slot

NI PXIe-7866 modules can be placed in PXI Express peripheral slots, PXI Express hybrid peripheral slots, or PXI Express system timing slots.

6. Touch any metal part of the chassis to discharge static electricity.

- 7. Place the module edges into the module guides at the top and bottom of the chassis. Slide the module into the slot until it is fully inserted.
- 8. Secure the module front panel to the chassis using the front-panel mounting screws.



Note Tightening the top and bottom mounting screws increases mechanical stability and also electrically connects the front panel to the chassis, which can improve the signal quality and electromagnetic performance.

9. Cover all empty slots using either filler panels (standard or EMC) or slot blockers with filler panels, depending on your application.



Note For more information about installing slot blockers and filler panels, go to <u>ni.com/r/pxiblocker</u>.

10. Power on the chassis.

Verifying Hardware Installation for Host Targets

You can verify that the system recognizes the NI PXIe-7866 by using Measurement & Automation Explorer (MAX).

- 1. Launch MAX by navigating to **Start** » **All Programs** » **National Instruments** » **MAX** or by clicking the MAX desktop icon.
- 2. Expand Devices and Interfaces.
- 3. Verify that the device appears under **Devices and Interfaces**. If the device does not appear, press <F5> to refresh the view in MAX. If the device does not appear after refreshing the view, visit <u>ni.com/support</u> for troubleshooting information.

Verifying Hardware Installation for Remote Systems

You can verify that the system recognizes the NI PXIe-7866 by using Measurement & Automation Explorer (MAX).

1. Launch MAX on the host computer.

- 2. Expand **Remote Systems** in the configuration tree and locate your system.
- 3. Install LabVIEW Real-Time Module2019 and NI R Series Multifunction RIO Device Drivers January 2020 or later on your **Remote System**.
 - 1. Refer to the **Installing Software on the Host Computer** section for information about installing software on the host.
 - 2. Refer to the **PXI Express Controllers User Manual** at <u>ni.com/manuals</u> for information on installing software on the target.
- 4. Under **Remote Systems**, find and expand **Devices and Interfaces**. If the device does not appear, press <F5> to refresh the view in MAX. If the device does not appear after refreshing the view, visit <u>ni.com/support</u> for troubleshooting information.

Connecting the NI PXIe-7866

NI recommends using the following cables and accessories with the NI PXIe-7866:

Connector Type	Connector Block	Cables
Analog I/O Connector 0 (RAIO) Analog Output Connector 2 (RAO)	SCB-68A Noise Rejecting, Shielded I/O Connector Block (782536-01)	 SHC68-68-RMIO Shielded Cable for the Reconfigurable MIO Connector, 68-pinD-Type to 68-pin VHDCI, 1 m(189588-01) SHC68-68-RMIO Shielded Cable for the Reconfigurable MIO Connector, 68-pinD-Type to 68-pin VHDCI, 2 m(189588-02)
Digital Connector 1 (RDIO)	SCB-68 HSDIO Shielded 68-Pin Connector Block for Reconfigurable DIO & HSDIO Products (782914-01)	 SHC68-C68-RDIO2 Shielded High Speed Digital Cable for the Reconfigurable DIO Connector, 1 m(156166-01)

Table 1. Recommended Cables and Accessories

Connector Type	Connector Block	Cables
		 SHC68-C68-RDIO2 Shielded High Speed Digital Cable for the Reconfigurable DIO Connector, 2 m(156166-02)

Note The SCB-68A DIP switches must be set for Direct Feedthrough mode for use with R Series devices. Visit <u>ni.com/info</u> and enter the Info Code scb68acables for more information on the SCB-68A accessory.

Note NI is not liable for connections that exceed any of the maximum ratings of input or output signals on the NI PXIe-7866 and on the computer chassis. Refer to the **NI PXIe-7866 Specifications**, available at <u>ni.com/info</u> for the maximum input and output ratings for each signal.

Pinout

	\frown	1		\frown			\frown)
AI0+	68 34	AI0-	GND	68 34	GND	AOCH16	68 34	AOGND16
AIGND0	67 33	AIGND1	EXTCLKIN	67 33	GND	NC	67 33	NC
Al1+	66 32	Al1-	GND	66 32	GND	AOCH17	66 32	AOGND17
NC	65 31	NC	DIO0	65 31	DIO1	AOCH18	65 31	AOGND18
GND	64 30	GND	GND	64 30	GND	NC	64 30	NC
NC	63 29	NC	DIO2	63 29	DIO3	AOCH19	63 29	AOGND19
NC	62 28	NC	GND	62 28	GND	AOCH20	62 28	AOGND20
GND	61 27	GND	DIO4	61 27	DIO5	NC	61 27	NC
NC	60 26	NC	GND	60 26	GND	AOCH21	60 26	AOGND21
NC	59 25	NC	DIO6	59 25	DIO7	AOCH22	59 25	AOGND22
GND	58 24	GND	GND	58 24	GND	NC	58 24	NC
NC	57 23	NC	DIO8	57 23	DIO9	AOCH23	57 23	AOGND23
AISENSE	56 22	NC	GND	56 22	GND	NC	56 22	NC
AO0	55 21	AOGND0	DIO10	55 21	DIO11	AO8	55 21	AOGND8
AO1	54 20	AOGND1	GND	54 20	GND	AO9	54 20	AOGND9
AO2	53 19	AOGND2	DIO12	53 19	DIO13	AO10	53 19	AOGND10
AO3	52 18	AOGND3	GND	52 18	GND	AO11	52 18	AOGND11
AO4	51 17	AOGND4	DIO14	51 17	DIO15	AO12	51 17	AOGND12
AO5	50 16	AOGND5	GND	50 16	GND	AO13	50 16	AOGND13
AO6	49 15	AOGND6	DIO16	49 15	DIO17	AO14	49 15	AOGND14
AO7	48 14	AOGND7	GND	48 14	GND	AO15	48 14	AOGND15
NC	47 13	NC	DIO18	47 13	DIO19	NC	47 13	NC
NC	46 12	NC	GND	46 12	GND	NC	46 12	NC
NC	45 11	NC	DIO20	45 11	DIO21	NC	45 11	NC
NC	44 10	NC	GND	44 10	GND	NC	44 10	NC
NC	43 9	GND	DIO22	43 9	DIO23	NC	43 9	NC
NC	42 8	GND	GND	42 8	GND	NC	42 8	NC
NC	41 7	GND	DIO24	41 7	DIO25	NC	41 7	NC
NC	40 6	GND	GND	40 6	GND	NC	40 6	NC
NC	39 5	GND	DIO26	39 5	DIO27	NC	39 5	NC
NC	38 4	GND	GND	38 4	GND	NC	38 4	NC
NC	37 3	GND	DIO28	37 3	DIO29	NC	37 3	NC
NC	36 2	GND	GND	36 2	GND	NC	36 2	NC
+5V	35 1	+5V	DIO30	35 1	DIO31	NC	35 1	NC
	\subseteq)		\subseteq)
	CONNECTOR	30	с	ONNECTOR	1	cc	ONNECTO	32
ERMINAL 34	(RAIO)	TERMINAL 1	TERMINAL 34	(RDIO)	TERMINAL 1	TERMINAL 34	(RAO)	TERMINAL
1		¥@			¥Ø			
ERMINAL 68		TERMINAL 35	TERMINAL 68		TERMINAL 35	TERMINAL 68		TERMINAL 3

Table 2. NI PXIe-7866 Signal Descriptions

Signal	Description
Al+	Positive analog input signal connection
Al-	Negative analog input signal connection
AISENSE	Reference connection for NRSE measurements
AIGND	Ground reference for the analog input signal
AO	Analog output signal connection
AOGND	Ground reference for the analog output signal
DIO	Digital input/output signal connection
EXTCLKIN	External clock input source that can be used for source synchronous acquisitions. The provided clock source must be stable and glitch-free.
GND	Ground connection
Supply (+5 V _{out})	5 V power output connection for external devices

Signal	Description
NC	No connection

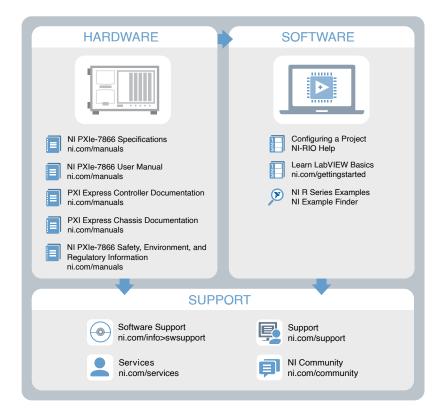
The NI PXIe-7866 is protected from overvoltage and overcurrent conditions.

Note Refer to the **NI PXIe-7866 Specifications**, available at <u>ni.com/</u> <u>manuals</u> for more information.



Note The pinout label on the lid of the SCB-68A accessory is incompatible with the NI PXIe-7866. Refer to the **NI 78xxR Pinout Labels for the SCB-68A**, available at <u>ni.com/manuals</u> for the compatible pinout labels.

Where to Go Next



NI Services

Visit <u>ni.com/support</u> to find support resources including documentation, downloads, and troubleshooting and application development self-help such as tutorials and examples.

Visit <u>ni.com/services</u> to learn about NI service offerings such as calibration options, repair, and replacement.

Visit <u>ni.com/register</u> to register your NI product. Product registration facilitates technical support and ensures that you receive important information updates from NI.

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