

**Manufacturer:** National Instruments

**Board Assembly Part Numbers** (Refer to Procedure 1 for identification procedure):

Part Number and Revision	Description
153956A-01L or later	PXIe-7971R
153956A-02L or later	PXIe-7972R
153956A-05L or later	PXIe-7975R
153956A-06L or later	PXIe-7976R

### Volatile Memory

<i>Target Data</i>	<i>Type</i>	<i>Size</i>	<i>Battery Backup</i>	<i>User<sup>1</sup> Accessible</i>	<i>System Accessible</i>	<i>Sanitization Procedure</i>
Data storage during VI Execution <sup>2</sup>	DRAM	2GB	No	Yes	Yes	Cycle power
Temporary storage for FPGA image	PSRAM	128MB	No	No	Yes	Cycle power
Field Programmable Gate Array	Kintex-7 Block RAM	16,020Kb – 28,620Kb	No	Yes	Yes	Cycle power

### Non-Volatile Memory (*incl. Media Storage*)

<i>Target Data</i>	<i>Type</i>	<i>Size</i>	<i>Battery Backup</i>	<i>User Accessible</i>	<i>System Accessible</i>	<i>Sanitization Procedure</i>
Board revision, clocking and voltage constants	EEPROM	2Kb	No	No	Yes	None
Bitfile to FPGA	CPLD	640 LUTs 5Kb RAM 18Kb SRAM 25Kb UFM	No	No	Yes	None
User FPGA Bitfile	Flash	512MB	No	Yes	Yes	Procedure 2
Device Attributes	Flash	256Kb	No	Yes	Yes	Procedure 3

<sup>1</sup> Refer to *Terms and Definitions* section for clarification of *User* and *System Accessible*

<sup>2</sup> The NI PXIe-7971R does not contain DRAM

## Procedures

### **Procedure 1 – Board Assembly Part Number identification:**

To determine the Board Assembly Part Number and Revision, refer to the label applied to the surface of your product. The Assembly Part Number should be formatted as “P/N: #####a-##L.

### **Procedure 2 – User FPGA Bitfile:**

The User FPGA Bitfile can be cleared by using the RIO Device Setup Utility. To clear the User FPGA bitfile, complete the following steps:

1. Open NI MAX
2. Find and select your device
3. Click “Update Firmware” to load a blank, factory bitfile to the device or click “Erase Firmware” to clear whatever bitfile is currently on the device.

### **Procedure 3 – Device Attributes Flash:**

The Device Attributes Flash can be cleared by using the FlexRIO Host Library. To clear the device attributes flash, complete the following steps:

1. Find and open the FlexRIO\_HostInterface.llb LabVIEW library from “(LabVIEW Directory)\vi.lib\FlexRIO\FlexRIO\_HostInterface.llb”
2. Open FlexRIO\_Host\_CustomAttributeWrite.vi select FlexRIO device from “RIO Device” dropdown
3. Run VI with “Value” set to 0 to clear that Attribute index
4. Repeat Step 3 for entire Attribute space to clear entire attribute flash

## Terms and Definitions

### **Cycle Power:**

The process of completely removing power from the device and its components and allowing for adequate discharge. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

### **Volatile Memory:**

Requires power to maintain the stored information. When power is removed from this memory, its contents are lost. This type of memory typically contains application specific data such as capture waveforms.

### **Non-Volatile Memory:**

Power is not required to maintain the stored information. Device retains its contents when power is removed. This type of memory typically contains information necessary to boot, configure, or calibrate the product or may include device power up states.

### **User Accessible:**

The component is read and/or write addressable such that a user can store arbitrary information to the component from the host using a publicly distributed NI tool, such as a Driver API, the System Configuration API, or MAX.

### **System Accessible:**

The component is read and/or write addressable from the host without the need to physically alter the product.

### **Clearing:**

Per *NIST Special Publication 800-88 Revision 1*, “clearing” is a logical technique to sanitize data in all User Accessible storage locations for protection against simple non-invasive data recovery techniques using the same interface available to the user; typically applied through the standard read and write commands to the storage device.

### **Sanitization:**

Per *NIST Special Publication 800-88 Revision 1*, “sanitization” is a process to render access to “Target Data” on the media infeasible for a given level of effort. In this document, clearing is the degree of sanitization described.