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PXIe-8115

Manufacturer: National Instruments

Board Assembly Part Numbers (Refer to Procedure 1 for identification procedure):

Part Number and Revision	Description
152034A-012L or later	NI PXIE-8115, INTEL CORE I5 2.5GHZ CONTROLLER, LOCALIZED OS
152034A-722L or later	NI PXIE-8115, INTEL CORE I5 2.5GHZ CONTROLLER, WINDOWS XP, EXT TEMP
152034A-912L or later	NI PXIE-8115, INTEL CORE I5 2.5GHZ CONTROLLER, WINDOWS 7
152034A-001L or later	NI PXIE-8115, INTEL CORE I5 2.5GHZ CONTROLLER, NO HDD
152034A-922L or later	NI PXIE-8115, INTEL CORE I5 2.5GHZ CONTROLLER, WINDOWS 7, EXT TEMP
152034A-022L or later	NI PXIE-8115, INTEL CORE I5-2510E 2.5 GHZ REAL-TIME EMBEDDED SW, EXTTEMP
152034A-712L or later	NI PXIE-8115, INTEL CORE I5 2.5GHZ CONTROLLER, WINDOWS XP
153201A-022L or later	NI PXI-8115,INTEL CORE I5 2.5GHZ CONTROLLER, LOCALIZED OS, EXT TEMP
153201A-722L or later	NI PXI-8115,INTEL CORE I5 2.5GHZ CONTROLLER, WINDOWS XP EXT TEMP.
153201A-912L or later	NI PXI-8115,INTEL CORE I5 2.5GHZ CONTROLLER, WINDOWS 7
153201A-922L or later	NI PXI-8115,INTEL CORE I5 2.5GHZ CONTROLLER, WINDOWS 7, EXT TEMP.
153201A-012L or later	NI PXI-8115,INTEL CORE I5 2.5GHZ CONTROLLER, LOCALIZED OS
153201A-712L or later	NI PXI-8115,INTEL CORE I5 2.5GHZ CONTROLLER, WINDOWS XP

Volatile Memory

Target Data	Type	Size	Battery Backup	User1 Accessible	System Accessible	Sanitization Procedure
Processor Data	DDR3L SDRAM	2GB+	No	Yes	Yes	Cycle Power
PCH Real Time Clock (RTC)	CMOS RAM	256B	Yes	Yes	Yes	Procedure 2

Non-Volatile Memory (incl. Media Storage)

Target Data	Type	Size	Battery Backup	User Accessible	System Accessible	Sanitization Procedure
Power sequence & data logic	CPLD	600 Macrocells	No	No	Yes	None
BIOS configuration	Flash	32Mb	No	Yes	Yes	Procedure 3
Management Engine	Flash	32Mb	No	Yes	Yes	Procedure 3
GPIB Configuration	EEPROM	256KB	No	No	Yes	None
Secondary Ethernet Port Firmware	EEPROM	1Mb	No	No	Yes	None
Primary Storage	Magnetic Disk	80GB+	No	Yes	Yes	Procedure 4

¹ Refer to *Terms and Definitions* section for clarification of *User* and *System Accessible*

Procedures

Procedure 1 – Board Assembly Part Number identification:

To determine the Board Assembly Part Number and Revision, refer to the label applied to the surface of your product. The Assembly Part Number should be formatted as “P/N: #####a-##L”

Procedure 2 – PCH Real Time Clock (RTC), CMOS RAM:

To clear the battery-backed PCH Real Time Clock (RTC) CMOS RAM, complete the following steps:

1. First method:
 - A. Power off the chassis.
 - B. Remove the controller from the chassis.
 - C. Press the ‘Clear CMOS’ button (SW1) for 2 to 3 seconds as shown by the following figure.
 - D. Wait for 10 seconds.
 - E. Reinstall the controller in the chassis.

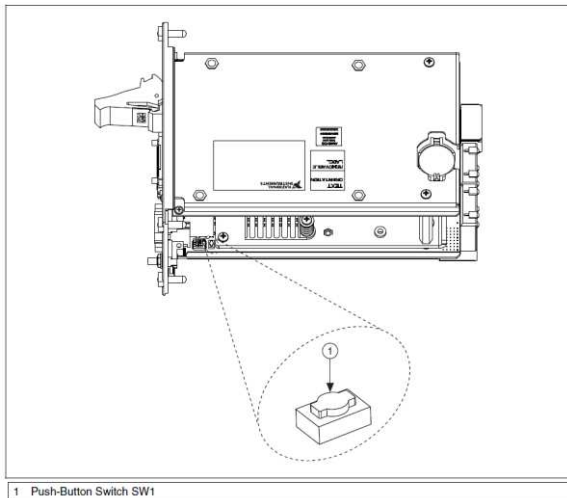


Figure: PXIe-8115

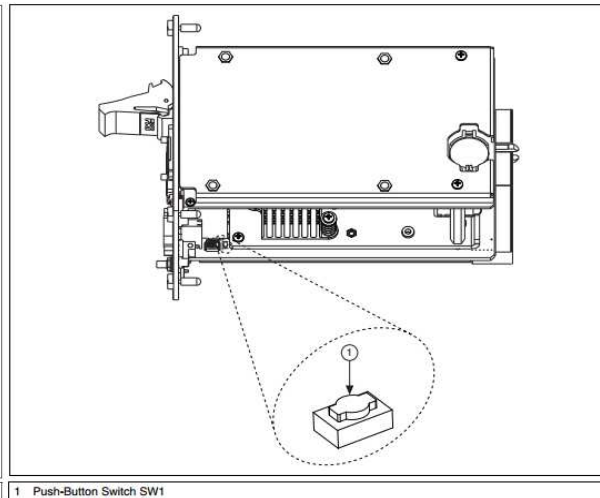


Figure: PXI-8115

2. Second method:

- A. Power off the chassis.
- B. Remove the controller from the chassis.
- C. Remove the coin-cell battery and wait for more than 10 seconds.
- D. Reinstall the battery.
- E. Reinstall the controller in the chassis and power up as usual.

Procedure 3 – BIOS Configuration Flash:

To clear the user-accessible information in the BIOS Flash, perform a factory reset within BIOS setup.

Procedure 4 – Primary Storage Magnetic Disk/ Solid State Drive (SSD):

There are several alternatives for sanitizing the Primary Storage Magnetic Disk’s contents. To sanitize the drive, perform one of the following steps:

1. Clear the disk using a commercially available utility for overwriting magnetic disk drives.
2. Remove the disk and apply sanitization procedures acceptable to your organization. You can also replace the disk with a removable one so that the stored data can be disassociated from the controller at any time.

Terms and Definitions

Cycle Power:

The process of completely removing power from the device and its components and allowing for adequate discharge. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

Volatile Memory:

Requires power to maintain the stored information. When power is removed from this memory, its contents are lost. This type of memory typically contains application specific data such as capture waveforms.

Non-Volatile Memory:

Power is not required to maintain the stored information. Device retains its contents when power is removed. This type of memory typically contains information necessary to boot, configure, or calibrate the product or may include device power up states.

User Accessible:

The component is read and/or write addressable such that a user can store arbitrary information to the component from the host using a publicly distributed NI tool, such as a Driver API, the System Configuration API, or MAX.

System Accessible:

The component is read and/or write addressable from the host without the need to physically alter the product.

Clearing:

Per NIST Special Publication 800-88 Revision 1, “clearing” is a logical technique to sanitize data in all User Accessible storage locations for protection against simple non-invasive data recovery techniques using the same interface available to the user; typically applied through the standard read and write commands to the storage device.

Sanitization:

Per NIST Special Publication 800-88 Revision 1, “sanitization” is a process to render access to “Target Data” on the media infeasible for a given level of effort. In this document, clearing is the degree of sanitization described.