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PXIe-8301

Manufacturer: National Instruments

Board Assembly Part Numbers (Refer to Procedure 1 for identification procedure):

Part Number and Revision	Description
140652D-01L or later	NI PXIe-8301

Volatile Memory

<i>Target Data</i>	<i>Type</i>	<i>Size</i>	<i>Battery Backup</i>	<i>User¹ Accessible</i>	<i>System Accessible</i>	<i>Sanitization Procedure</i>
Module and SMBus Control	FPGA	Altera EP4C3GX	No	No	Yes	Procedure 2
Thunderbolt™ Controller ²	On-chip	Intel DSL6540	No	No	Yes	Power Cycle

Non-Volatile Memory (*incl. Media Storage*)

<i>Target Data</i>	<i>Type</i>	<i>Size</i>	<i>Battery Backup</i>	<i>User Accessible</i>	<i>System Accessible</i>	<i>Sanitization Procedure</i>
Device configuration	SPI	8 Mb	No			
• Device information	Flash			No	Yes	None
• FPGA bitstream				No	Yes	None
Thunderbolt™ Configuration	SPI Flash	8 Mb	No	No	No	None
Spread Spectrum Clock (config)	On-chip	Silicon Labs Si5338M	No	No	No	None
Multi-Phase Buck Controller (config)	On-chip	International Rectifier IR3584	No	No	No	None

¹ Refer to *Terms and Definitions* section for clarification of *User* and *System Accessible*

² Thunderbolt™ and the Thunderbolt™ logo are trademarks of Intel Corporation in the U.S. and/or other countries.

Procedures

Procedure 1 – Board Assembly Part Number identification:

To determine the Board Assembly Part Number and Revision, refer to the label applied to the surface of your product. The Assembly Part Number should be formatted as “#####a-##L”.

Procedure 2 – Module and SMBus Control FPGA:

To clear the Module and SMBus Control FPGA, remove auxiliary power from the module using one of the following two methods:

1. Remove Mains power from the chassis
2. Eject the module from the chassis for a few seconds

Terms and Definitions

Cycle Power:

The process of completely removing power from the device and its components and allowing for adequate discharge. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

Volatile Memory:

Requires power to maintain the stored information. When power is removed from this memory, its contents are lost. This type of memory typically contains application specific data such as capture waveforms.

Non-Volatile Memory:

Power is not required to maintain the stored information. Device retains its contents when power is removed. This type of memory typically contains information necessary to boot, configure, or calibrate the product or may include device power up states.

User Accessible:

The component is read and/or write addressable such that a user can store arbitrary information to the component from the host using a publicly distributed NI tool, such as a Driver API, the System Configuration API, or MAX.

System Accessible:

The component is read and/or write addressable from the host without the need to physically alter the product.

Clearing:

Per *NIST Special Publication 800-88 Revision 1*, “clearing” is a logical technique to sanitize data in all User Accessible storage locations for protection against simple non-invasive data recovery techniques using the same interface available to the user; typically applied through the standard read and write commands to the storage device.

Sanitization:

Per *NIST Special Publication 800-88 Revision 1*, “sanitization” is a process to render access to “Target Data” on the media infeasible for a given level of effort. In this document, clearing is the degree of sanitization described.