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Manufacturer: Supermicro¹

Board Assembly Part Numbers (Refer to Procedure 1 for identification procedure):

Part Number and Revision	OEM P/N	Description
159328A-947	SYS-5019S-M2-01-NI031	RMC-8356

Volatile Memory

			Battery	User ²	System	Sanitization
Target Data	Туре	Size	Backup	Accessible	Accessible	Procedure
System Memory/RAM	DDR4	16GB	No	Yes	Yes	Cycle Power

Non-Volatile Memory (incl. Media Storage)

			Battery	User	System	Sanitization
Target Data	Туре	Size	Backup	Accessible	Accessible	Procedure
BIOS configuration	Flash	8 MB	Yes	Yes	Yes	Procedure 2
Primary Storage	Magnetic Disk	1 TB	No	Yes	Yes	Procedure 3
Ethernet Port Firmware	Flash	128kB	No	No	Yes	None

¹ Support for this product is provided by National Instruments

² Refer to *Terms and Definitions* section for clarification of *User* and *System Accessible*



Procedures

Procedure 1 – Board Assembly Part Number identification:

To determine the Board Assembly Part Number and Revision, refer to the print between heat sink and PCI express slot. The Revision is under the Part Number should be X11SSZ-F and Revision is right below Part Number

Procedure 2 – BIOS Configuration Clear:

To clear the BIOS configuration, complete the following steps:

- 1. Power down the system and unplug all power cords.
- 2. Remove the cover of the chassis to access the motherboard.
- 3. Remove the onboard battery.
- 4. Short the CMOS pads (JBT1) with a metal object such as a small screwdriver for at least four seconds.



- 5. Remove the screwdriver (or shorting device).
- 6. Replace the cover, reconnect the power cord(s) and power on the system.

Note: Clearing BIOS Configurations will also clear all user passwords stored in the BIOS Configurations. Note: Do not use the PW_ON connector to clear CMOS.

Procedure 3 – Primary Storage Magnetic Disk:

There are several alternatives for sanitizing the Primary Storage Magnetic Disk's contents. To sanitize the drive, perform one of the following steps:

- 1. Clear the disk using a commercially available utility approved by your organization for overwriting magnetic disk drives.
- 2. Remove the disk and apply sanitization procedures acceptable to your organization. You can also replace the disk with a removable one so that the stored data can be disassociated from the controller at any time.



Terms and Definitions

Cycle Power:

The process of completely removing power from the device and its components and allowing for adequate discharge. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

Volatile Memory:

Requires power to maintain the stored information. When power is removed from this memory, its contents are lost. This type of memory typically contains application specific data such as capture waveforms.

Non-Volatile Memory:

Power is not required to maintain the stored information. Device retains its contents when power is removed. This type of memory typically contains information necessary to boot, configure, or calibrate the product or may include device power up states.

User Accessible:

The component is read and/or write addressable such that a user can store arbitrary information to the component from the host using a publicly distributed NI tool, such as a Driver API, the System Configuration API, or MAX.

System Accessible:

The component is read and/or write addressable from the host without the need to physically alter the product.

Clearing:

Per *NIST Special Publication 800-88 Revision 1*, "clearing" is a logical technique to sanitize data in all User Accessible storage locations for protection against simple non-invasive data recovery techniques using the same interface available to the user; typically applied through the standard read and write commands to the storage device.

Sanitization:

Per *NIST Special Publication 800-88 Revision 1*, "sanitization" is a process to render access to "Target Data" on the media infeasible for a given level of effort. In this document, clearing is the degree of sanitization described.