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cRIO-9081

Board Model Name **Board Part Number Range**
908x Series cRIO 151712A-01L, 151712A-02L

Manufacturer: National Instruments

Volatile Memory

Type¹	Size	User Accessible/ System Accessible²	Battery Backup?	Purpose	Method of Clearing³
(9081/9082) DDR3-SDRAM	2 GB	Yes/Yes	No	RAM	Cycle Power
(9081) FPGA RAM	XC6SLX75	Yes/Yes	No	FPGA	Cycle Power
(9082) FPGA RAM	XC6SLX150	Yes/Yes	No	FPGA	Cycle Power

Non-Volatile Memory

Type	Size	User Accessible/ System Accessible	Battery Backup?	Purpose	Method of Clearing
(9081) Riser Card	16 GB	Yes/Yes	No	Primary Storage	Cleared by Reformatting
(9082) Riser Card	32 GB	Yes/Yes	No	Primary Storage	Cleared by Reformatting
(9081/9082) Firmware	32 MBIT	No/No	No	Maintains BIOS Settings	None Available to User
Boot Flash	32 MBIT	No/No	No	Contains Boot Code	None Available to User
FPGA Config	64 MB	Yes/Yes	No	FPGA Startup	Clear Startup
SPD Flash	2 MB	No/No	No	Memory Config Info	None Available to User
Ethernet Flash	1 MB	No/No	No	Ethernet Control Firmware	None Available to User

Media Storage

Type	Size	User Accessible/ System Accessible	Battery Backup?	Purpose	Method of Clearing
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NONE

¹ Calibration constants that are stored in device EEPROMs include information for the device’s full operating range and does not maintain any unique data for specific frequencies at which the device is used.

² Items above that are noted as **No** for User Accessible/System Accessible are for the following reason(s): Hardware changes or a unique software tool from National Instruments are required to modify contents of the memory listed. This software tool is not distributed to public users for any personal access or customization; also known as non-normal use.

³ The designation *None Available to User* indicates that the ability to clear this memory is not available to the user under normal operation. The utilities required to perform this action are not distributed by National Instruments to customers for normal use.

Terms and Definitions

User Accessible The user can directly write or modify the contents of the memory during normal instrument operation.

System Accessible Any data that can access, change or modify the memory. This could be something that is not deliberate by the user and could be a background driver implementation, such as storing application information in RAM to increase speed of use.

Cycle Power This defined the process of completely removing power from the device and its components. This includes a complete shutdown of the PC or Chassis containing the device; a reboot is not sufficient for the completion of this process.

Volatile Memory Volatile memory requires power to maintain the stored information. When power is removed from this memory its contents are lost.

Non-Volatile Non-volatile memory will retain its contents when power is removed. This type of memory typically contains calibration or chip configuration information, such as power up states.